

TN5050H-12WY

Datasheet - production data

50 A 1200 V automotive grade SCR

TO-247 un-insulated

Features

- On-state current: 50 A rms
- Blocking voltage: 1200 V
- High static and dynamic commutation:
 - dI/dt = 200 A/µs
 - $dV/dt = 1000 V/\mu s$
- AEC-Q101
- I_{GT} = 50 mA
- ECOPACK[®]2 compliant component

Applications

- Automotive:
 - on board, off board battery charger
- Solar, wind renewable energy inverters
- Solid state relay
- UPS:
 - Bypass
 - ICL (inrush current limiter)
 - Battery charger
- Industrial welding systems
- Voltage control rectifier

January 2015

DocID026846 Rev 1

This is information on a product in full production.

Description

Available in TO-247 high power package, the TN5050H-12WY autograde is suitable in applications such as automotive / stationary battery charger, renewable energy generator, interruptible power supply, solid state relay, welding equipment and motor drive applications. Its power switching, voltage robustness and power dissipation performances are the key features for functions such as a 80 A AC switch, an AC phasing inverter and an AC-DC controlled rectifier bridge.

The TN5050H-12WY is an automotive grade product and offers a superior performance in surge current handling, thermal cooling capabilities and overvoltage robustness.

Table 1. Device summary

Symbol	Value	Unit
I _{T(RMS)}	50	A
V _{DRM} , V _{RRM}	1200	V
V _{DSM} , V _{RSM}	1300	V
I _{GT}	50	mA
Tj	150	°C

1 Characteristics

Table 2. Absolute ratings (limiting values	s. T ₁ = 25 °C unless otherwise stated)

Symbol	Parameter	Value	Unit		
V _{DRM/} V _{RMM}	Repetitive off-state voltage (50-60 Hz) $T_j = 150^{\circ}$			1200	V
I _{T(RMS)}	On-state rms current (180° conduction angle)			50	А
I _{T(AV)}	Average on-state current (180° conduction angle)		T _c = 137 °C	32	A
I _{T(RMS)}	On-state rms current (180° conduction angle)			80	^
I _{T(AV)}	Average on-state current (180° conduction angle) $T_c = 12$			51	A
I _{TSM} ⁽¹⁾	Non repetitive surge peak on-state current (T_i initial = 25 °C)		t _p = 8.3 ms	633	А
'TSM` '			t _p = 10 ms	580	A
dl/dt	$ \begin{array}{ c c c } Critical rate of rise of on-state current \\ I_G = 2 \ x \ I_{GT}, \ t_r \leq 100 \ ns \end{array} \end{array} F = 50 \ Hz $		T _j = 150 °C	200	A/µs
I _{GM}	Forward peak gate current T _j = 150 °C		t _p = 20 μs	8	А
P _{G(AV)}	Average gate power dissipation $T_j = 150 \text{ °C}$			1	W
T _{stg}	Storage junction temperature range			- 40 to + 150	°C
Тj	Operating junction temperature range			- 40 to + 150	°C

1. ST recommends I²t value for fusing = 1680 A²s for T_j = 25 °C and T_p = 10 ms.



Symbol	Test conditions				Unit
	V 12V D 22 0		Min.	10	
I _{GT}	$V_{\rm D} = 12 \text{ V}, \text{ R}_{\rm L} = 33 \Omega$			50	mA
V _{GT}	$V_{\rm D} = 12 \text{ V}, \text{ R}_{\rm L} = 33 \Omega$		Max.	1	V
V _{GD}	$V_D = 2/3 X V_{DRM}, R_L = 3.3 k \Omega$ $T_j = 150 °C$		Min.	0.15	V
Ι _Η	I _T = 500 mA, gate open Ma				mA
١L	$I_{G} = 1.2 \times I_{GT}$ Ma			125	mA
t _{gt}	$I_{T} = 50 \text{ A}, V_{D} = V_{DRM}, I_{G} = 200 \text{ mA}, dI_{G}/dt = 0.2 \text{ A}/\mu\text{s}$		Тур	3	μs
dV/dt	$V_D = 2/3 \times V_{DRM}$, gate open $T_j = 150 \text{ °C}$		Min.	1000	V/µs
t _q	$ \begin{array}{l} I_{T} = 33 \; A, \; V_{D} = 800 \; V, \; V_{R} = 75 \; V, \; t_{P} = 100 \; \mu s, \; dI_{T} / dt = 10 \; A / \mu s, \\ dV_{D} / dt = 20 \; V / \mu s \end{array} \begin{array}{l} T_{j} = 150 \; ^{\circ} C \end{array} $		Тур	150	μs
V _{TM}	I _{TM} = 100 A, t _P = 380 μs		Max.	1.55	V
V _{TO}	Threshold voltage $T_j = 150 \text{ °C}$		Max.	0.88	V
R _D	Dynamic resistance $T_j = 150 \text{ °C}$		Max.	6	mΩ
		T _j = 25 °C	Max.	5	μA
I _{DRM} /I _{RRM}	$V_{D} = V_{DRM}, V_{R} = V_{RRM}$	T _j = 125 °C	Max.	3	mA
		T _j = 150 °C	Max.	7.5	mA
I _{DSM} /I _{RSM}	$V_{\rm D} = V_{\rm DSM}, V_{\rm R} = V_{\rm RSM}$	T _j = 25 °C	Max.	10	μA

Table 3. Electrical characteristics (T_i = 25 °C, unless otherwise specified)

Table 4. Thermal resistance

Symbol	Parameter		Value	Unit
R _{th(j-c)}	Junction to case (DC)	TO-247	0.3	°C/W
R _{th(j-a)}	Junction to ambient	10-247	50	°C/W

Figure 1. Maximum average power dissipation versus average on-state current









Figure 3. Average and D.C. on-state current versus case temperature



Figure 5. Relative variation of thermal impedance junction to case and junction to ambient versus pulse duration



Figure 7. Relative variation of holding and latching current versus junction temperature (typical values)

Figure 4. Average and D.C. on-state current versus ambient temperature



Figure 6. Relative variation of gate trigger current and gate voltage versus junction temperature (typical values)



Figure 8. Surge peak on-state current versus number of cycles





Figure 10. On-state characteristics

Figure 9. Non repetitive surge peak on-state current for a sinusoidal pulse (t_p < 10 ms)



Figure 11. Relative variation of leakage current versus junction temperature for different values of blocking voltage (typical values)





2 Package information

- Epoxy meets UL94, V0
- Lead-free package

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.



Figure 12. TO-247 dimension definitions



	Dimensions						
Ref.		Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур	Max.	
А	4.85		5.15	0.191		0.203	
A1	2.20		2.60	0.086		0.102	
b	1.00		1.40	0.039		0.055	
b1	2.00		2.40	0.078		0.094	
b2	3.00		3.40	0.118		0.133	
С	0.40		0.80	0.015		0.031	
D ⁽¹⁾	19.85		20.15	0.781		0.793	
Е	15.45		15.75	0.608		0.620	
е	5.30	5.45	5.60	0.209	0.215	0.220	
L	14.20		14.80	0.559		0.582	
L1	3.70		4.30	0.145		0.169	
L2		18.50 typ.			0.728 typ.		
ØP ⁽²⁾	3.55		3.65	0.139		0.143	
ØR	4.50		5.50	0.177		0.217	
S	5.30	5.50	5.70	0.209	0.216	0.224	

Table 5. TO-247 dimension values

1. Dimension D plus gate protrusion does not exceed 20.5 mm

2. Resin thickness around the mounting hole is not less than 0.9 mm



3 Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
TN5050H-12WY	TN5050H12Y	TO-247	4.43 g	30	Tube

4 Revision history

Table 7. Document revision history

Date	Revision	Changes
07-Jan-2015	1	Initial release.



IMPORTANT NOTICE - PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2015 STMicroelectronics - All rights reserved



DocID026846 Rev 1