TOSHIBA Photocoupler GaAs Ired & Photo-Transistor

# TLP280,TLP280-4

Programmable Controllers
AC/DC-Input Module
PC Card Modem (PCMCIA)

TLP280 and TLP280-4 is a very small and thin coupler, suitable for surface mount assembly in applications such as PCMCIA fax modem, programmable controllers.

TLP280 and TLP280-4 consist of photo transistor, optically coupled to two gallium arsenide infrared emitting diode connected inverse parallel, and can operate directly by AC input current

- Collector-emitter voltage: 80 V (min)
- Current transfer ratio: 50% (min)
  Rank GB: 100% (min)
- Isolation voltage: 2500 Vrms (min)
- UL recognized: UL1577, file No. E67349
- BSI approved: BS EN 60065: 2002,

BS EN 60950-1: 2002 Certificate No. 8143, 8144 Unit in mm

TLP280

1 2

2.6 ± 0.25

7.0 ± 0.4

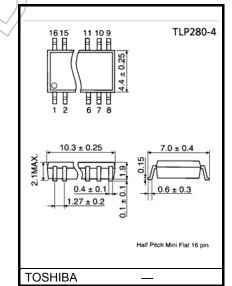
1.27 ± 0.2

Half Pitch Mini Flat 4 pin

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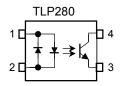
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Weight: 0.05 g (typ.)

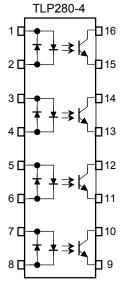


Weight: 0.19 g (typ.)

### Pin Configuration (top view)



- 1 : Anode Cathode
- 2 : Cathode Anode
- 3 : Emitter
- 4 : Collector



1,3,5,7 : Anode-

Cathode 2,4,6,8 : Cathode

Anode 9,11,13,15 : Emitter

10,12,14,16 : Collector

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rai	Unit	
	Characteristic	Syllibol	TLP280	TLP280-4	Offic
	Forward current	I <sub>F(RMS)</sub>	±	50	mA
TED	Forward current derating	ΔI <sub>F</sub> /°C	–0.7 (Ta ≥ 53°C)	–0.5 (Ta ≥ 25°C)	mA /°C
	Pulse forward current	I <sub>FP</sub>	±1 (100µs pulse, 100pps)		A
	Junction temperature	Tj	125		°C
	Collector-emitter voltage	V <sub>CEO</sub>	80		V
	Emitter-collector voltage	V <sub>ECO</sub>	7		7 N
'n	Collector current	Ic	50		(mA)
Detector	Collector power dissipation (1 circuit)	P <sub>C</sub>	150	100	mW
	Collector power dissipation derating (Ta ≥ 25°C) (1 circuit)	ΔP <sub>C</sub> /°C	-1.5		mW /°C
	Junction temperature	Tj	125		°C
Sto	rage temperature range	T <sub>stg</sub>	-55~125		~ °C ((
Operating temperature range		T <sub>opr</sub>	-55~100		~ %C
Lead soldering temperature		T <sub>sol</sub>	260 (10s)		°C
Total package power dissipation (1 circuit)		PT	200	170	(mW)
Total package power dissipation derating (Ta ≥ 25°C) (1 circuit)		ΔP <sub>T</sub> /°C	-2:0	-1.7	mW /°C
Isol	ation voltage (Note)	BVS	2500 (AC, 1mi	n., R.H.≤ 60%)	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note): Device considered a two terminal device: LED side pins shorted together and detector side pins shorted together.



### Individual Electrical Characteristics (Ta = 25°C)

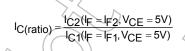
	Characteristic	Symbol	Test Condition	Min	Тур	Max	Unit
LED	Forward voltage	V <sub>F</sub>	I <sub>F</sub> = ±10 mA	1.0	1.15	1.3	V
ä	Capacitance	C <sub>T</sub>	V = 0, f = 1 MHz	_	60		pF
	Collector–emitter breakdown voltage	V <sub>(BR)</sub> CEO	I <sub>C</sub> = 0.5 mA	80	_		٧
Detector	Emitter-collector breakdown voltage	V <sub>(BR)</sub> ECO	I <sub>E</sub> = 0.1 mA	(7	)>	_	V
	Collector dark current (Note 1)	ICEO	V <sub>CE</sub> = 48 V, Ambient light below (100 1x)		0.01 (2)	0.1 (10)	μΑ
			V <sub>CE</sub> = 48 V, Ta = 85°C Ambient light below (100 1x)		2 (4)	50 (50)	μΑ
	Capacitance (collector to emitter)	C <sub>CE</sub>	V = 0, f = 1 MHz	_	10	_	pF

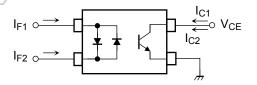
(Note 1): Because of the construction, leak current might be increased by ambient light. Please use photocoupler with less ambient light.

## Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Current transfer ratio	I <sub>C</sub> / I <sub>F</sub>	$I_F = \pm 5$ mA, $V_{CE} = 5$ V	50	_	600	%
Current transfer fatto	10714	Rank GB	100	1	600	
Saturated CTR	I <sub>C</sub> / I <sub>F (sat)</sub>	IF = ±1 mA, V <sub>CE</sub> = 0.4 V Rank GB		60	_	%
Saturated CTIX	IC / IF (sat)		30	1	1	/0
	V <sub>CE</sub> (sat)	$I_C = 2.4 \text{ mA}, I_F = \pm 8 \text{ mA}$	1	1	0.4	
Collector-emitter saturation voltage		$I_C = 0.2 \text{ mA}, I_F = \pm 1 \text{ mA}$ Rank GB	l	0.2	1	V
			1	1	0.4	
Off–state collector current	I <sub>C(off)</sub>	$V_F = \pm 0.7 \text{ V}, V_{CE} = 48 \text{ V}$		_	10	μA
CTR symmetry	I <sub>C (ratio)</sub>	$I_{C}$ ( $I_{F} = -5 \text{ mA}$ ) / $I_{C}$ ( $I_{F} = 5 \text{ mA}$ ) (Note 2)	0.33	1	3	_

(Note 2):





V<sub>CC</sub> 4.5V

0.5V

## Isolation Characteristics (Ta = 25°C)

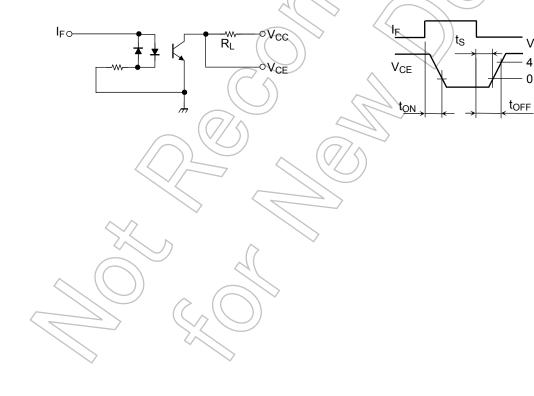
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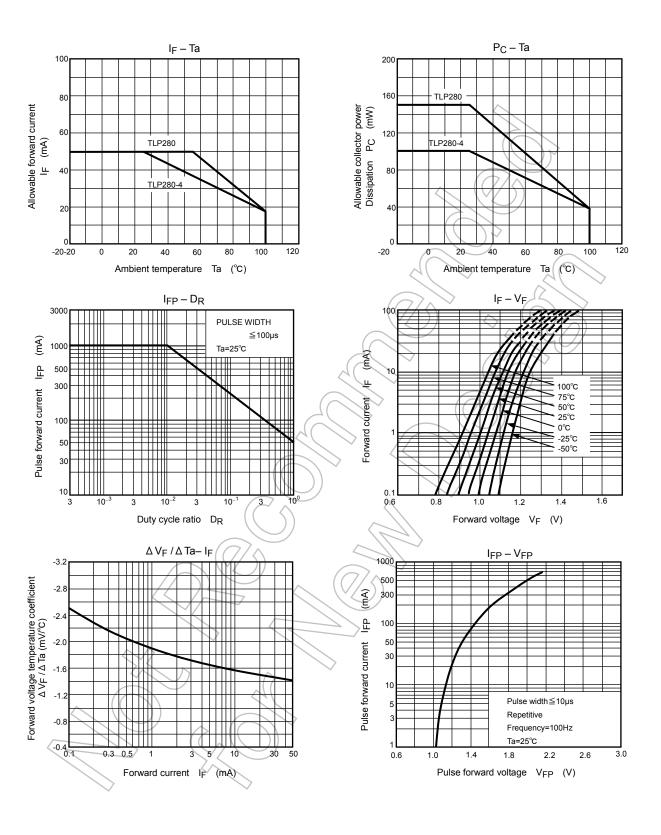
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	Cs	V <sub>S</sub> = 0V, f = 1 MHz	_	8.0	_	pF
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H.≤ 60%	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
	BVS	AC, 1 minute	2500	_	_	- V <sub>rms</sub>
Isolation voltage		AC, 1 second, in oil	(-)	5000	_	
		DC, 1 minute, in oil	1	5000	_	V <sub>dc</sub>

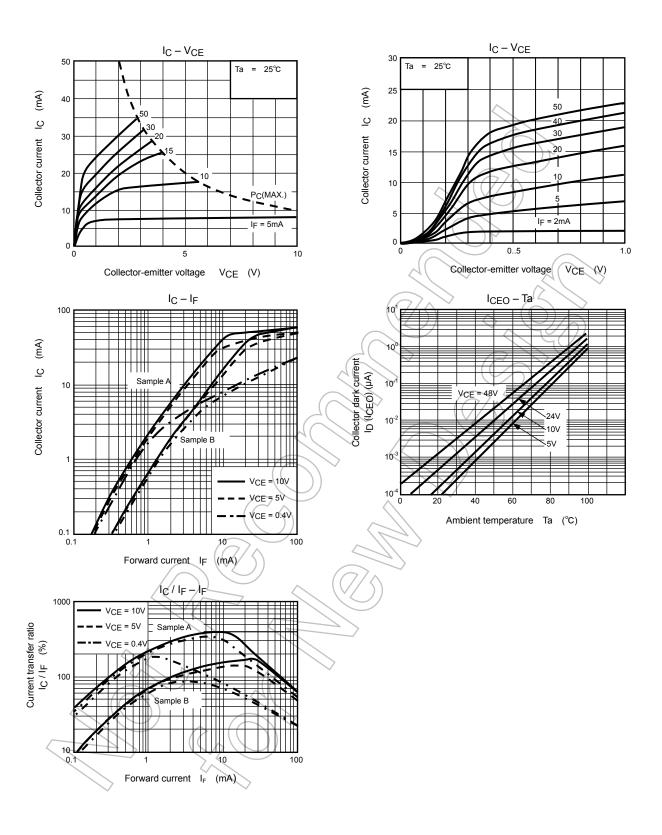
### **Switching Characteristics (Ta = 25°C)**

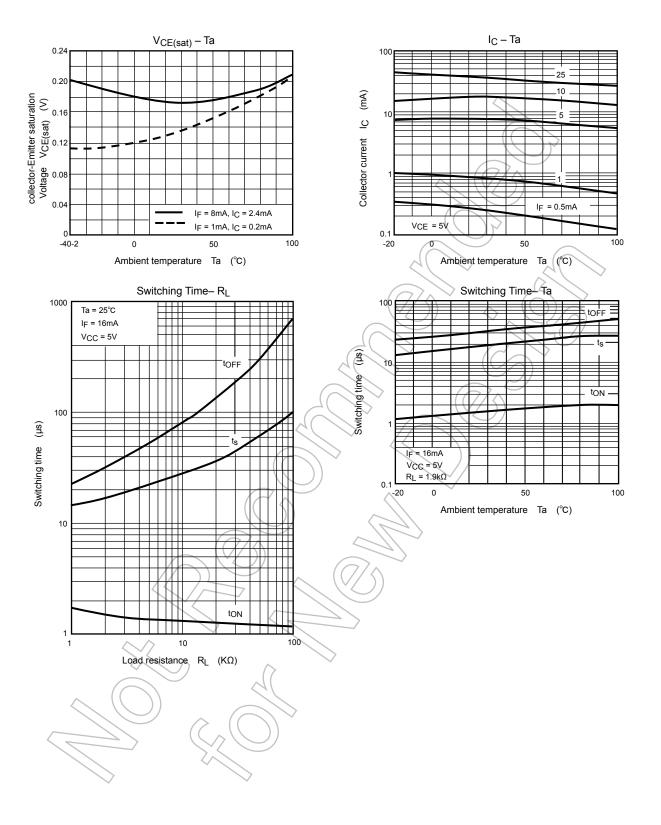
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	t <sub>r</sub>		_	2	_	
Fall time	t <sub>f</sub>	$V_{CC} = 10 \text{ V}, I_{C} = 2 \text{ mA}$ $R_{L} = 100\Omega$		<b>3</b>	$\searrow$	μs
Turn-on time	t <sub>on</sub>	R <sub>L</sub> = 100Ω	-6	3	> —	
Turn-off time	t <sub>off</sub>		~_(	3	) —	
Turn-on time	ton		1	2/	_	
Storage time	ts	$R_L = 1.9 \text{ k}\Omega$ (Fig.1) $V_{CC} = 5 \text{ V, I}_F = \pm 16 \text{ mA}$		25	_	μs
Turn-off time	toff			40	_	

(Fig. 1): Switching time test circuit









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