TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TD62107P,TD62107BP,TD62107F

4CH HIGH-CURRENT DARLINGTON SINK DRIVER

The TD62107P / BP / F are high–voltage, high–current darlington drivers and enable inputs which can gate the outputs. All units feature integral clamp diodes for switching inductive loads. The TD62107P / BP / F have a wide supply voltage range and all

The TD62107P / BP / F have a wide supply voltage range and all input are compatible with TTL and 5–V CMOS.

Application include relay, hammer, lamp and stepping moter drivers.

Please observe the thermal condition for using.

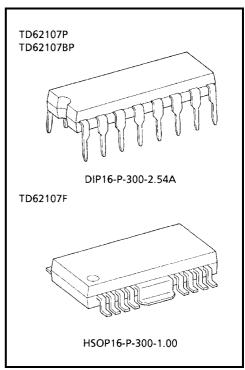
FEATURES

- Output current (single output) 750mA (MAX)
- High sustaining voltage output
 80 V MIN. (TD62107BP)

45 V MIN. (TD62107P) 35 V MIN. (TD62107F)

- Output clamp diodes
- Enable inputs E1, E2
- Wide supply voltage range $V_{CC} = 4.75 \sim 17 \text{ V}$
- Input compatible with TTL and 5-V CMOS
- GND terminal = heat sink

Package type-P, BP : DIP-16pinPackage type-F : HSOP-16pin

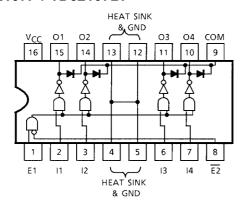


Weight

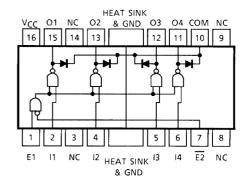
DIP16-P-300-2.54A : 1.11 g (Typ.) HSOP16-P-300-1.00 : 0.50 g (Typ.)

PIN CONNECTION (TOP VIEW)

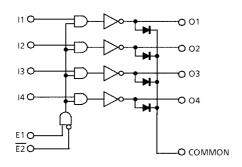
TD62107P / TD62107BP



TD62107F



SCHEMATICS (EACH DRIVER)

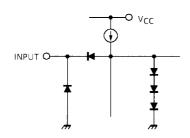


TRUTH TABLE

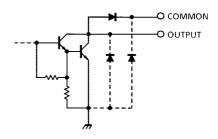
E1	E2	I1 ~ I4	O1~O4
L	L	L or H	Disable OFF
L	Н	L or H	Disable OFF
Н	L	L or H	Enable In
Н	Н	L or H	Disable OFF

In = I1 ~ I4

INPUT EQUIVALENT CIRCUIT



OUTPUT EQUIVALENT CIRCUIT



Note: The input and output parasitic diodes cannot be used as clamp diodes.

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT		
Supply Voltage	V _{CC}	-0.5~17	V		
	Р		-0.5~45		
Output Sustaining Voltage	BP	V _{CE} (SUS)	-0.5~80	V	
	F		-0.5~35		
Output Current	lout	750	mA		
Input Voltage		V _{IN}	-0.5~V _{CC} + 0.5	٧	
	Р		45		
Clamp Diode Reverse Voltage	BP	V _R	80	V	
	F		35		
Clamp Diodo Forward Current	P, F	1_	500	mA	
Clamp Diode Forword Current	BP	l _F	750		
Power Discination	P, BP	D-	2.7 (Note 1)	W	
Power Dissipation	F	P_{D}	1.4 (Note 2)		
Operating Temperature		T _{opr}	-40~85	°C	
Storage Temperature	T _{stg}	-55~150	°C		

2

Note 1: On Glass Epoxy PCB (50 × 50 × 1.6 mm Cu 50%) Note 2: On Glass Epoxy PCB (60 × 30 × 1.6 mm Cu 30%)

RECOMMENDED OPERATING CONDITIONS (Ta = $-40 \sim 85$ °C)

CHARACTERISTIC		SYMBOL	CONDITION		MIN	TYP.	MAX	UNIT	
Supply Voltage		V _{CC}			4.75	_	15	V	
Output Sustaining Voltage	Р				0	_	45		
	BP	V _{CE (SUS)}			0	_	80	V	
	F			0	_	35			
Output Current	P, F	Гоит	T _{pw} = 25ms, Duty = 75%	6, 1 Circuit	0	_	500		
	BP		T _{pw} = 25ms, Duty = 10%, 4 Circuits		0	_	750		
	P, BP		T _{pw} = 25ms, 4 Circuits	Duty = 30%	0	_	400	mA	
	F			Duty = 40%	_	_	300		
Input Voltage		V _{IN}			0	_	V _{CC}	V	
Clamp Diode ReverseVoltage	Р	V _R			_	_	45		
	BP				_	_	80	V	
	F				_	_	35		
Clamp Diode Forward Current	P, F	IF			_	_	500	mA	
	BP				_	_	750	IIIA	
Power Dissipation	B, BP	P _D			_	_	1.0	W	
	F		Ta = 85°C	(Note) — — 0.7		0.7	7 **		

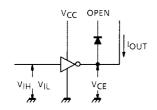
Note: On Glass Epoxy PCB (60 × 30 × 1.6 mm Cu 30%)

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

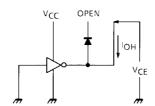
CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Input	"H" Level	"H" Level		1		2.0	_	V _C C	V
Voltage	"L" Level		V _{IL}			_	_	0.8	V
Output Current	"H" Level	Р	Іон	2	V _{CE} = 45 V, Ta = 75°C	_	_	100	μΑ
		BP			V _{CE} = 80 V, Ta = 85°C	_	_	100	
		F			V _{CE} = 35 V, Ta = 85°C	_	_	V _{CC} 0.8 100	
Output	"L" Level	P, F	V _a ,	3	I _{OUT} = 50 mA	_	_	1.3	V
Voltage	L Level	BP	V _{OL}	3	I _{OUT} = 750 mA	_	_	1.6	V
Input	"H" Level		liH	4	V _{IN} = 13 V	_	_	100	μA
Current	"L" Level		I _{IL}	5	$V_{IN} = 0.4 \text{ V}$	_	_	-0.3	mA
		Р	I _R		V _R = 45 V	_	_	100	μА
Clamp Dioc Current	de Reverse	BP		6	V _R = 80 V	_	_	100	
		F			V _R = 35 V	_	_	100	
Clamp Diode Forward		P, F	\/-	7	I _F = 500 mA	_	_	2.0	V
Voltage		BP	V _F	'	I _F = 750 mA	_	_	2.0	v
Clamp Diode F Voltage	Output "H"	- ICC	Іссн	4	V _{CC} = 13 V, V _{IN} = 0 V, OUTPUT OPEN	_	_	13	mA
	Output "L"		I _{CCL}	5	V _{CC} = 13 V, V _{IN} = 5 V, OUTPUT OPEN	_	_	17	IIIA
	Р			8	V _{CC} = 5 V, R _L = 90 Ω C _L = 15 pF, V _{OUT} = 45 V	_	5	_	µs
Turn-On Delay		BP	t _{ON}		V_{CC} = 5 V, V_{OUT} = 80 V R_L = 160 Ω	_	0.4	_	
		F	•		V _{CC} = 5 V, R _L = 70 Ω C _L = 15 pF, V _{OUT} = 35 V	_	5	_	
Turn-Off Delay BP				V_{CC} = 5 V, R_L = 90 Ω C_L = 15 pF, V_{OUT} = 45 V	_	5	_		
		BP		8	V _{CC} = 5 V, V _{OUT} = 80 V R _L = 160 Ω	_	1.7	_	μs
		F			V _{CC} = 5 V, R _L = 70 Ω C _L = 15 pF, V _{OUT} = 35 V	_	5	_	

TEST CIRCUIT

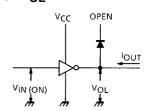
1. VIH, VIL



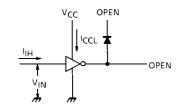
2. I_{OH}



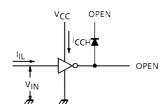
3. Vol



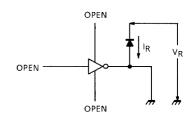
4. I_{IH}, I_{CCL}



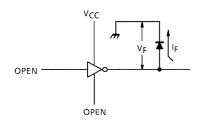
5. I_{IL}, I_{CCH}



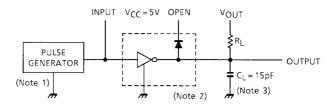
6. I_R



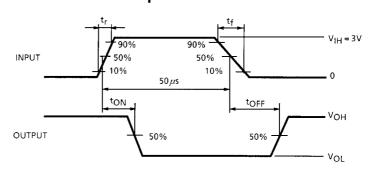
7. V_F



8. ton, toff



Input condition



Note 1: Pulse Width 50 µs, Duty Cycle 10%

Output Impedance 50 Ω , $t_f \le 5$ ns, $t_f \le 10$ ns

Note 2: $V_{IH} = 3 V$, $E1 = V_{IH}$, $\overline{E2} = GND$,

 $V_{CC} = 5 V$

Note 3: C_L includes probe and jig capacitance

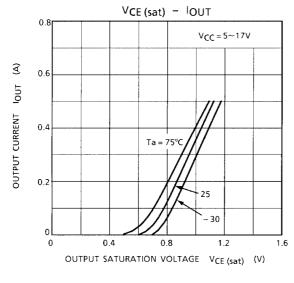
PRECAUTIONS for USING

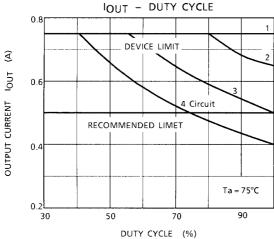
This IC does not include built-in protection circuits for excess current or overvoltage.

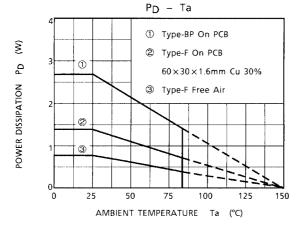
If this IC is subjected to excess current or overvoltage, it may be destroyed.

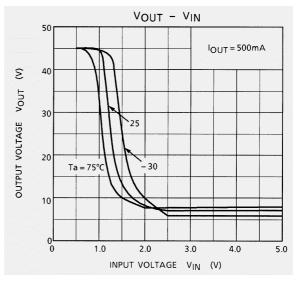
Hence, the utmost care must be taken when systems which incorporate this IC are designed.

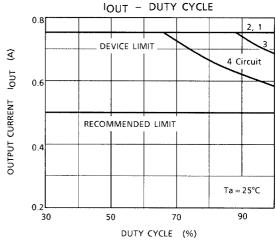
Utmost care is necessary in the design of the output line, V_{CC}, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

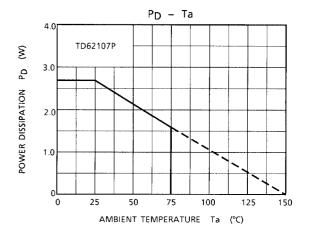




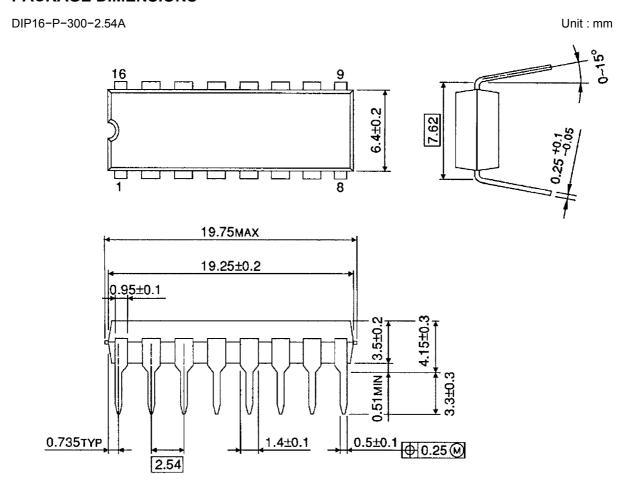








PACKAGE DIMENSIONS

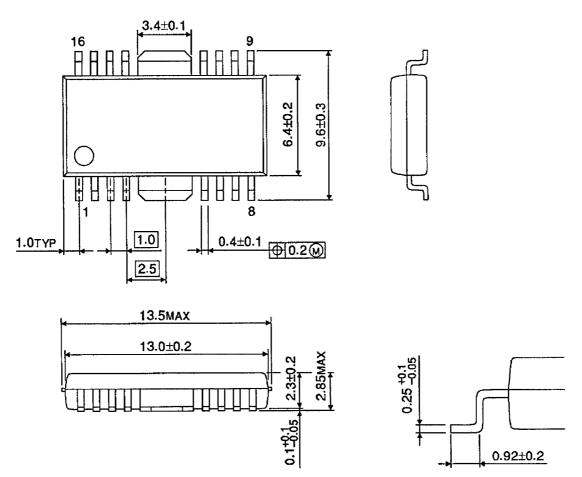


7

Weight: 1.11 g (Typ.)

PACKAGE DIMENSIONS

HSOP16-P-300-1.00 Unit: mm



Weight: 0.50 g (Typ.)

RESTRICTIONS ON PRODUCT USE

000707EBA

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