TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WZ32FU,TC7WZ32FK

2 Input or Gate

Features

- High output drive: ±24 mA (min) @V_{CC} = 3 V
- Super high speed operation: $t_{pd} 2.4 \text{ ns} (typ.) @V_{CC} = 5 \text{ V}, 50 \text{ pF}$
- Operation voltage range: V_{CC} (opr) = 1.65~5.5 V
- Latch-up performance: ±500 mA or more
- ESD performance: ±200 V or more (JEITA)
 - ± 2000 V or more (MIL)
- Power down protection is provided on all inputs and outputs.
- Matches the performance of TC74LCX series when operated at 3.3 V VCC.

Marking



Maximum Ratings (Ta = 25°C)

TC7WZ32FU	~
	\rightarrow
d A	TOTAT
	LHHH
Ч	COB .
	8-P-0.65
TC7WZ32FK	
	TITAL
	RACION
SSOP8	-P-0.50A
Weight	

SSOP8-P-0.65 SSOP8-P-0.50A

: 0.02 g (typ.) : 0.01 g (typ.)

Pin Assignment (top view)



Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	-0.5~6	V	
DC input voltage	V _{IN}	-0.5~6	V	
DC output voltage	V _{OUT}	-0.5~6	V	
Input diode current	I _{IK}	-20	mA	
Output diode current	I _{OK}	-20	mA	
DC output current	IOUT	±50	mA	
DC V _{CC} /ground current	ICC	±50	mA	
Power dissipation	PD	300 (SM8) 200 (US8)	mW	
Storage temperature	T _{stg}	-65~150	°C	
Lead temperature (10s)	ΤL	260	°C	

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Truth Table

Logic Diagram





Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit	
Supply voltage	Vcc	1.65~5.5	V	
Supply voltage	VCC	1.5~5.5 (Note 1)		
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~5.5 (Note 2)	v	
	V001	0~V _{CC} (Note 3)		
Operating temperature	T _{opr}	-40~85	°C	
		0~20 (V _{CC} = 1.8 V \pm 0.15 V, 2.5 V \pm 0.2 V)	ns/V	
Input rise and fall time	d _t /d _v	0~10 (V_{CC} = 3.3 V \pm 0.3 V)		
		0~5 (V _{CC} = 5.5 V \pm 0.5 V)		

Note 1: Data retention only

Note 2: $V_{CC} = 0 V$

Note 3: High or low state

Electrical Characteristics

DC Characteristics

Characteristics Symbol		Symbol	Symbol Test Condition			Ta = 25°C			Ta = -40~85°C		Unit
				V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit	
High level		Mar			1.65~ 1.95	$\begin{array}{c} 0.75 \\ \times \ V_{CC} \end{array}$	_	_	$\begin{array}{c} 0.75 \\ \times \ V_{CC} \end{array}$	—	V
Input voltage	Figitievei	gh level V _{IH}		—		$0.7 \\ \times V_{CC}$	_	_	$0.7 \\ \times V_{CC}$	_	
			_		1.65~ 1.95	_		$\begin{array}{c} 0.25 \\ \times \ V_{CC} \end{array}$	_	$0.25 \\ \times V_{CC}$	V
	Low level	VIL			2.3~5.5	_		$0.3 \\ \times V_{CC}$	_	$0.3 \\ \times \ V_{CC}$	
					1.65	1.55	1.65	_	1.55	—	
		V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = −100 μA	2.3	2.2	2.3		2.2	—	
				10H = -100 μΑ	3.0	2.9	3.0		2.9	—	
					4.5	4.4	4.5		4.4	—	
	High level			$I_{OH} = -4 \text{ mA}$	1.65	1.29	1.52		1.29	—	
				I _{OH} = -8 mA	2.3	1.9	2.15		1.9	—	
				I _{OH} = -16 mA	3.0	2.4	2.8	_	2.4	—	
				$I_{OH} = -24 \text{ mA}$	3.0	2.3	2.68		2.3	—	
Output				I _{OH} = -32 mA	4.5	3.8	4.2	_	3.8	_	
voltage	Low level	V _{OL}	V _{IN} = V _{IL}	I _{OL} = 100 μA	1.8		0	0.1		0.1	v
					2.3		0	0.1		0.1	
					3.0	—	0	0.1		0.1	
					4.5		0	0.1	_	0.1	
				$I_{OL} = 4 \text{ mA}$	1.65	—	0.08	0.24	_	0.24	
				I _{OL} = 8 mA	2.3		0.1	0.3		0.3	
				I _{OL} = 16 mA	3.0		0.15	0.4		0.4	
				I _{OL} = 24 mA	3.0	—	0.22	0.55		0.55	
				I _{OL} = 32 mA	4.5		0.22	0.55	_	0.55	
	Input leakage current I_{IN} $V_{IN} = 5.5$ V or GND		0~5.5		_	±1		±10	μA		
Power off lea	kage current	IOFF	$V_{\text{IN}} \text{ or } V_{\text{OL}}$		0.0			1		10	μA
Quiescent supply current		ICC	$V_{IN} = 5.5 \text{ V or GND}$		1.65~5.5	—	—	1	_	10	μA

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40~85°C		Unit	
Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	^t pLH t _{pHL}	C_L = 15 pF, R_L = 1 M Ω	1.8 ± 0.15	2.0	5.8	10.5	2.0	11.0	ns
			$\textbf{2.5}\pm\textbf{0.2}$	1.0	3.5	5.8	1.0	6.2	
			$\textbf{3.3}\pm\textbf{0.3}$	0.8	2.6	3.9	0.8	4.3	
			5.0 ± 0.5	0.5	2.6	3.1	0.5	3.3	
		$C_L = 50 \text{ pF}, R_L = 500 \Omega$	$\textbf{3.3}\pm\textbf{0.3}$	1.2	3.2	4.8	1.2	5.2	
			5.0 ± 0.5	0.8	2.4	3.7	0.8	4.0	
Input capacitance	C _{IN}	—	0~5.5	_	3.0	_	_	—	pF
Power dissipation capacitance	C _{PD}	(Note)	3.3	_	20		_	_	pF
		(Note)	5.5		26				

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

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Package Dimensions

SSOP8-P-0.65

Unit : mm





Weight: 0.02 g (typ.)

TOSHIBA

Package Dimensions

SSOP8-P-0.50A

Unit : mm





Weight: 0.01 g (typ.)

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Handbook" etc..

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