TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

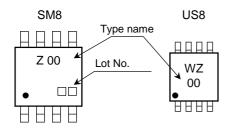
TC7WZ00FU,TC7WZ00FK

2 Input Nand Gate

Features

- High output drive: ±24 mA (min) @VCC = 3 V
- Super high speed operation: t_{pd} 2.4 ns (typ.) @VCC = 5 V, 50 pF
- Operation voltage range: $V_{CC \text{ (opr)}} = 1.65 \sim 5.5 \text{ V}$
- Latch-up performance: ±500 mA or more
- ESD performance: ±200 V or more (JEITA) ±2000 V or more (MIL)
- Power down protection is provided on all inputs and outputs.
- \bullet Matches the performance of TC74LCX series when operated at 3.3 V VCC.

Marking



TC7WZ00FU SSOP8-P-0.65 TC7WZ00FK SSOP8-P-0.50A

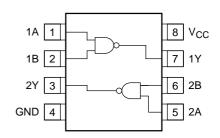
Weight

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Power supply voltage	Vcc	-0.5~6	V
DC input voltage	V _{IN}	-0.5~6	V
DC output voltage	V _{OUT}	-0.5~6	V
Input diode current	I _{IK}	-20	mA
Output diode current	lok	-20	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	P _D	300 (SM8) 200 (US8)	mW
Storage temperature	T _{stg}	-65~150	°C
Lead temperature (10s)	TL	260	°C

Pin Assignment (top view)



Truth Table

А	В	Υ
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

Logic Diagram



Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	1.65~5.5	V	
Supply voltage	vCC	1.5~5.5 (Note 1)	V	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~5.5 (Note 2)	V	
		0~V _{CC} (Note 3)	V	
Operating temperature	T _{opr}	-40~85	°C	
		$0 \sim 20 \; (V_{CC} = 1.8 \; V \pm 0.15 \; V, \\ 2.5 \; V \pm 0.2 \; V)$	ns/V	
Input rise and fall time	d _t /d _√	$0 \sim 10 \; (V_{CC} = 3.3 \; V \pm 0.3 \; V)$		
		$0~5~(V_{CC} = 5.5~V \pm 0.5~V)$		

Note 1: Data retention only

Note 2: $V_{CC} = 0 V$

Note 3: High or low state



Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Condition		Symbol Test Condition			Ta = 25°C			Ta = -40~85°C		Unit	
		Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit		
High level		V _{IH}			1.65~ 1.95	0.75 × V _{CC}	_	_	0.75 × V _{CC}		
Input	VIH		_	2.3~5.5	0.7 × V _{CC}	_	_	0.7 × V _{CC}		V	
voltage	Low level	.,	_		1.65~ 1.95		_	0.25 × V _{CC}		0.25 × V _{CC}	v
	Low level	V _{IL}			2.3~5.5		_	0.3 × V _{CC}	_	$\begin{array}{c} 0.3 \\ \times \text{V}_{CC} \end{array}$	
					1.65	1.55	1.65	_	1.55		
				I _{OH} = -100 μA	2.3	2.2	2.3		2.2	_	
				10η – 100 μ/	3.0	2.9	3.0	_	2.9	_	
					4.5	4.4	4.5	_	4.4	_	
	High level	V_{OH}	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -4 \text{ mA}$	1.65	1.29	1.52		1.29	_	
				$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15	_	1.9	_	
				$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.8	_	2.4	_	
				$I_{OH} = -24 \text{ mA}$	3.0	2.3	2.68	_	2.3		
Output				$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8	_	
voltage			V _{OL} V _{IN} = V _{IH}	I _{OL} = 100 μA	1.65	_	0	0.1	_	0.1	
					2.3	—	0	0.1	_	0.1	
		ow level V _{OL}			3.0		0	0.1		0.1	
					4.5	_	0	0.1	_	0.1	
Low le	Low level			I _{OL} = 4 mA	1.65	_	0.08	0.24		0.24	
				$I_{OL} = 8 \text{ mA}$	2.3	_	0.1	0.3		0.3	
				I _{OL} = 16 mA	3.0	_	0.15	0.4		0.4	
				I _{OL} = 24 mA	3.0	_	0.22	0.55		0.55	
				I _{OL} = 32 mA	4.5	_	0.22	0.55	_	0.55	
Input leakage	t leakage current I _{IN} V _{IN} = 5.5 V or GND		0~5.5	_	_	±1	_	±10	μА		
Power off leal	kage current	I _{OFF}	V _{IN} or V _{OL}	_T = 5.5 V	0.0	_	_	1	_	10	μΑ
Quiescent supply current I _{CC} V _{IN} = 5.5 V or GND		or GND	1.65~5.5			1	_	10	μΑ		

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AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Cumbal	Test Condition		Ta = 25°C			Ta = -40~85°C		Lloit
Characteristics	Symbol	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	^t pLH ^t pHL	C_L = 15 pF, R_L = 1 $M\Omega$	1.8 ± 0.15	2.0	5.3	9.6	2.0	9.8	- ns
			2.5 ± 0.2	1.2	3.2	5.3	1.2	5.7	
			3.3 ± 0.3	0.8	2.4	3.7	0.8	4.0	
			5.0 ± 0.5	0.5	1.9	2.9	0.5	3.2	
		$C_L = 50 \text{ pF}, R_L = 500 \Omega$	3.3 ± 0.3	1.2	3.0	4.6	1.2	4.9	
			5.0 ± 0.5	0.8	2.4	3.6	0.8	3.9	
Input capacitance	C _{IN}		0~5.5		3.0		_		pF
Power dissipation capacitance	C _{PD} (No	(Note)	3.3		22	_	_	_	pF
		(Note)	5.5	_	32	_	_	_	Ы

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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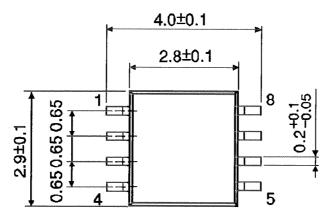
Average operating current can be obtained by the equation:

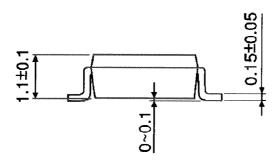
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$



Package Dimensions

SSOP8-P-0.65 Unit: mm





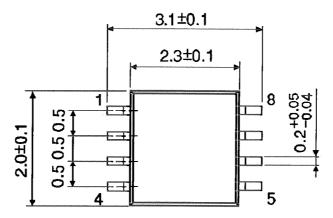
Weight: 0.02 g (typ.)

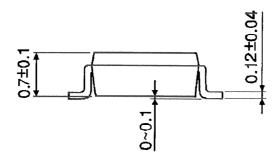
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Package Dimensions

SSOP8-P-0.50A Unit: mm





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Weight: 0.01 g (typ.)

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