TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TC7W14F, TC7W14FU, TC7W14FK

# SCHMITT INVERTER

The TC7W14 is high speed C<sup>2</sup>MOS SCHMITT INVERTER fabricated with silicon gate C<sup>2</sup>MOS technology. It achieves the high speed operation similar to equivalent LSTTL while maintaining the C<sup>2</sup>MOS low power dissipation.

Pin configuration and function are the same as the TC7WU04 but the inputs have 25% V<sub>CC</sub> hysteresis and with its schmitt trigger function, the TC7W14 can be used as a line receivers which will receive slow input signals.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### FEATURES

- High Speed .....  $t_{pd} = 11ns$  (Typ.) at  $V_{CC} = 5V$
- Low Power Dissipation ......  $I_{CC} = 1 \mu A$  (Max.) at Ta = 25°C
- High Noise Immunity ..... V<sub>H</sub> = 1.1V at
  - V<sub>CC</sub> = 5V
- Output Drive Capability ..... 10 LSTTL Loads
- Symmetrical Output Impedance ... |I<sub>OH</sub>| = I<sub>OL</sub> = 4mA
- Balanced Propagation Delays ..... t<sub>pLH</sub>≒t<sub>pHL</sub>
- Wide Operating Voltage Range ... V<sub>CC (opr)</sub> = 2~6V

#### MARKING





#### **MAXIMUM RATINGS** ( $Ta = 25^{\circ}C$ )

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	Vcc	-0.5~7	V
DC Input Voltage	V <sub>IN</sub>	-0.5~V <sub>CC</sub> +0.5	V
DC Output Voltage	νουτ	-0.5~V <sub>CC</sub> +0.5	V
Input Diode Current	lικ	± 20	mA
Output Diode Current	lок	± 20	mA
DC Output Current	ΙΟUΤ	± 25	mA
DC V <sub>CC</sub> /Ground Current	lcc	± 25	mA
Pouron Dissingtion	D-	300 (FM8, SM8)	
Power Dissipation	PD	200 (US8)	m₩
Storage Temperature	T <sub>stg</sub>	- 65~150	°C
Lead Temperature (10s)	ТL	260	°C

#### LOGIC DIAGRAM

1A——	$\square$	└─── 1Y
2A	$\square$	2Y
3A —	$\square$	—— 3Y

### TRUTH TABLE

А	Y
L	Н
Н	L

PIN ASSIGNMENT (TOP VIEW)



#### **RECOMMENDED OPERATING CONDITIONS**

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	Vcc	2~6	V
Input Voltage	VIN	0~V <sub>CC</sub>	V
Output Voltage	Vout	0~V <sub>CC</sub>	V
Operating Temperature	T <sub>opr</sub>	- 40~85	°C

## DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL				T	Ta = 25°C			$Ta = -40 \sim 85^{\circ}C$		
CHARACTERISTIC	STIVIBOL	IE31	TEST CONDITION		MIN.	TYP.	MAX.	MIN.	MAX.	UNIT	
Positive Threshold				2.0	1.0	1.25	1.5	1.0	1.5		
	V <sub>P</sub>		—	4.5	2.3	2.7	3.15	2.3	3.15	V	
Voltage				6.0	3.0	3.5	4.2	3.0	4.2		
Negativo					0.3	0.65	0.9	0.3	0.9		
Negative	∨ <sub>N</sub>		—	4.5	1.13	1.6	2.0	1.13	2.0	V	
Threshold Voltage				6.0	1.5	2.3	2.6	1.5	2.6		
				2.0	0.3	0.6	1.0	0.3	1.0		
Hysteresis Voltage	VH		—	4.5	0.6	1.1	1.4	0.6	1.4	V	
				6.0	0.8	1.2	1.7	0.8	1.7		
				2.0	1.9	2.0	—	1.9	_		
Illach Laural			l <sub>OH</sub> = – 20μA	4.5	4.4	4.5		4.4	—		
High-Level	V <sub>OH</sub>	$V_{IN} = V_{IL}$	6.0	5.9	6.0	—	5.9	—	v		
Output Voltage			$I_{OH} = -4mA$	4.5	4.18	4.31	—	4.13	_		
			l <sub>OH</sub> = – 5.2mA	6.0	5.68	5.80		5.63	—		
				2.0	_	0.0	0.1	_	0.1		
			l <sub>OL</sub> =20μA	4.5		0.0	0.1	—	0.1		
Low-Level	VOL	V <sub>IN</sub> = V <sub>IH</sub>		6.0		0.0	0.1	—	0.1	v	
Output Voltage			I <sub>OL</sub> = 4mA	4.5	_	0.17	0.26	_	0.33		
			$I_{OL} = 5.2 \text{mA}$	6.0		0.18	0.26	—	0.33		
Input Leakage Current	ЧN	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	—	—	±0.1		± 1.0		
Quiescent Supply Current	lcc	V <sub>IN</sub> = V <sub>CC</sub> o	or GND	6.0			1.0		10.0	μA	

**Propagation Delay** 

Time

21

ns

11

AC ELECTRICAL CHARAC		$L = 15 p_{\rm e}, v_{\rm e} C = 5 v, ra = 25 C)$				
CHARACTERISTIC	SYMBOL	TEST CONDITION	Ta = 25°C			UNIT
CHARACTERISTIC	STIVIDOL	TEST CONDITION	MIN.	TYP.	MAX.	
Output Transition	t⊤LH	_	_	4	8	ns
Time	tthl					

# **AC ELECTRICAL CHARACTERISTICS** ( $C_L = 15pF$ , $V_{CC} = 5V$ , $Ta = 25^{\circ}C$ )

#### AC ELECTRICAL CHARACTERISTICS ( $C_L = 50pF$ , Input $t_r = t_f = 6ns$ )

t<sub>pLH</sub>

t<sub>pHL</sub>

CHARACTERISTIC	SYMBOL	TEST CONDITION		Ta = 25°C			Ta = - 4	UNIT		
CHARACTERISTIC	STIVIBUL	TEST CONDITION	Vcc	MIN.	TYP.	MAX.	MIN.	MAX.		
Output Transition	+		2.0	—	30	75	—	95		
Time		—	4.5	—	8	15	—	19	ns	
Time	<sup>t</sup> THL		6.0		7	13	—	16		
Draw and the Dalam t	+		2.0	_	42	125	—	155		
Propagation Delay Time	t <sub>pLH</sub>	• •	—	4.5	_	14	25	—	31	ns
nme	t <sub>pHL</sub>		6.0		12	21	_	26		
Input Capacitance	CIN	_		—	5	10	—	10		
Power Dissipation Capacitance	C <sub>PD</sub>	(Note 1)		_	28	_	_	_	рF	

(Note 1) : C<sub>PD</sub> is defined as the value of internal equivalent capacitance of IC which is calculated from the operating current consumption without load (refer to Test Circuit).

Average operating current can be obtained by the equation hereunder.  $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC} / 2$  (per gate) PACKAGE DIMENSIONS

SOP8-P-1.27





Weight : 0.05g (Typ.)

#### PACKAGE DIMENSIONS SSOP8-P-0.65

Unit : mm





Weight : 0.02g (Typ.)

#### PACKAGE DIMENSIONS SSOP8-P-0.50A

Unit : mm





Weight : 0.01g (Typ.)

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