TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74HC540AP,TC74HC540AF,TC74HC541AP,TC74HC541AF

Octal Bus Buffer

TC74HC540AP/AF Inverting, 3-State Outputs

TC74HC541AP/AF Non-Inverting, 3-State Outputs

The TC74HC540A/TC74HC541A are high speed CMOS OCTAL BUS BUFFERs fabricated with silicon gate C<sup>2</sup>MOS technology.

They achieve the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

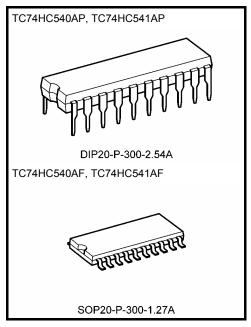
The TC74HC540A is an inverting type, and the TC74HC541A is a non-inverting type.

When either  $\overline{G}1$  or  $\overline{G}2$  are high, the terminal outputs are in the high-impedance state.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### **Features**

- High speed:  $t_{pd} = 10 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 4 \mu A \text{ (max)}$  at  $T_a = 25^{\circ}C$
- High noise immunity: V<sub>NIH</sub> = V<sub>NIL</sub> = 28% V<sub>CC</sub> (min)
- Output Drive Capability: 15 LSTTL loads
- Symmetrical output impedance: | I<sub>OH</sub> | = I<sub>OL</sub> = 6 mA (min)
- Balanced propagation delays:  $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range:  $V_{CC}$  (opr) = 2 to 6 V
- Pin and function compatible with 74LS540/541

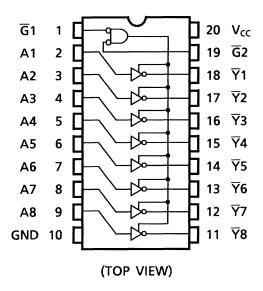


Weight

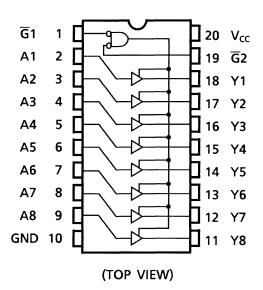
DIP20-P-300-2.54A : 1.30 g (typ.) SOP20-P-300-1.27A : 0.22 g (typ.)

### **Pin Assignment**

#### **TC74HC540A**

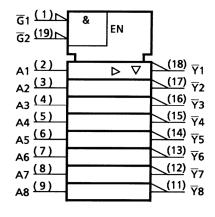


#### **TC74HC541A**

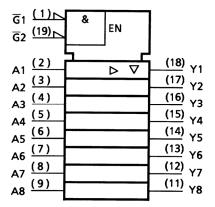


## **IEC Logic Symbol**

#### TC74HC540A



#### **TC74HC541A**



### **Truth Table**

|    | Inputs | Outputs |     |      |  |
|----|--------|---------|-----|------|--|
| G1 | G2 An  |         | Yn* | ₹n * |  |
| Н  | Х      | Х       | Z   | Z    |  |
| Х  | Н      | Х       | Z   | Z    |  |
| L  | L      | Н       | Н   | L    |  |
| L  | L      | L       | L   | Н    |  |

X: Don't care

Z: High impedance

\*: Yn..... HC541

Yn ..... HC540

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### **Absolute Maximum Ratings (Note 1)**

| Characteristics                    | Symbol           | Rating                        | Unit |
|------------------------------------|------------------|-------------------------------|------|
| Supply voltage range               | V <sub>CC</sub>  | –0.5 to 7                     | V    |
| DC input voltage                   | V <sub>IN</sub>  | -0.5 to V <sub>CC</sub> + 0.5 | V    |
| DC output voltage                  | V <sub>OUT</sub> | −0.5 to V <sub>CC</sub> + 0.5 | V    |
| Input diode current                | I <sub>IK</sub>  | ±20                           | mA   |
| Output diode current               | lok              | ±20                           | mA   |
| DC output current                  | lout             | ±35                           | mA   |
| DC V <sub>CC</sub> /ground current | Icc              | ±75                           | mA   |
| Power dissipation                  | PD               | 500 (DIP) (Note 2)/180 (SOP)  | mW   |
| Storage temperature                | T <sub>stg</sub> | -65 to 150                    | °C   |

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to  $65^{\circ}C$ . From Ta = 65 to  $85^{\circ}C$  a derating factor of -10 mW/°C shall be applied until 300 mW.

## **Operating Ranges (Note)**

| Characteristics          | Symbol                          | Rating                              | Unit |
|--------------------------|---------------------------------|-------------------------------------|------|
| Supply voltage           | $V_{CC}$                        | 2 to 6                              | V    |
| Input voltage            | V <sub>IN</sub>                 | 0 to V <sub>CC</sub>                | ٧    |
| Output voltage           | V <sub>OUT</sub>                | 0 to V <sub>CC</sub>                | >    |
| Operating temperature    | T <sub>opr</sub>                | −40 to 85                           | °C   |
|                          |                                 | 0 to 1000 (V <sub>CC</sub> = 2.0 V) |      |
| Input rise and fall time | t <sub>r</sub> , t <sub>f</sub> | 0 to 500 (V <sub>CC</sub> = 4.5 V)  | ns   |
|                          |                                 | 0 to 400 (V <sub>CC</sub> = 6.0 V)  |      |

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.



# **Electrical Characteristics**

## **DC Characteristics**

| Characteristics                  | Symbol          |  | Test Condition             |                     | Ta = 25°C |      |      | Ta =<br>-40 to 85°C |      | Unit  |
|----------------------------------|-----------------|--|----------------------------|---------------------|-----------|------|------|---------------------|------|-------|
| S. Israelonollos Oynib           | Cymbol          |  |                            | V <sub>CC</sub> (V) | Min       | Тур. | Max  | Min                 | Max  | Offic |
|                                  |                 | _  |                            | 2.0                 | 1.50      | _    | _    | 1.50                | _    |       |
| High-level input voltage         | V <sub>IH</sub> |  |                            | 4.5                 | 3.15      | _    | _    | 3.15                | _    | V     |
|                                  |                 |  |                            |                     | 4.20      | _    | _    | 4.20                | _    |       |
|                                  |                 | _  |                            | 2.0                 | _         | _    | 0.50 | _                   | 0.50 |       |
| Low-level input voltage          | $V_{IL}$        |  |                            | 4.5                 | _         | _    | 1.35 | _                   | 1.35 | V     |
| ŭ                                |                 |  |                            | 6.0                 | _         |      | 1.80 |                     | 1.80 |       |
|                                  |                 |  | Ι <sub>ΟΗ</sub> = -20 μΑ   | 2.0                 | 1.9       | 2.0  | _    | 1.9                 | _    |       |
|                                  |                 | V <sub>IN</sub><br>= V <sub>IH</sub> or<br>V <sub>IL</sub>             |                            | 4.5                 | 4.4       | 4.5  | _    | 4.4                 | _    | V     |
| High-level output voltage        | V <sub>OH</sub> |  |                            | 6.0                 | 5.9       | 6.0  | _    | 5.9                 | _    |       |
|                                  |                 |  | $I_{OH} = -6 \text{ mA}$   | 4.5                 | 4.18      | 4.31 | _    | 4.13                | _    |       |
|                                  |                 |  | $I_{OH} = -7.8 \text{ mA}$ | 6.0                 | 5.68      | 5.80 | _    | 5.63                | _    |       |
|                                  | VoL             | V <sub>IN</sub><br>= V <sub>IH</sub> or<br>V <sub>IL</sub>             | I <sub>OL</sub> = 20 μA    | 2.0                 | _         | 0.0  | 0.1  | _                   | 0.1  |       |
|                                  |                 |  |                            | 4.5                 | _         | 0.0  | 0.1  | _                   | 0.1  |       |
| Low-level output voltage         |                 |  |                            | 6.0                 | _         | 0.0  | 0.1  |                     | 0.1  | V     |
|                                  |                 |  | I <sub>OL</sub> = 6 mA     | 4.5                 | _         | 0.17 | 0.26 | _                   | 0.33 |       |
|                                  |                 |  | I <sub>OL</sub> = 7.8 mA   | 6.0                 | _         | 0.18 | 0.26 |                     | 0.33 |       |
| 3-state output off-state current | I <sub>OZ</sub> | $V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = V_{CC} \text{ or GND}$ |                            | 6.0                 | _         | _    | ±0.5 | _                   | ±5.0 | μА    |
| Input leakage current            | I <sub>IN</sub> | V <sub>IN</sub> = V <sub>CC</sub> or GND                               |                            | 6.0                 | _         | _    | ±0.1 | _                   | ±1.0 | μА    |
| Quiescent supply current         | Icc             | V <sub>IN</sub> = V <sub>CC</sub> or GND                               |                            | 6.0                 | _         | _    | 4.0  | _                   | 40.0 | μА    |

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AC Characteristics (input:  $t_r = t_f = 6$  ns)

| Characteristics               | Symbol                               | Test Condition            |            | Ta = 25°C           |     |      | Ta =<br>-40 to 85°C |     | Unit |       |
|-------------------------------|--------------------------------------|---------------------------|------------|---------------------|-----|------|---------------------|-----|------|-------|
| Characteristics               | Ondiacteristics Symbol               |                           | CL<br>(pF) | V <sub>CC</sub> (V) | Min | Тур. | Max                 | Min | Max  | Offic |
|                               | 4                                    |                           |            | 2.0                 | _   | 25   | 60                  | _   | 75   |       |
| Output transition time        | t <sub>TLH</sub>                     | _                         | 50         | 4.5                 | _   | 7    | 12                  | _   | 15   | ns    |
|                               | t <sub>THL</sub>                     |                           |            | 6.0                 | _   | 6    | 10                  | _   | 13   |       |
|                               |                                      |                           |            | 2.0                 | _   | 36   | 90                  | _   | 115  |       |
|                               |                                      |                           | 50         | 4.5                 | _   | 12   | 18                  | _   | 23   |       |
| Propagation delay             | $t_{pLH}$                            |                           |            | 6.0                 | _   | 10   | 15                  | _   | 20   |       |
| time                          | $t_{pHL}$                            | _                         |            | 2.0                 | _   | 51   | 130                 | _   | 165  | ns    |
|                               |                                      |                           | 150        | 4.5                 | _   | 17   | 26                  | _   | 33   |       |
|                               |                                      |                           |            | 6.0                 | _   | 14   | 22                  | _   | 28   |       |
|                               | <sup>t</sup> pZL<br><sup>t</sup> pZH | $R_L = 1 \text{ k}\Omega$ |            | 2.0                 | _   | 45   | 125                 | _   | 155  | - ns  |
|                               |                                      |                           | 50         | 4.5                 | _   | 14   | 25                  | _   | 31   |       |
| Output enable time            |                                      |                           |            | 6.0                 | _   | 12   | 21                  | _   | 26   |       |
| Output enable time            |                                      |                           | 150        | 2.0                 | _   | 60   | 165                 | _   | 205  |       |
|                               |                                      |                           |            | 4.5                 | _   | 19   | 33                  | _   | 41   |       |
|                               |                                      |                           |            | 6.0                 | _   | 16   | 28                  | _   | 35   |       |
|                               | t <sub>pLZ</sub><br>t <sub>pHZ</sub> | $R_L = 1 \text{ k}\Omega$ | 50         | 2.0                 | _   | 40   | 125                 | _   | 155  | ns    |
| Output disable time           |                                      |                           |            | 4.5                 | _   | 16   | 25                  | _   | 31   |       |
|                               |                                      |                           |            | 6.0                 | _   | 14   | 21                  | _   | 26   |       |
| Input capacitance             | C <sub>IN</sub>                      | _                         |            |                     | _   | 5    | 10                  | _   | 10   | pF    |
| Output capacitance            | C <sub>OUT</sub>                     | _                         |            |                     | _   | 10   | _                   | _   | _    | pF    |
| Power dissipation capacitance | C <sub>PD</sub>                      | TC74HC540A                |            |                     | _   | 32   | _                   | _   | _    |       |
|                               | (Note)                               | TC74HC541A                |            |                     | _   | 35   | _                   | _   | _    | pF    |

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

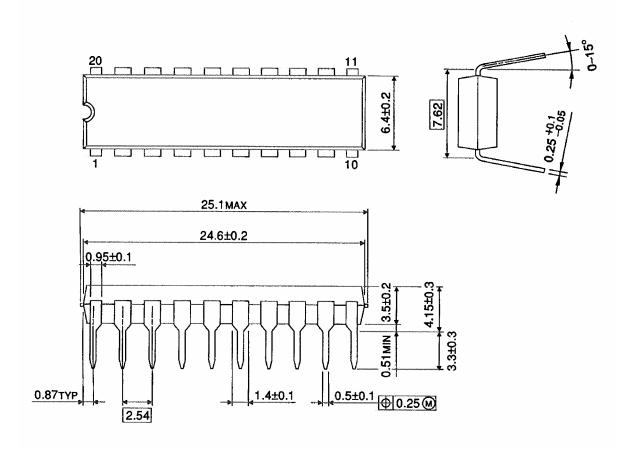
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Average operating current can be obtained by the equation:

$$I_{CC}$$
 (opr) =  $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8$  (per bit)

# **Package Dimensions**

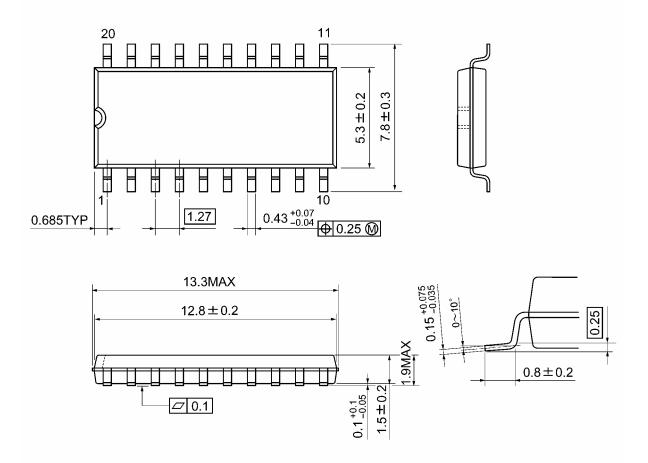
DIP20-P-300-2.54A Unit: mm



Weight: 1.30 g (typ.)

# **Package Dimensions**

SOP20-P-300-1.27A Unit: mm



Weight: 0.22 g (typ.)

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20070701-EN GENERAL

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