TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC4S584F

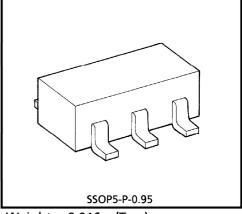
SCHMITT TRIGGER

TC4S584F is the one circuit inverter having the schmitt trigger function at the input terminal.

That is, since the circuit threshold level voltage at the leading and trailing edges of input waveform are different (Vp, V_N), the TC4S584F can be used in the broad range application, including line receiver, waveform shaping circuit, astable multivibrator, etc. In addition to ordinary inverter.

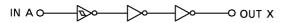
MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V_{DD}	Vss - 0.5~Vss + 20	V
Input Voltage	V _{IN}	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	٧
Output Voltage	Vout	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	٧
DC Input Current	I _{IN}	± 10	mA
Power Dissipation	PD	200	mW
Operating Temperature Range	T _{opr}	- 40~85	°C
Storage Temperature Range	T _{stg}	- 65~150	°C
Lead Temperature (10s)	TL	260	°C

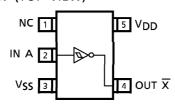


Weight: 0.016g (Typ.)

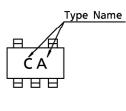
LOGIC DIAGRAM



PIN ASSIGNMENT (TOP VIEW)



MARKING



RECOMMENDED OPERATING CONDITIONS $(V_{SS} = 0V)$

CHARACTERISTIC	SYMBOL		MIN.	TYP.	MAX.	UNIT
DC Supply Voltage	V_{DD}	_	3	_	18	V
Input Voltage	VIN		0	_	V_{DD}	V

STATIC ELECTRICAL CHARACTERISTICS ($V_{SS} = 0V$)

I CHARACTERISTIC I		SYM-	TEST CONDITION	V_{DD}	– 40°C		25°C			85	UNIT	
CHARAC	BOL		1231 CONDITION	(V)	MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.	ONIT
High-Leve Output Vo		Vон	$ I_{OUT} < 1\mu A$ $V_{IN} = V_{SS}, V_{DD}$	5 10 15	4.95 9.95 14.95	_	4.95 9.95 14.95			4.95 9.95 14.95	_	V
Low-Level Output Vo		VOL	$ I_{OUT} < 1\mu A$ $V_{IN} = V_{SS}, V_{DD}$	5 10 15	111	0.05 0.05 0.05	1 1 1	0.00 0.00 0.00	0.05		0.05 0.05 0.05	V
Output Hi Current	igh	ЮН	$V_{OH} = 4.6V$ $V_{OH} = 2.5V$ $V_{OH} = 9.5V$ $V_{OH} = 13.5V$ $V_{IN} = V_{SS}, V_{DD}$	5 5 10 15	- 0.61 - 2.5 - 1.5 - 4.0	_ _	- 0.51 - 2.1 - 1.3 - 3.4		l	- 0.42 - 1.7 - 1.1 - 2.8	_	
Output Lo Current	ow	loL	$V_{OL} = 0.4V$ $V_{OL} = 0.5V$ $V_{OL} = 1.5V$ $V_{IN} = V_{SS}, V_{DD}$	5 10 15	0.61 1.5 4.0	_	0.51 1.3 3.4	1.5 3.8 15.0	l	0.42 1.1 2.8	_	mA
Positive Ti Threshold		V _P	V _{OUT} = 0.5V V _{OUT} = 1.0V V _{OUT} = 1.5V	5 10 15	1.95 4.3 6.9	7.1	2.05 4.5 7.1		7.1	2.05 4.7 7.1	7.2	
Negative Threshold		ν _N	V _{OUT} = 4.5V V _{OUT} = 9.0V V _{OUT} = 13.5V	5 10 15	1.05 2.1 3.2	4.9 7.0	1.1 2.2 3.3	2.1 3.5 5.0	2.6 4.7 6.8	0.95 2.0 3.1	4.8 6.9	V
Hystersis \	/oltage*	VΗ	-	5 10 15	0.1 1.7 3.1	1.35 3.2 4.8	0.4 1.8 3.2	4.0	1.3 3.2 4.8	0.4 1.7 3.2	1.50 3.4 4.9	
Input	H Level	ΊΗ	V _{IH} = 18V	18	_	0.1		10-5		_	1.0	μΑ
Current	L Level	կլ	V _{IL} = 0V	18	_	- 0.1	_	- 10 ⁻⁵		_	- 1.0	μ,,
Quiescent Device Cu		I _{DD}	V _{IN} = V _{SS} , V _{DD}	5 10 15		1 2 4		0.001 0.002 0.004	1 2 4	_ _ _	7.5 15 30	μΑ

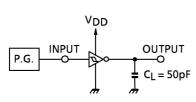
(Note) Values are different to TC4584BP, TC4584BF marked* (Vp, VN, VH).

DYNAMIC ELECTRICA	L CHARACTERISTICS	$(Ta = 25^{\circ}C,$	$V_{SS} = 0V_{A}$	$C_1 = 50pF$
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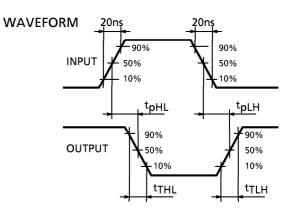
CHARACTERISTIC	SYMBOL	TEST CONDITION	V _{DD} (V)	MIN.	TYP.	MAX.	UNIT
Output Transition Time (Low to High)	tTLH	_	5 10	_	80 50	200 100	
Output Transition Time (High to Low)	t _{THL}	_	15 5 10		40 80 50	200 100	ns
(ingii to Low)			15 5		40 170	80 340	
Propagation Delay Time	t _{pLH} t _{pHL}	_	10 15	_	80 60	160 120	ns
Input Capacitance	CIN	_	_	5	7.5	pF	

CIRCUIT AND WAVEFORM FOR MEASUREMENT OF DYNAMIC CHARACTERISTICS

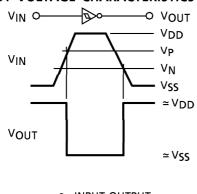
CIRCUIT

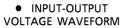


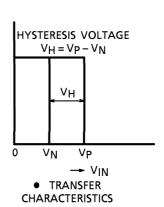
P.G.: PULSE GENERATOR



INPUT-OUTPUT VOLTAGE CHARACTERISTICS



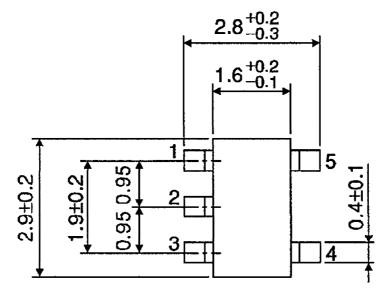


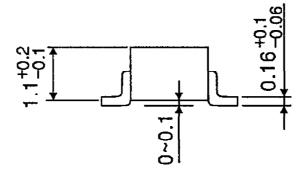


PACKAGE DIMENSIONS

SSOP5-P-0.95

Unit: mm





Weight: 0.016g (Typ.)

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000707EBA

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