TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA8210AHQ,TA8210ALQ

20W BTL × 2ch Audio Power Amplifier

The thermal resistance θj -T of TA8210AHQ, TA8210ALQ package designed for low thermal resistance, has a high efficiency of heat radiation.

The temperature rise of chip can be reduced, and the influence from the degradation of the features due to the temperature rise at the high output can also be reduced.

This stereo audio power IC, designed for car audio use, has two built–in channels to reduce the characteristic difference between L and R channels.

In addition, the functions of stand-by and muting, and a variety of protection circuits are involved.



Features

- Low thermal resistance : $\theta_j - T = 1.5^{\circ}C / W$ (infinite heat sink)
- High power
 - : POUT (1) = 22W (typ.) / channel (V_{CC} = 14.4V, f = 1kHz, THD = 10%, R_L = 4 Ω) POUT (2) = 19W (typ.) / channel (V_{CC} = 13.2V, f = 1kHz, THD = 10%, R_L = 4 Ω)

Weight

HZIP17-P-2.00: 9.8g (typ.) HSIP17-P-2.00: 9.8g (typ.)

- Low distortion ratio: THD = 0.04% (typ.) (V_{CC} = 13.2V, f = 1kHz, P_{OUT} = 1W, R_L = 4 Ω , G_V = 50dB)
- Low noise: $V_{NO} = 0.30 \text{mV}_{rms}$ (typ.) ($V_{CC} = 13.2\text{V}$, $R_L = 4\Omega$, $G_V 50 \text{dB}$, $R_g = 0\Omega$, $BW = 20 \text{Hz} \sim 20 \text{kHz}$)
- Built-in stand-by function (with pin(4) set at low, power is turned off.): ISB = 1µA (typ.)
- Built-in muting function (with pin(1) set at low, power is turned off.): V (mute) = 1V (typ.)
- Built-in various protection circuits

 $Protection \ circuits: \ Thermal \ shut \ down, \ Over \ voltage, \ Out \rightarrow V_{CC} \ short, \ Out \rightarrow GND \ short \ and \ Out - Out \ short.$

• Operating supply voltage: V_{CC} = 9~18V

Block Diagram

TA8210AHQ, TA8210ALQ (G_V = 50dB)



Cautions And Application Method (description is made only on the single channel.)

1. Voltage gain adjustment



This IC has the amplifier construction as shown in Fig.1. The pre–amp (amp 1) is provided to the primary stage, and the input voltage is amplified by the flat amps, amp 3 and amp 4 of each channel through the phase amp. (Amp 2).

Since the input offset is prevented by pre–amp when V_{CC} is set to on, this circuit can remarkably reduce the pop noise.

The total closed loop gain G_V of this IC can be obtained by expression below when the closed loop voltage gain of amp 1 is G_{V1} .

$$G_{V1} \!=\! 20 \ell \text{og} \, \frac{\text{R1} \! + \! (\text{R}_f + \text{R2})}{\text{R}_f + \text{R2}} \, (\text{dB}) \quad \quad (1)$$

The closed loop voltage gain of power amp, amp 3 and amp 4 is fixed at $G_{V3} = G_{V4} = 20$ dB. Therefore, the total closed circuit voltage gain GV is

obtained through BTL connection by the expression below.

 $G_V = G_{V1} + G_{V3} + 6 (dB)$ (2)

For example, when $R_f = 0\Omega$, GV is obtained by the expressions (1) and (2) as shown below.

$$G_V = 24 + 20 + 6 = 50 dB$$

The voltage gain is reduced when Rf is increased. (Fig.2) With the voltage gain reduced, since (1) the oscillation stability is reduced, and (2) the pop noise changes when VCC is set to on, refer to the items 3 and 4.







Fig.3 With pin(4) set to high, power is on.

2. Stand-by SW function

By means of controlling pin(4) (stand–by terminal) to high and low, the power supply can be set to on and off. The threshold voltage of pin(4) is set at 2.1V (3VBE.), and the power supply current is about 1 μ A (typ.) at the stand–by state.

Pin(4) control voltage : V (SB)

Stand-by	Power	V _(SB) (V)
On	Off	0~2
Off	On	3~V _{CC}

Advantage of stand-by SW

- (1) Since V_{CC} can directly be controlled to on / off by the microcomputer, the switching relay can be omitted.
- (2) Since the control current is microscopic, the switching relay of small current capacity is satisfactory for switching.



(Standby Switch Method)

3. Preventive measure against oscillation

For preventing the oscillation, it is advisable to use C4, the condenser of polyester film having small characteristic fluctuation of the temperature and the frequency.

The resistance R to be series applied to C_4 is effective for phase correction of high frequency, and improves the oscillation allowance.

Since the oscillation allowance is varied according to the causes described below, perform the temperature test to check the oscillation allowance.

- (1) Voltage gain to be used (GV setting)
- (2) Capacity value of condenser
- (3) Kind of condenser
- (4) Layout of printed board

In case of its use with the voltage gain GV reduced or with the feedback amount increased, care must be taken because the phase–inversion is caused by the high frequency resulting in making the oscillation viably generated.

4. Input offset prevention circuit at VCC \rightarrow on

Having Pre–Amp (Amp 1) mounted on the primary stage, this IC contains the circuit for making the Amp 1 input voltage and the NF terminal voltage equipotential.

Therefore, the offset voltage produced at the input stage is suppressed to prevent the pop noise at $V_{CC} \rightarrow on$. The capacity values of the input and NF condenser (C₁ and C₂) shall be set according to the gain to be used. (Reference) (A) At $G_V = 50 \text{dB}$ (R_f = 0 Ω)

 $C_1 = 4.7 \mu F, C_2 = 47 \mu F$ (B) At GV = 40dB (Rf = 470Ω) C_1 = 3.3 \mu F, C_2 = 33 \mu F

5. Muting function

Through setting pin(1) (mute terminal) at about 1V or less, muting becomes possible.

The interval circuit of IC is shown in Fig.4.

When pin(1) is set to low, Q1 and Q2 are turned to on, the charge of the ripple condenser is discharged and the bias is cut. The mute amount of 60dB or over can be obtained.

Since this muting function rapidly discharge the charge of the ripple filter capacitor of pin(8), the pop noise is generated by the DC fluctuation of the bias section.

Therefore, this muting function is not appropriate to the audio muting but it is effective in muting at $V_{CC} \rightarrow on$.

Fig.4 Mute circuit

6. External part list and description

Sym- Recom-			Influence			
bol Malue	Feature	Smaller Than Recommended Value	Larger Than Recommended Value	Remarks		
C ₁	4.7µF	DC blocking	Related to pop noise at $V_{CC} \rightarrow on$.		Related to gain. Refer to item 4.	
	Co 470E		Related to pop noise at $V_{CC} \rightarrow on$.			
			Determination of low cut-off frequency.			
C ₂		Feedback condenser				
C ₃	220µF	Ripple reduction	Time constant is small at V_{CC} \rightarrow on or off.	Time constant is large at V_{CC} \rightarrow on or off.		
C ₄	0.12µF	Oscillation prevention	Made liable to oscillate.	Oscillation allowance.	Refer to item 3.	
C ₅	1000µF	Ripple filter	For filtering power supply hum and ripple. Large at using AC rectified power supply. Small at using DC power supply.			
C ₆	1000 _P F	Oscillation prevention	Oscillation allowance improved. Noise reduction		Refer to item 3.	

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Peak supply voltage (0.2s)	V _{CC} (surge)	50	V
DC supply voltage	V _{CC (DC)}	25	V
Operating supply voltage	V _{CC (opr)}	18	V
Output current (peak)	I _{O (peak)}	9	А
Power dissipation	PD	50	W
Operating temperature	T _{opr}	-30~85	°C
Storage temperature	T _{stg}	-55~150	°C

Electrical Characteristics (unless otherwise specified, V_{CC} = 13.2V, R_L = 4 Ω , f = 1kHz, Ta = 25°C)

Characteristic	Symbol	Test Cir– cuit	Test Condition	Min.	Тур.	Max.	Unit	
Quiescent supply current	ICCQ	_	V _{IN} = 0	_	120	250	mA	
Output power	P _{OUT (1)}	_	V _{CC} = 14.4V, THD = 10%	_	22	_	w	
	P _{OUT (2)}	_	THD = 10%	16	19	_		
Total harmonic distortion	THD	_	P _{OUT} = 1W	_	0.04	0.4	%	
Voltage gain	GV	_	_	48	50	52	dB	
Output noise voltage	V _{NO}	_	R _g = 0Ω, BW = 20Hz~20kHz	_	0.30	0.70	mV _{rms}	
Ripple rejection ratio	R.R.	_	fripple = 100Hz, R _g = 600 Ω	40	54	_	dB	
Input resistance	R _{IN}	_	_		30	_	kΩ	
Output offset voltage	Voffset		V _{IN} = 0	-0.3	0	0.3	V	
Current at stand-by state	I _{SB}	_	_	_	1	10	μA	
Cross talk	C.T.	_	R _g = 600Ω, V _{OUT} = 0.775V _{rms} (0dBm)	_	60	_	dB	
Pni(4) control voltage	V _{SB}	_	Stand–by→off (power→on)	2.5	_	V _{CC}	V	
Pin(1) control voltage	V _(mute)	_	Mute→on (power→off)	-	1.0	2.0	V	

Test Circuit

TA8210AHQ, TA8210ALQ (G_V = 50dB)





V_{CC} = 13.2V

V_{OUT} = 0.775V_{rms} (0dBm)

 $R_L = 4\Omega$ +

C.T. – f

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 $\left| \right| \right|$

10k 30k

f=1kHz

 $R_L = 4\Omega$

32

40

Dual Ope.

100k

3k

1k

PD - POUT

16

24

 $R_L = 4\Omega$

16

20

12



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Package Dimensions

HZIP17-P-2.00

Unit : mm



Weight: 9.8g (typ.)

Package Dimensions



Unit : mm



Weight: 9.8g (typ.)

- Use an appropriate power supply fuse to ensure that a large current does not continuously flow in case of over current and/or IC failure. The IC will fully break down when used under conditions that exceed its absolute maximum ratings, when the wiring is routed improperly or when an abnormal pulse noise occurs from the wiring or load, causing a large current to continuously flow and the breakdown can lead smoke or ignition. To minimize the effects of the flow of a large current in case of breakdown, appropriate settings, such as fuse capacity, fusing time and insertion circuit location, are required.
- If your design includes an inductive load such as a motor coil, incorporate a protection circuit into the design to
 prevent device malfunction or breakdown caused by the current resulting from the inrush current at power ON or
 the negative current resulting from the back electromotive force at power OFF. For details on how to connect a
 protection circuit such as a current limiting resistor or back electromotive force adsorption diode, refer to individual
 IC datasheets or the IC databook. IC breakdown may cause injury, smoke or ignition.
- Use a stable power supply with ICs with built-in protection functions. If the power supply is unstable, the protection function may not operate, causing IC breakdown. IC breakdown may cause injury, smoke or ignition.
- Carefully select external components (such as inputs and negative feedback capacitors) and load components (such as speakers), for example, power amp and regulator. If there is a large amount of leakage current such as input or negative feedback condenser, the IC output DC voltage will increase. If this output voltage is connected to a speaker with low input withstand voltage, overcurrent or IC failure can cause smoke or ignition. (The over current can cause smoke or ignition from the IC itself.) In particular, please pay attention when using a Bridge Tied Load (BTL) connection type IC that inputs output DC voltage to a speaker directly.
- Over current Protection Circuit

Over current protection circuits (referred to as current limiter circuits) do not necessarily protect ICs under all circumstances. If the Over current protection circuits operate against the over current, clear the over current status immediately. Depending on the method of use and usage conditions, such as exceeding absolute maximum ratings can cause the over current protection circuit to not operate properly or IC breakdown before operation. In addition, depending on the method of use and usage conditions, if over current continues to flow for a long time after operation, the IC may generate heat resulting in breakdown.

• Thermal Shutdown Circuit

Thermal shutdown circuits do not necessarily protect ICs under all circumstances. If the Thermal shutdown circuits operate against the over temperature, clear the heat generation status immediately. Depending on the method of use and usage conditions, such as exceeding absolute maximum ratings can cause the thermal shutdown circuit to not operate properly or IC breakdown before operation.

Heat Radiation Design

When using an IC with large current flow such as power amp, regulator or driver, please design the device so that heat is appropriately radiated, not to exceed the specified junction temperature (Tj) at any time and condition. These ICs generate heat even during normal use. An inadequate IC heat radiation design can lead to decrease in IC life, deterioration of IC characteristics or IC breakdown. In addition, please design the device taking into considerate the effect of IC heat radiation with peripheral components.

Installation to Heat Sink

Please install the power IC to the heat sink not to apply excessive mechanical stress to the IC. Excessive mechanical stress can lead to package cracks, resulting in a reduction in reliability or breakdown of internal IC chip. In addition, depending on the IC, the use of silicon rubber may be prohibited. Check whether the use of silicon rubber is prohibited for the IC you intend to use, or not. For details of power IC heat radiation design and heat sink installation, refer to individual technical datasheets or IC databooks.

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 The product is often the final stage (the external output stage) of a circuit. Substandard performance or malfunction of the destination device to which the circuit supplies output may cause damage to the circuit or to the product. 030619_R

About solderability, following conditions were confirmed

- Solderability
 - (1) Use of Sn-37Pb solder Bath
 - solder bath temperature = 230°C
 - · dipping time = 5 seconds
 - \cdot the number of times = once
 - · use of R-type flux
 - (2) Use of Sn-3.0Ag-0.5Cu solder Bath
 - solder bath temperature = 245°C
 - · dipping time = 5 seconds
 - $\cdot \,$ the number of times = once
 - use of R-type flux