

STPS10L60C

Power Schottky rectifier

Main product characteristics

I _{F(AV)}	2 x 5 A
V _{RRM}	60 V
T _{j (max)}	150° C
V _{F (max)}	0.52 V

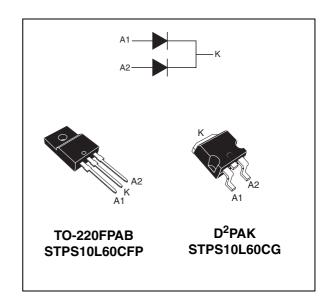
Features and benefits

- Low forward voltage drop
- Negligible switching losses
- Insulated package: TO-220FPAB Insulating voltage = 2000 V DC Capacitance = 12 pF
- Avalanche capability specified

Description

Dual center tap Schottky rectifier suited for switch mode power supplies and high frequency DC to DC converters.

Packaged in TO-220FPAB and D²PAK, this device is intended for use in high frequency inverters.



STPS10L60C **Characteristics**

Characteristics

Symbol	Parameter				Value	Unit
V _{RRM}	Repetitive peak reverse	voltage			60	V
I _{F(RMS)}	RMS forward current				30	Α
I _{F(AV)}	Average forward TO220FPAB current		$T_{C} = 130^{\circ} \text{ C}$ $\delta = 0.5$	Per diode Per device	5 10	Α
I _{FSM}	Surge non repetitive forward current tp = 10 ms Sinusoidal			180	Α	
I _{RRM}	Repetitive peak reverse current tp = 2 µs square F=1 kHz			1	Α	
P _{ARM}	Repetitive peak avalanche power $tp = 1 \mu s T_j = 25^{\circ} C$			4000	W	
T _{stg}	Storage temperature range			-65 to + 175	°C	
Tj	Maximum operating junction temperature ⁽¹⁾			150	°C	
dV/dt	Critical rate of rise reverse voltage			10000	V/µs	

^{1.} $\frac{dPtot}{dT_j} < \frac{1}{Rth(j-a)}$ thermal runaway condition for a diode on its own heatsink

Table 1. Thermal resistance

Symbol	Parameter		Value	Unit
R _{th (j-c)}	Junction to case TO-220FPAB	Per diode Total	4.5 3.5	° C/W
R _{th (c)}		Coupling	2.5	° C/W

When the diodes 1 and 2 are used simultaneously:

 Δ Tj(diode 1) = P(diode1) x R_{th(j-c)}(Per diode) + P(diode 2) x R_{th(c)}

Table 2. Static electrical characteristics (per diode)

Symbol	Parameter	Tests Conditions		Min.	Тур.	Max.	Unit
{I} (1)	Reverse leakage current	T _j = 25° C	$V_R = V_{RRM}$			220	μΑ
'R		T _j = 125° C	YR - YRRM		45	60	mA
		T _j = 25° C	I _F = 5 A			0.55	
V_ (1)	V _F ⁽¹⁾ Forward voltage drop	T _j = 125° C	I _F = 5 A		0.43	0.52	V
VF \		T _j = 25° C	I _F = 10 A			0.67	V
		T _j = 125° C	I _F = 10 A		0.55	0.64	

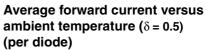
^{1.} Pulse test : tp = 380 μ s, δ < 2%

To evaluate the conduction losses use the following equation: P = 0.44 x $I_{F(AV)}$ + 0.0091x $I_{F}^{2}_{(RMS)}$

$$P = 0.44 \times I_{F(AV)} + 0.0091 \times I_{F}^{2}_{(RMS)}$$

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Figure 1. Average forward power dissipation Figure 2. versus average forward current (per diode)



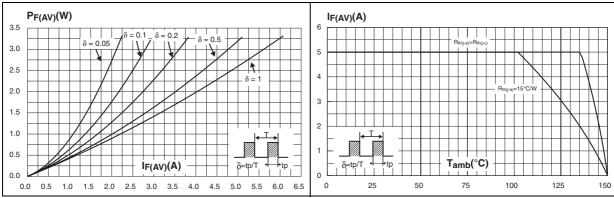


Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature

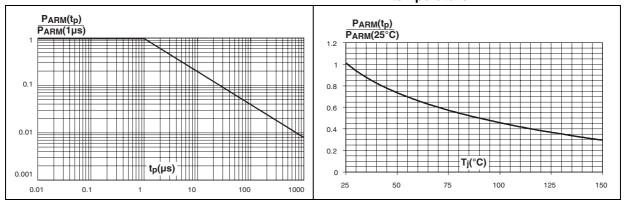
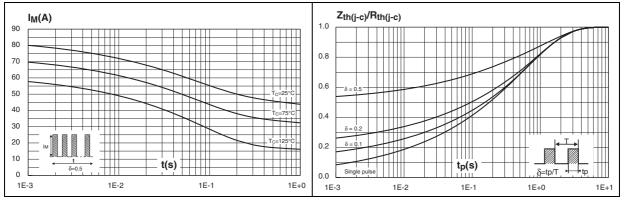


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values, per diode) (TO-220FPAB)

Figure 6. Relative variation of thermal transient impedance junction to case versus pulse duration (TO-220FPAB)



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Figure 7. Relative variation of thermal transient impedance junction to case versus pulse duration (D²PAK)

Figure 8. Reverse leakage current versus reverse voltage applied (typical values, per diode)

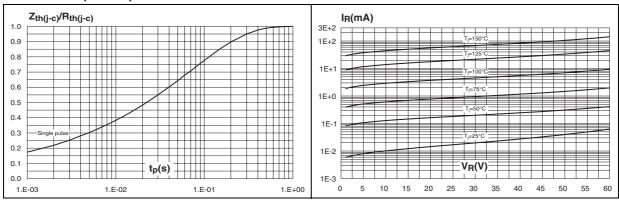


Figure 9. Junction capacitance versus reverse voltage applied (typical values, per diode)

Figure 10. Forward voltage drop versus forward current (maximum values, per diode)

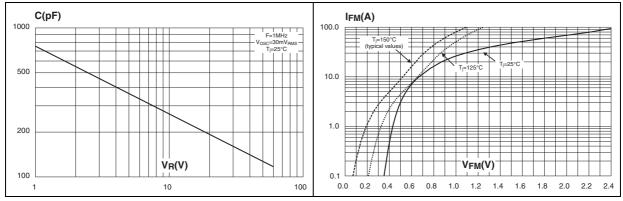
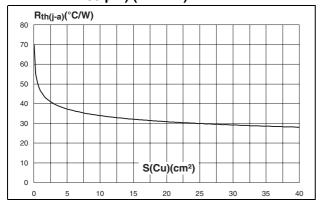


Figure 11. Thermal resistance junction to ambient versus copper surface under tab (Epoxy printed circuit board FR4, copper thickness: 35 µm) (D²PAK)



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STPS10L60C Package information

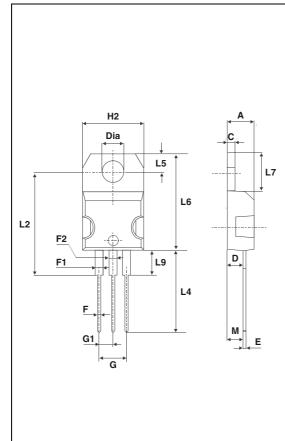
2 Package information

Epoxy meets UL94, V0

Cooling method: by conduction (C)Recommended torque value: 0.55 Nm

Maximum torque value: 0.70 Nm

Table 3. TO-220FPAB dimensions



	Dimensions			
REF.	Millimeters		Inc	nes
	Min.	Max.	Min.	Max.
Α	4.40	4.60	0.173	0.181
В	2.50	2.70	0.098	0.106
D	2.50	2.75	0.098	0.108
E	0.45	0.70	0.018	0.027
F	0.75	1.00	0.030	0.039
F1	1.15	1.70	0.045	0.067
F2	1.15	1.70	0.045	0.067
G	4.95	5.20	0.195	0.205
G1	2.40	2.70	0.094	0.106
Н	10.00	10.40	0.393	0.409
L2	16.00	Тур.	0.630	Тур.
L3	28.60	30.60	1.126	1.205
L4	9.80	10.60	0.386	0.417
L5	2.9	3.6	0.114	0.142
L6	15.90	16.40	0.626	0.646
L7	9.00	9.30	0.354	0.366
Diam	3.00	3.20	0.118	0.126

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Table 4. D²PAK dimensions

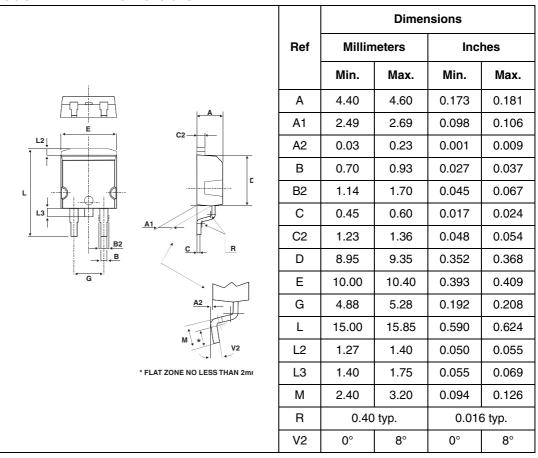
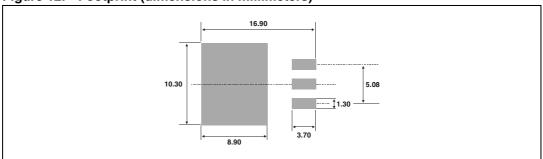


Figure 12. Footprint (dimensions in millimeters)



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

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3 Ordering information

Туре	Marking	Package	Weight	Base qty	Delivery mode
STPS10L60CFP	STPS10L60CFP	TO-220FPAB	2 g	50	Tube
STPS10L60CG	STPS10L60CG	D ² PAK	1.48 g	50	Tube
STPS10L60CG-TR	STPS10L60CG	D ² PAK	1.48 g	1000	Tape and reel

4 Revision history

Date	Revision	Description of Changes
Jul-2003	3C	Last release.
26-Mar-2007	4	Removed ISOWATT package. Added D ² PAK package.

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