

Selection. Service. Support.

Power Solutions from ON Semiconductor



Integrated Solutions for USB Line Protection and Termination

ON Semiconductor provides a selection of integrated solutions for USB line protection and termination. Integrated IC solutions combine diodes with resistive and capacitive elements into a single package, providing:

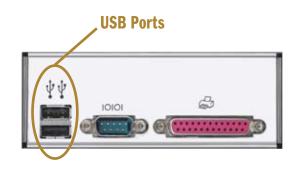
- improved parametric stability
- reduced parasitic inductance
- component count reduction
- board space reduction
- · simplified supply chain
- · lower overall system cost

Related Application Notes (download from www.onsemi.com)

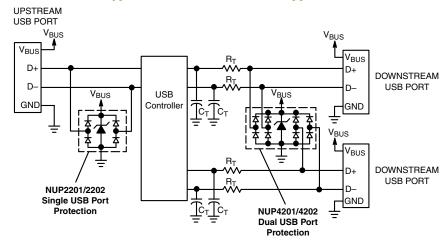
- ESD Protection for Dual USB 2.0 Port Using the Low Capacitance TVS Diode Array, NUP4201DR2 Device download AND8082/D
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Speed of Data Transport

Port	Speed
Serial Port	0.115 Mbit/s
Standard Parallel Port	0.92 Mbit/s
USB 1.1	
Low Speed	1.5 Mbit/s
High Speed	12 Mbit/s
USB 2.0	
Low Speed	1.5 Mbit/s
Full Speed	12 Mbit/s
High Speed	480 Mbit/s



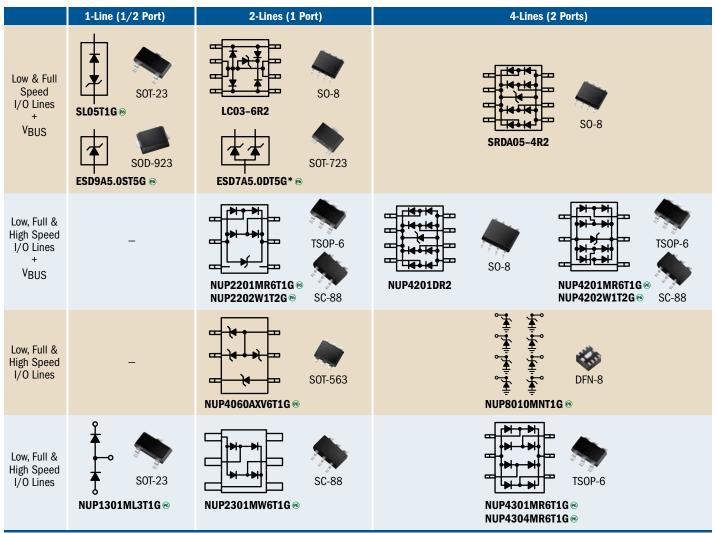
Typical USB Line Protection Application



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USB Line Protection Devices

Integrated protection devices for USB from ON Semiconductor can replace up to 9 discrete devices, minimize board space and simplify board layouts. Our portfolio includes Low-, Full- and High-Speed USB solutions. All of our devices are IEC61000-4-2 compliant.

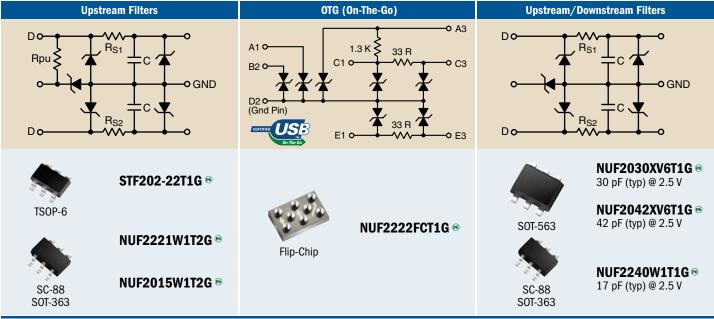


Symbol denotes 2nd level interconnect Pb-Fee (lead) content.
Coming soon.

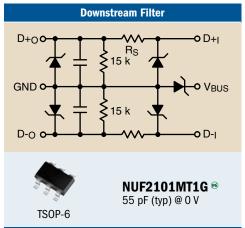
Page 3 **USB Solutions**

USB Filter Devices

Integrated protection and termination devices for USB from ON Semiconductor can replace up to 11 devices by combining TVS diodes with resistive and capacitive elements. They are suitable for Low- and Full-Speed USB solutions. All of our devices are IEC61000-4-2 Level 4 compliant.

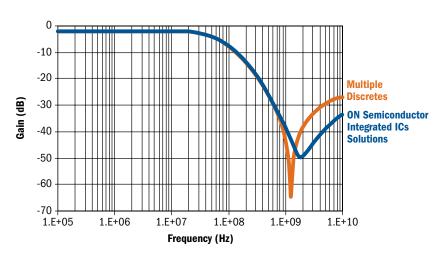


■ Symbol denotes 2nd level interconnect Pb-Fee (lead) content.



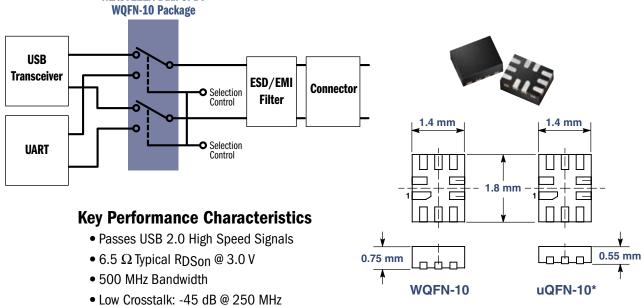
69 = Symbol denotes 2nd level interconnect Pb-Fee (lead) content.

The integrated solution from ON Semiconductor delivers improved insertion losses since there are lower parasitics compared to discrete solutions which use multiple packages.



NLAS7222A Dual SPDT Switch





Device	Configuration	$\operatorname{Ron} \operatorname{Max} \ (\Omega)$	C Typ (pF)	Package
NLAS7222A	Dual SPDT	9	6.5	WQFN-10, uDFN-10*
NLAS4717EP	Dual SPDT	4.5	38	Flip-Chip 10

^{*} Coming Soon

• Smallest Package in the Industry



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