# Ultra-Low 0.4 $\Omega$ SPDT Analog Switch

The NLAS5157 is Single Pole Double Throw (SPDT) switch designed for audio systems in portable applications. The NLAS5157 features Ultra–Low R<sub>ON</sub> of 0.4  $\Omega$  typical at V<sub>CC</sub> = 3.0 V and 0.15  $\Omega$  R<sub>ON</sub> Flatness for +3.0 V supply across temperature. This device also has a broad V<sub>CC</sub> operating range of 1.65 V to 4.5 V, ideal for battery–powered devices.

The NLAS5157 is protected on all pins with 8 kV Human Body Model ESD protection. This allows the device to be placed in a variety of locations, including near the interface, without risk of damage.

#### Features

- $R_{ON} = 0.4 \Omega$  Typical @  $V_{CC} = 3.0 V$
- V<sub>CC</sub> Range: 1.65 V to 4.5 V
- 8 kV Human Body Model ESD on All Pins
- These are Pb-Free Devices

#### **Typical Applications**

- Mobile Phones
- Portable Devices



http://onsemi.com



- A = Specific Device Code\* (Rotated 90°)
- M = Date Code

### **PIN ASSIGNMENTS**



### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NLAS5157MUTCG	UDFN6 (Pb-Free)	3000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## PIN DESCRIPTION

PIN #	Name	Direction	Description
1	IN	Input	Control Input Select Line
2	V <sub>CC</sub>	Power	Voltage Supply
3	COM	I/O	Common Signal Line
4	NC	I/O	Normally Closed Signal Line
5	GND	Power	Ground
6	NO	I/O	Normally Open Signal Line

## **TRUTH TABLE**

Control Input	Function
L	NC Connected to COM
Н	NO Connected to COM

## MAXIMUM RATINGS

Symbol	Pins	Rating	Value	Condition	Unit
V <sub>CC</sub>	V <sub>CC</sub>	Positive DC Supply Voltage	0.5 to +5.5		V
V <sub>IS</sub>	NO, NC, or COM	Analog Signal Voltage	–0.5 to V <sub>CC</sub> + 0.5		V
V <sub>IN</sub>	IN	Control Input Voltage	-0.5 to +5.5		V
I <sub>IS_CON</sub>	NO, NC, or COM	Analog Signal Continuous Current	±300	Closed Switch	mA
I <sub>IS_PK</sub>	NO, NC, or COM	Analog Signal Peak Current	±500	10% Duty Cycle	mA
I <sub>IN</sub>	IN	Control Input Current	±20		mA
T <sub>STG</sub>		Storage Temperature Range	-65 to 150		°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

## **RECOMMENDED OPERATING CONDITIONS**

Symbol	Pins	Parameter	Value	Condition	Unit
V <sub>CC</sub>	V <sub>CC</sub>	Positive DC Supply Voltage	1.65 to 4.5		V
V <sub>IS</sub>	NO, NC, or COM	Analog Signal Voltage	GND to V <sub>CC</sub>		V
V <sub>IN</sub>	IN	Control Input Voltage (OVT) Overvoltage Tolerance	GND to 4.5		V
T <sub>A</sub>		Operating Temperature Range	-40 to +85		°C
t <sub>r</sub> , t <sub>f</sub>		Input Rise or Fall Time	20	$V_{CC} = 1.6 V - 2.7 V$	ns/V
			10	$V_{CC} = 3.0 V - 4.5 V$	

Minimum and maximum values are guaranteed through test or design across the **Recommended Operating Conditions**, where applicable. Typical values are listed for guidance only and are based on the particular conditions listed for each section, where applicable. These conditions are valid for all values found in the characteristics tables unless otherwise specified in the test conditions.

## ESD PROTECTION

Pins	Description	Minimum Voltage
All Pins	Human Body Model	7 kV

## DC ELECTRICAL CHARACTERISTICS

## **CONTROL INPUT** (Typical: T = 25°C; $V_{CC}$ = 3.0 V)

				v <sub>cc</sub>	–40°C to +85°C			
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
V <sub>IH</sub>	IN	Control Input High		1.8 – 2.7 3.0 4.5	1.0 1.3 1.6			V
VIL	IN	Control Input Low		1.8 – 2.7 3.0 4.5			0.4 0.5 0.6	V
I <sub>IN</sub>	IN	Control Input Leakage	$0 \le V_{IN} \le V_{CC}$	4.5		±0.1	±0.5	μA

## SUPPLY CURRENT AND LEAKAGE (Typical: T = $25^{\circ}C$ ; V<sub>CC</sub> = 3.0 V)

				V <sub>cc</sub>	–40°C to +85°C			
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
I <sub>NO/NC</sub> (OFF)	NC, NO	OFF State Leakage		4.5		±10	±100	nA
I <sub>СОМ</sub> (ON)	СОМ	ON State Leakage	$\label{eq:VIN} \begin{array}{l} V_{IN} = V_{IL} \mbox{ or } V_{IH} \\ V_{NO} = 0.3 \mbox{ V or } 4.0 \mbox{ V with} \\ V_{NC} \mbox{ Floating or} \\ V_{NC} = 0.3 \mbox{ V or } 4.0 \mbox{ V with} \\ V_{NO} \mbox{ Floating} \\ V_{COM} = 0.3 \mbox{ V or } 4.0 \mbox{ V} \end{array}$	4.5		±20	±100	nA
I <sub>CC</sub>	V <sub>CC</sub>	Quiescent Supply	$V_{IN}$ and $V_{IS}$ = $V_{CC}$ or GND $I_{D}$ = 0 A	1.65 – 4.5		±0.1	±1.0	μA
I <sub>OFF</sub>	IN	Power Off Leakage	V <sub>IN</sub> = 4.5 V or GND	0		±0.5	±1.0	μA

# **ON RESISTANCE** (Typical: T = $25^{\circ}$ C; V<sub>CC</sub> = 3.0 V)

				v <sub>cc</sub>	–40°C to +85°C			
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
R <sub>ON</sub>	NO, NC COM	ON Resistance	$  I_{ON} = -100 \text{ mA} $ $  V_{IS} = 0 \text{ to } V_{CC} $	3.0 4.5		0.4 0.35	0.6 0.5	Ω
R <sub>FLAT</sub>	NO, NC COM	R <sub>ON</sub> Flatness	$I_{ON} = -100 \text{ mA}$ $V_{IS} = 0 \text{ to } V_{CC}$	3.0 4.5		0.12 0.15	0.16 0.17	Ω
$\Delta R_{ON}$	NO, NC COM	R <sub>ON</sub> Matching	$I_{ON} = -100 \text{ mA}$ $V_{IS} = 0 \text{ to } V_{CC}$	3.0 4.5		0.08 0.08	0.1 0.1	Ω

# AC ELECTRICAL CHARACTERISTICS

				V <sub>cc</sub>	–40°C to +85°C			
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
t <sub>ON</sub>	IN to NC or NO	Turn On Time		2.3 – 4.3		30	40	ns
t <sub>OFF</sub>	IN to NC or NO	Turn Off Time		2.3 – 4.53		18	25	ns
t <sub>BBM</sub>	IN to NC or NO	Break Before Make		3.0	2	15		ns
BW		-3dB Bandwidth	C <sub>L</sub> = 5 pF	1.65 – 4.5		36		MHz

## **TIMING/FREQUENCY** (Typical: T = 25°C; $V_{CC}$ = 3.0 V, $R_L$ = 50 $\Omega$ , $C_L$ = 35 pF, f = 1 MHz)

## **ISOLATION AND THD** (Typical: T = 25°C; $V_{CC}$ = 3.0 V, RL = 50 $\Omega$ , CL = 5 pF, f = 1 MHz)

				V <sub>CC</sub> -40°C to +85°C			ΰ°C	
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
Q		Charge Injection		1.65 – 4.3		38		рС
THD		Total Harmonic Distortion	$\begin{array}{l} {F_{IS}} = 20 \; Hz \; to \; 20 \; kHz \\ {R_L} = {R_{gen}} = 600 \; \Omega, \\ {C_L} = 1.0 \; pF \\ {V_{IS}} = 1.0 \; V_{PP} \end{array}$	3.0		0.02		%
O <sub>IRR</sub>	NO	Off Isolation	$V_{IN} = 0$ $V_{NO}$ or $V_{NC}$ (pk-pk) = 1.0 V	1.65 – 4.5		-54		dB
Xtalk	COM to COMy	Non-Adjacent Channel	$V_{NO} \text{ or } V_{NC} \text{ (pk-pk)} = 1.0 \text{ V}$	1.65 – 4.5		-54		dB

# $\textbf{CAPACITANCE} \text{ (Typical: } T = 25^{\circ}\text{C}\text{; } \text{V}_{CC} = \textbf{3.0 V}\text{, } \text{R}_{L} = \textbf{50} \ \Omega\text{, } \text{C}_{L} = \textbf{5} \text{ pF}\text{, } \textbf{f} = \textbf{1} \text{ MHz}\text{)}$

				v <sub>cc</sub>	-40	0°C to +85	ΰ°C	
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
C <sub>IN</sub>	IN	Control Input		0 V		3.5		pF
C <sub>ON</sub>	NC to COM	Through Switch	V <sub>IN</sub> = 0V	3.0 V		95		pF
C <sub>OFF</sub>	NC, NC	Unselected Port	$V_{IS}$ = 3.0V, $V_{IN}$ = 3.0 V	3.0 V		47		pF

## **TYPICAL CHARACTERISTICS**











Figure 7. t<sub>ON</sub>/t<sub>OFF</sub>







Channel switch control/s test socket is normalized. Off isolation is measured across an off channel. On loss is the bandwidth of an On switch.  $V_{ISO}$ , Bandwidth and  $V_{ONL}$  are independent of the input signal direction.

$$\begin{split} V_{ISO} &= \text{Off Channel Isolation} = 20 \text{ Log } \left( \frac{V_{OUT}}{V_{IN}} \right) \text{ for } V_{IN} \text{ at } 100 \text{ kHz} \\ V_{ONL} &= \text{On Channel Loss} = 20 \text{ Log } \left( \frac{V_{OUT}}{V_{IN}} \right) \text{ for } V_{IN} \text{ at } 100 \text{ kHz to } 50 \text{ MHz} \end{split}$$

Bandwidth (BW) = the frequency 3 dB below V<sub>ONL</sub> V<sub>CT</sub> = Use V<sub>ISO</sub> setup and test to all other switch analog input/outputs terminated with 50  $\Omega$ 





Figure 10. Charge Injection: (Q)

#### PACKAGE DIMENSIONS



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