

# The RF Line NPN Silicon High-Frequency Transistors

Designed for low noise, wide dynamic range front-end amplifiers and low-noise VCO's. Available in a surface-mountable plastic packages. This Motorola series of small-signal plastic transistors offers superior quality and performance at low cost.

- High Gain-Bandwidth Product  
 $f_T = 8.0 \text{ GHz (Typ) } @ 50 \text{ mA}$
- Low Noise Figure  
 $NF_{\min} = 1.6 \text{ dB (Typ) } @ f = 1.0 \text{ GHz (MRF5711LT1, MRF571)}$
- High Gain  
 $GNF = 17 \text{ dB (Typ) } @ 30 \text{ mA}/500 \text{ MHz (MMBR571LT1)}$
- High Power Gain  
 $G_{pe} (\text{matched}) = 13.5 \text{ dB (Typ) (MRF5711LT1)}$
- State-of-the-Art Technology
  - Fine Line Geometry
  - Ion-Implanted Arsenic Emitters
  - Gold Top Metallization and Wires
  - Silicon Nitride Passivation
- Available in tape and reel packaging options:  
T1 suffix = 3,000 units per reel

## MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CEO}$	10	Vdc
Collector-Base Voltage	$V_{CBO}$	20	Vdc
Emitter-Base Voltage	$V_{EBO}$	3.0	Vdc
Collector Current — Continuous	$I_C$	80	mA
Total Device Dissipation @ $T_{case} = 75^\circ\text{C}$ MMBR571LT1, MRF5711LT1 Derate linearly above $T_{case} = 75^\circ\text{C}$ @	$P_D(\max)$	0.33 4.44	W mW/ $^\circ\text{C}$
Total Device Dissipation (1) @ $T_C = 75^\circ\text{C}$ Derate above $75^\circ\text{C}$ MRF571	$P_D$	0.58 7.73	Watts mW/ $^\circ\text{C}$
Operating and Storage Temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

## THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit
Thermal Resistance, Junction to Case MRF5711LT1, MMBR571LT1	$R_{\theta JC}$	225	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case MRF571	$R_{\theta JC}$	130	$^\circ\text{C/W}$
Maximum Junction Temperature	$T_{Jmax}$	150	$^\circ\text{C}$

## DEVICE MARKING

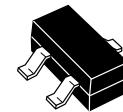
MMBR571LT1 = 7X      MRF5711LT1 = 02

### NOTE:

1. Case temperature measured on collector lead immediately adjacent to body of package.

# MMBR571LT1 MRF571 MRF5711LT1

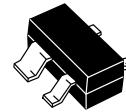
$I_C = 80 \text{ mA}$   
LOW NOISE  
HIGH-FREQUENCY  
TRANSISTORS



CASE 318-08, STYLE 6  
SOT-23  
LOW PROFILE  
MMBR571LT1



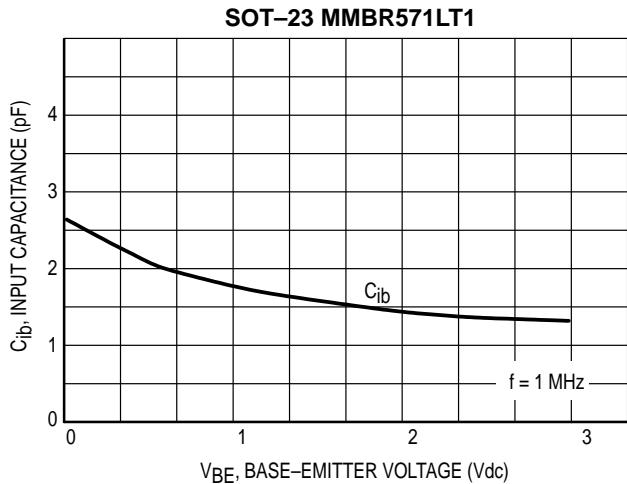
CASE 317-01, STYLE 2  
MACRO-X  
MRF571



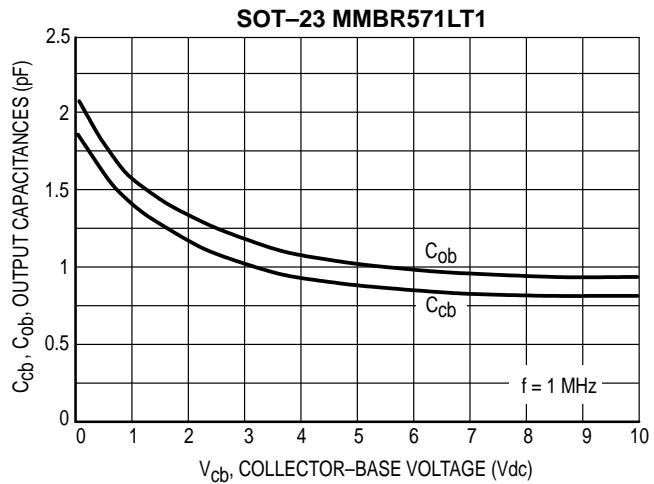
CASE 318A-05, STYLE 1  
SOT-143  
LOW PROFILE  
MRF5711LT1



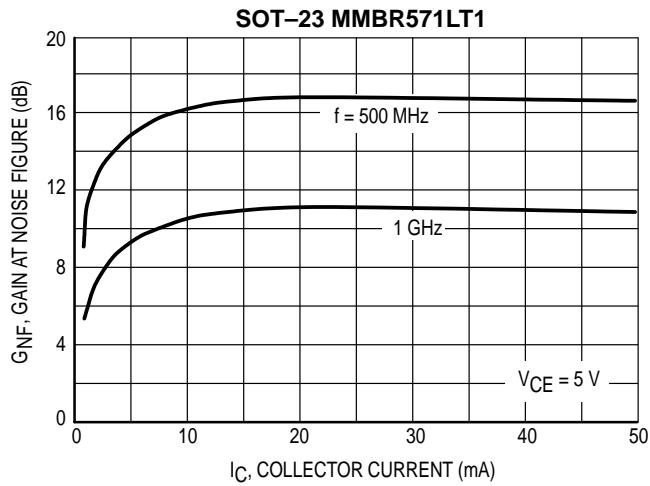
**TYPICAL CHARACTERISTICS**  
**MMBR571LT1**



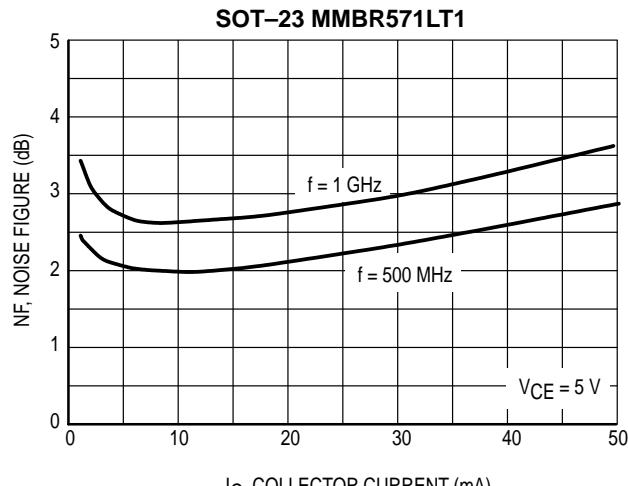
**Figure 3. Input Capacitance versus Emitter Base Voltage**



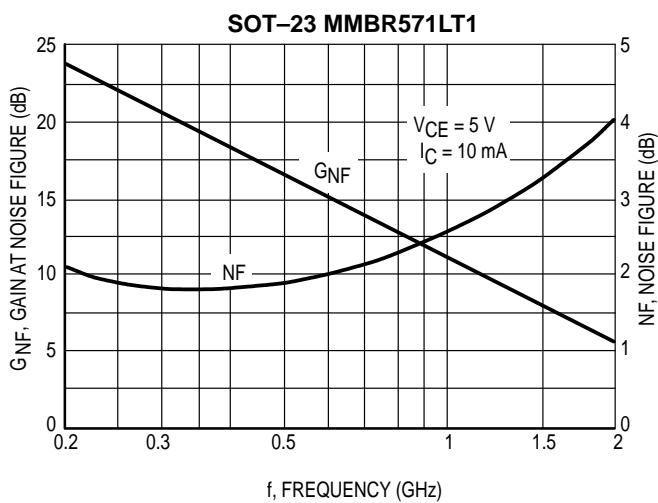
**Figure 4. Output Capacitances versus Collector-Base Voltage**



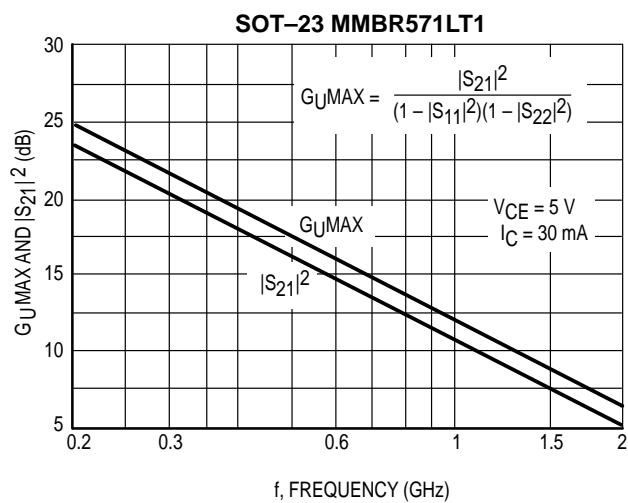
**Figure 5. Gain at Noise Figure versus Collector Current**



**Figure 6. Noise Figure versus Collector Current**

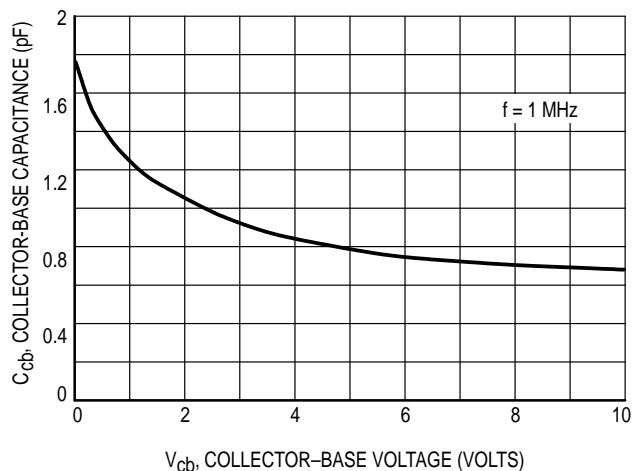


**Figure 7. Gain at Noise Figure and Noise Figure versus Frequency**

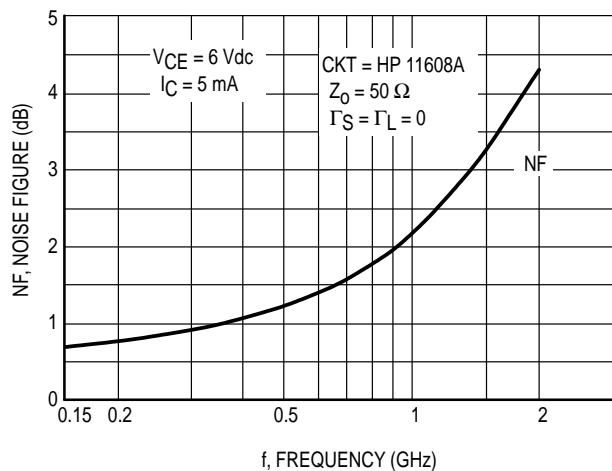


**Figure 8. Maximum Unilateral Gain and Insertion Gain versus Frequency**

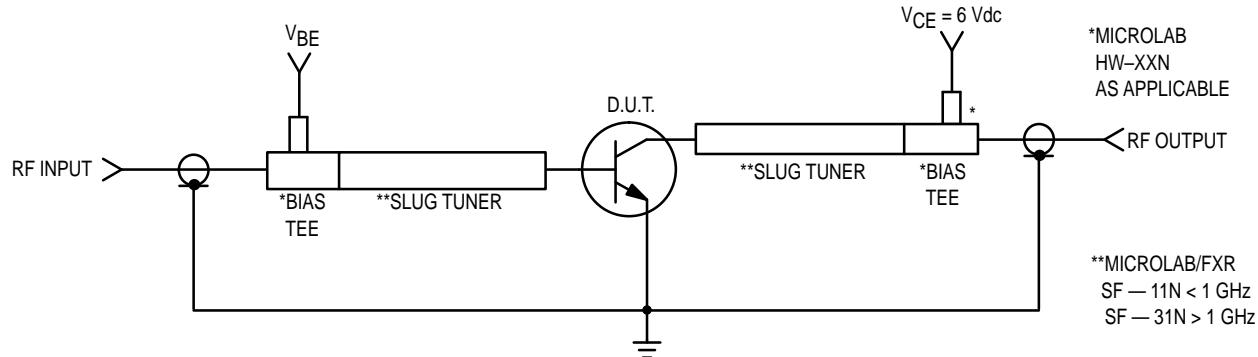
**TYPICAL CHARACTERISTICS**  
**MRF5711LT1**



**Figure 9. Collector-Base Capacitance  
versus Collector-Base Voltage**

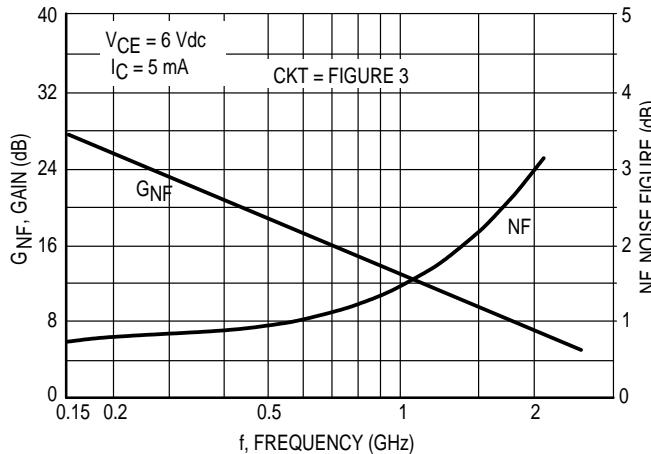


**Figure 10. 50 Ω Noise Figure  
versus Frequency**

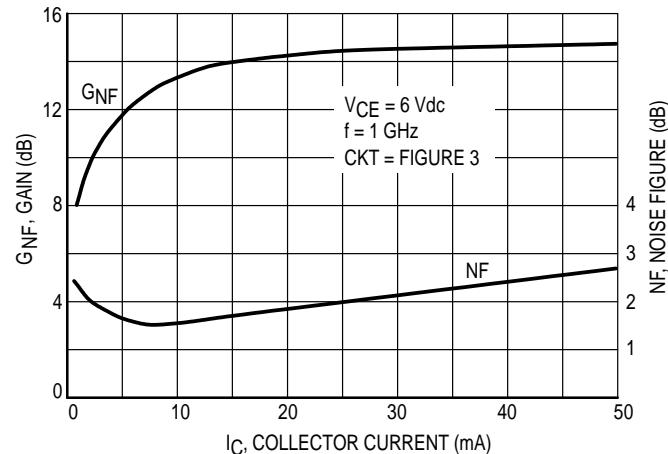


**Figure 11. Functional Circuit Schematic**

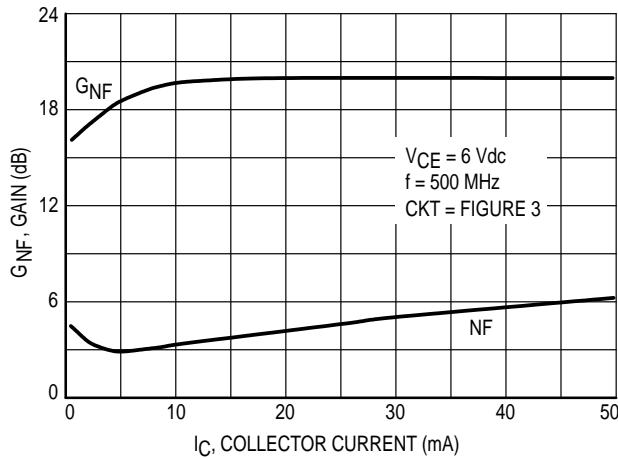
**TYPICAL CHARACTERISTICS**  
**MRF5711LT1**



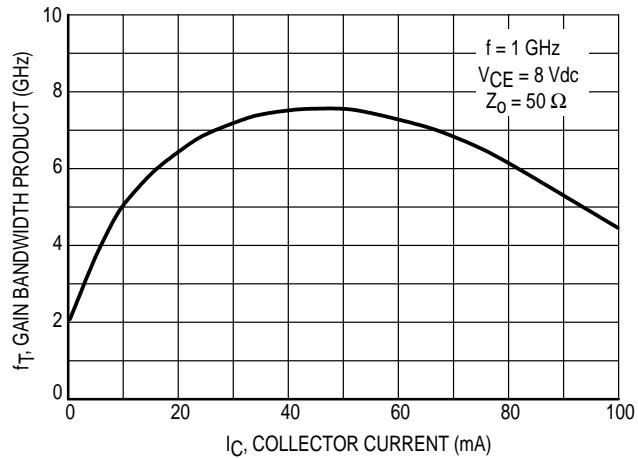
**Figure 12. Gain and Noise Figure versus Frequency**



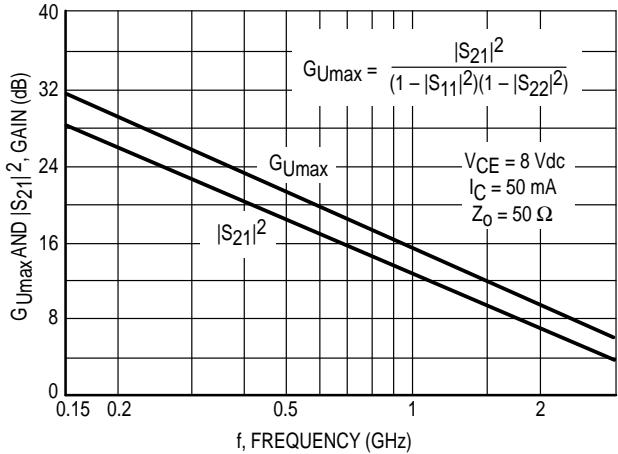
**Figure 13. Gain and Noise Figure versus Collector Current**



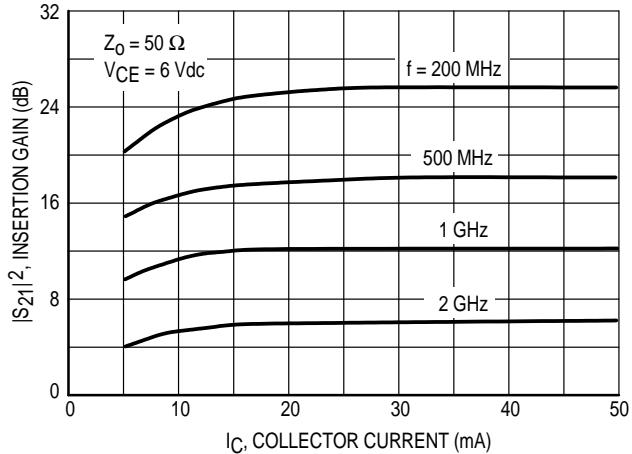
**Figure 14. Gain and Noise Figure versus Collector Current**



**Figure 15. Gain Bandwidth Product versus Collector Current**

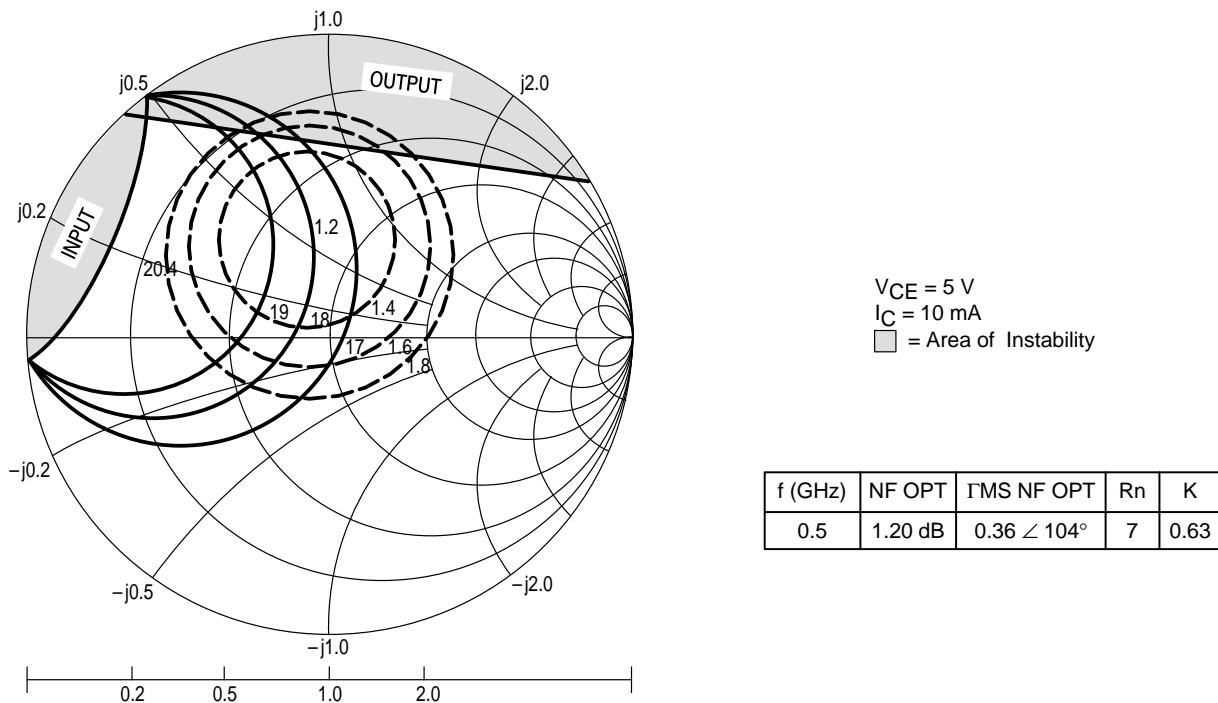


**Figure 16. GUmax and |S21|^2 versus Frequency**

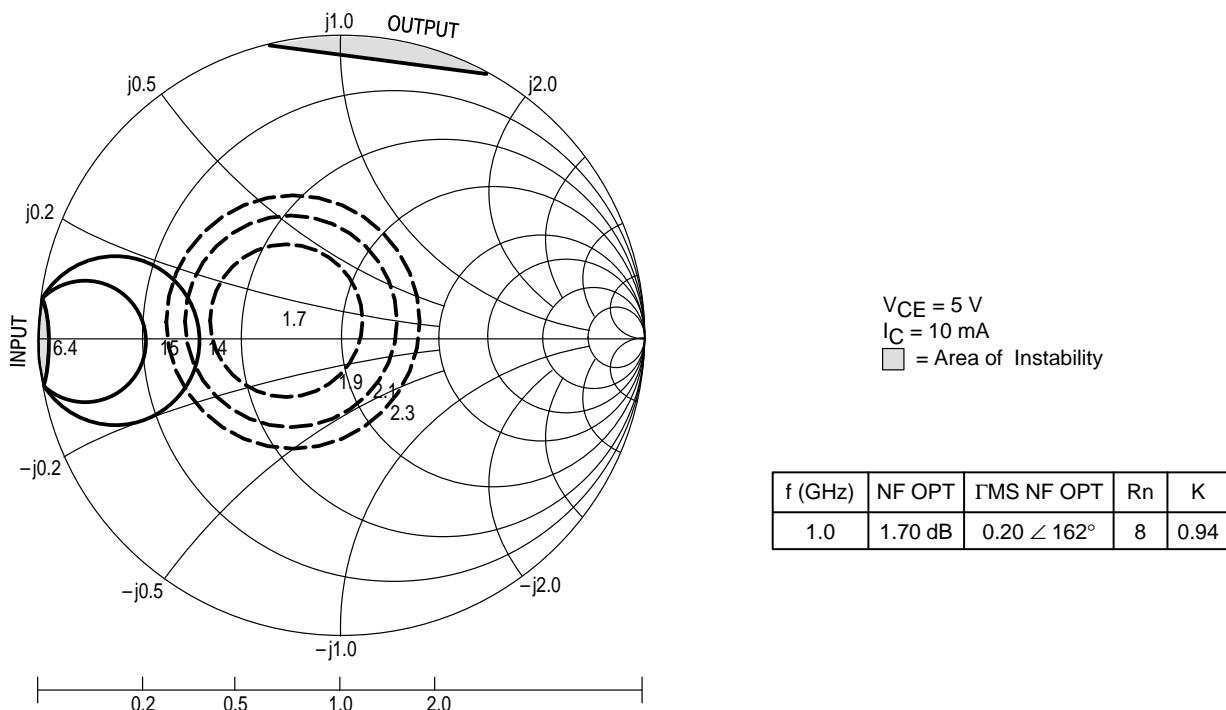


**Figure 17. Insertion Gain versus Collector Current**





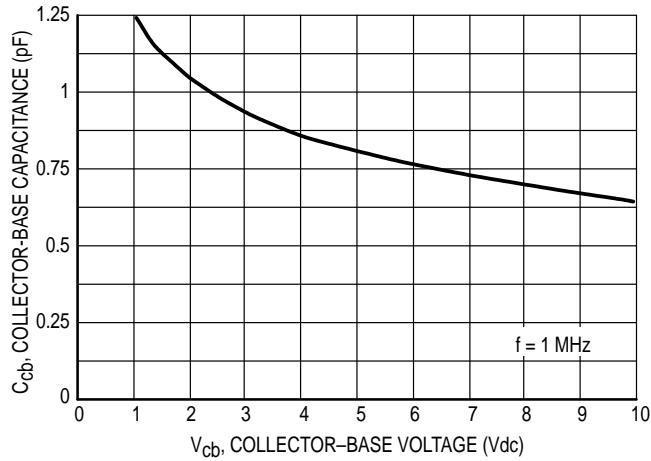
**Figure 20. MRF5711LT1 Constant Gain and Noise Figure Contours  
(f = 0.5 GHz)**



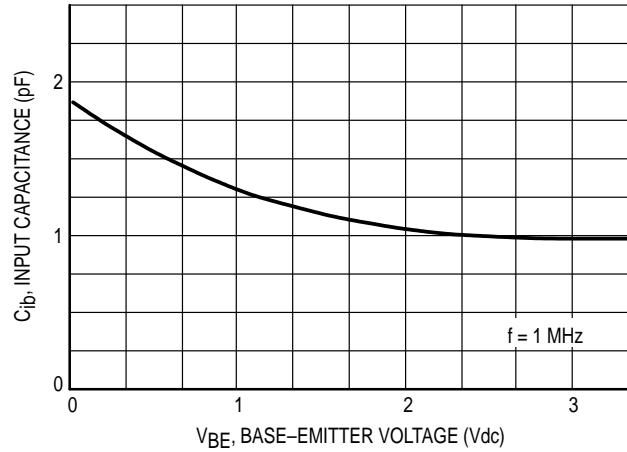
**Figure 21. MRF5711LT1 Constant Gain and noise Figure Contours  
(f = 1.0 GHz)**



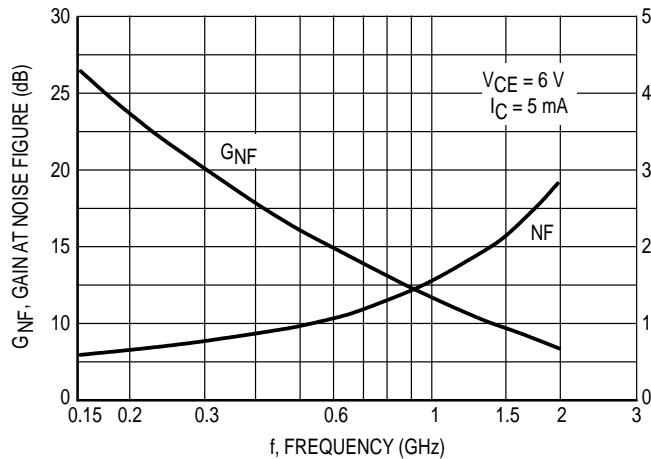
**TYPICAL CHARACTERISTICS**  
**MRF571**



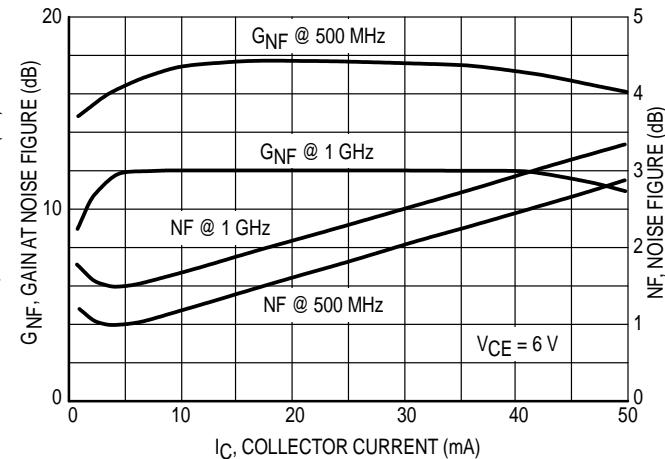
**Figure 22.  $C_{cb}$ , Collector-Base Capacitance versus Voltage**



**Figure 23.  $C_{jb}$ , Input Capacitance versus Emitter Base Voltage**

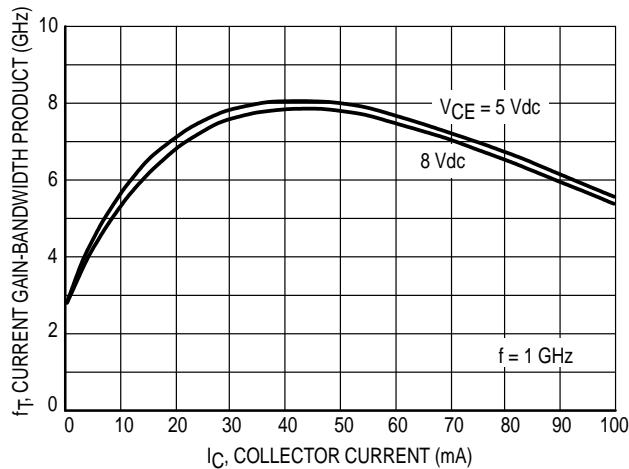


**Figure 24. Gain at Noise Figure and Noise Figure versus Frequency**

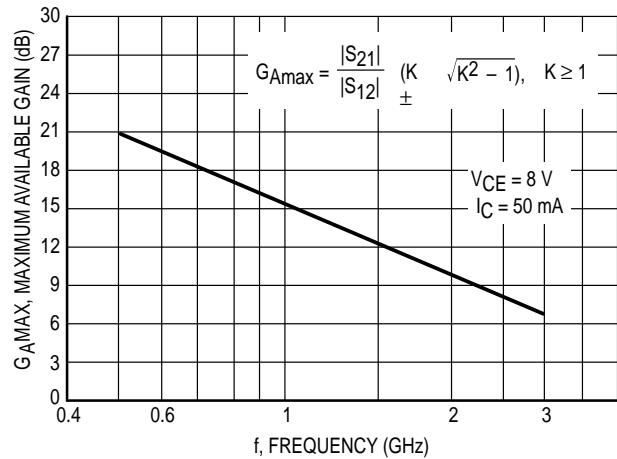


**Figure 25. Gain at Noise Figure and Noise Figure versus Collector Current**

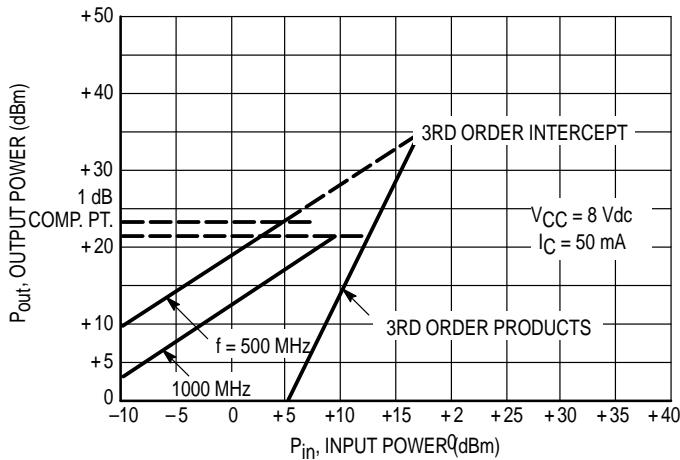
## TYPICAL CHARACTERISTICS MRF571



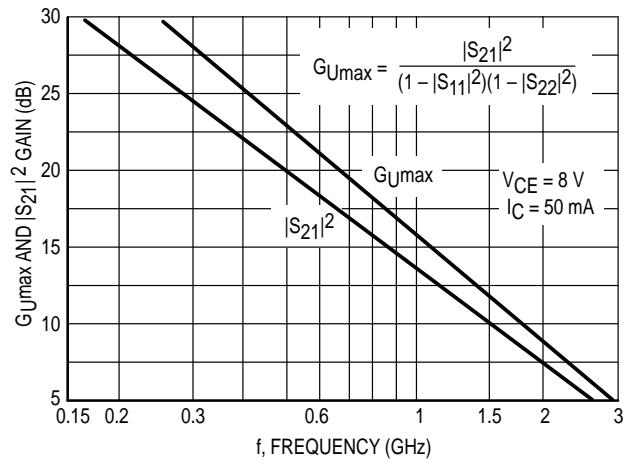
**Figure 26.**  $f_T$ , Current Gain–Bandwidth Product versus Collector Current



**Figure 27.**  $G_{A\max}$ , Maximum Available Gain versus Frequency

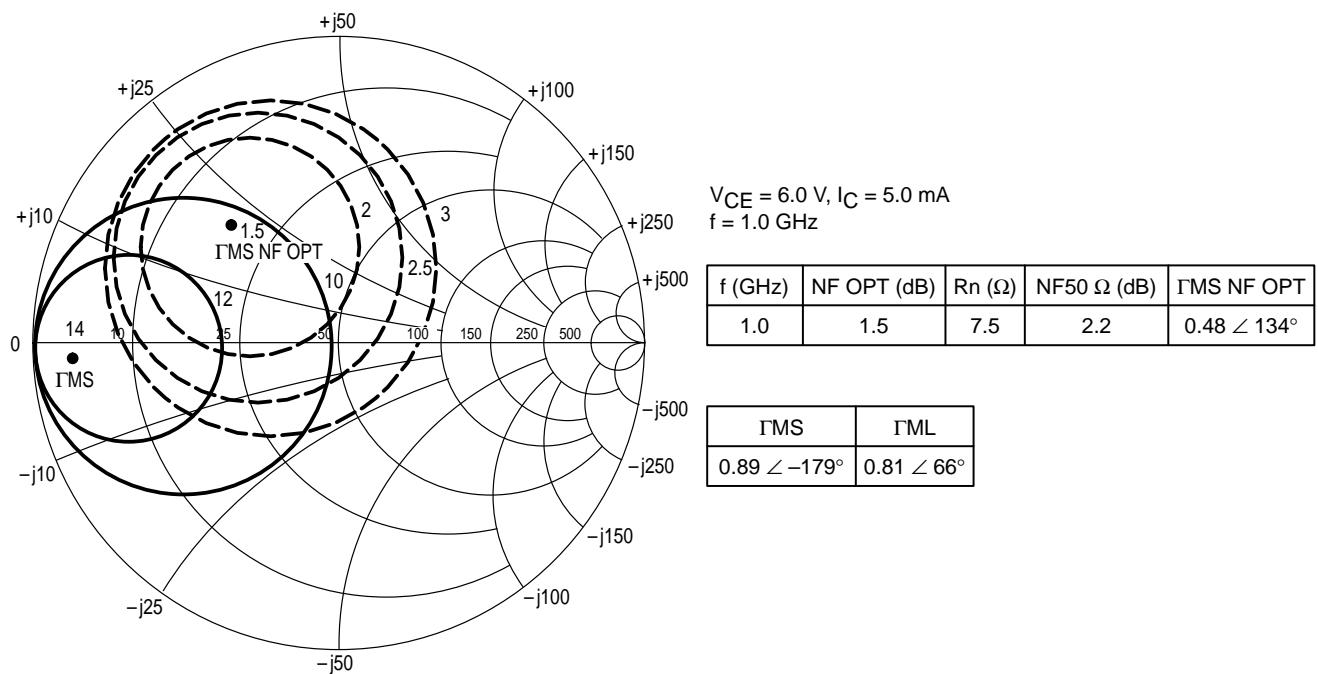
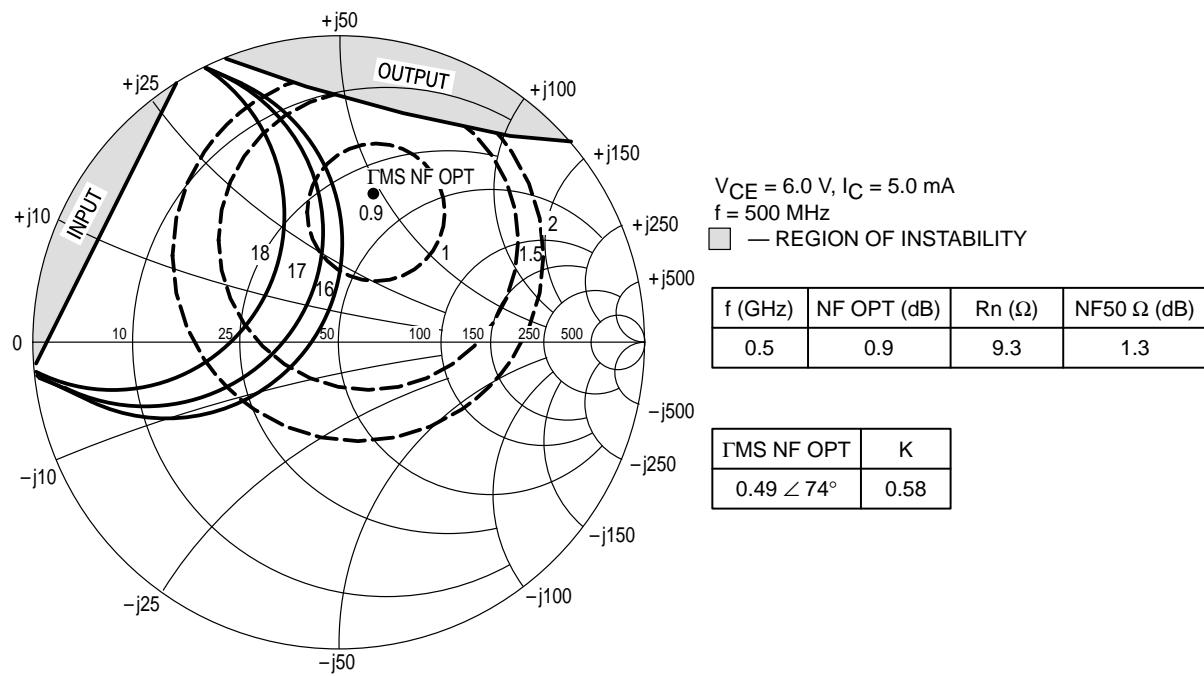


**Figure 28.** 1.0 dB Compression Point and Third Order Intercept

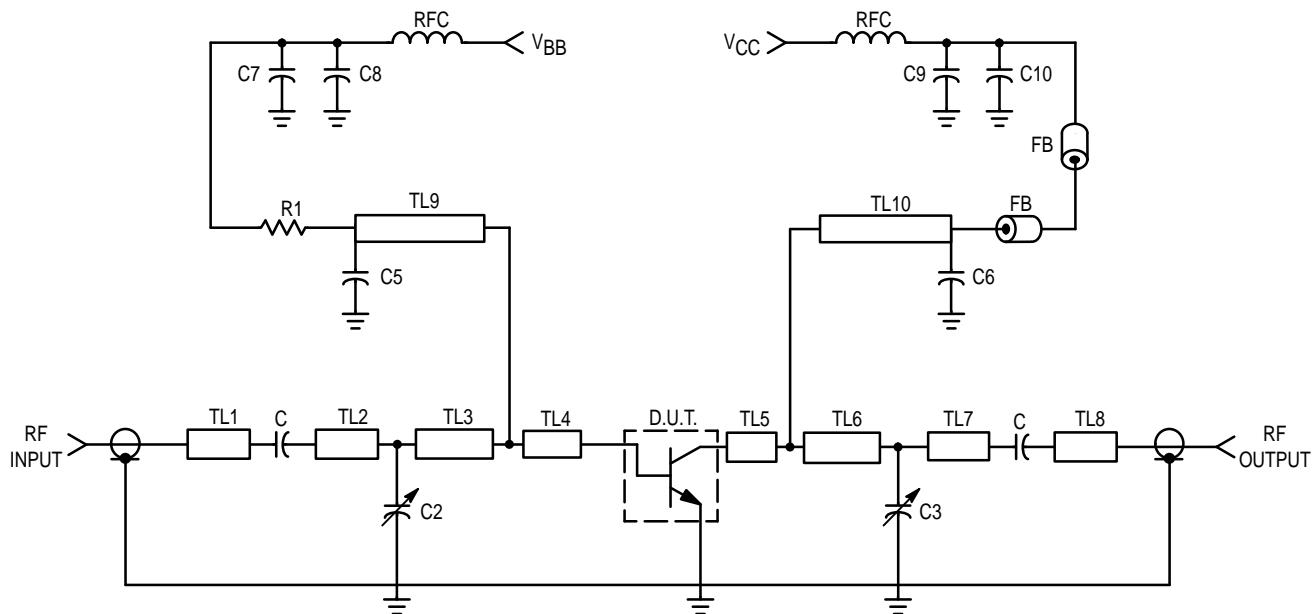


**Figure 29.**  $G_{U\max}$  and  $|S_{21}|^2$  versus Frequency





**Figure 32. MRF571 Constant Gain and Noise Figure Contours**

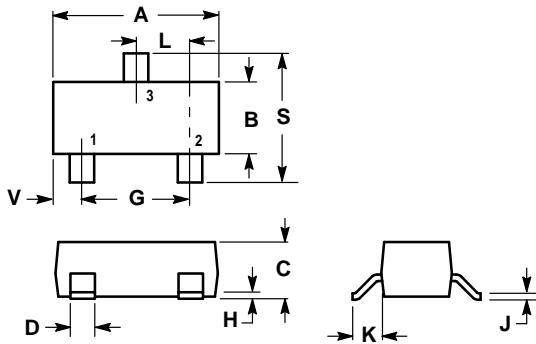


C1, C4, C5, C6, C8, C9 — 100 pF Chip Capacitor  
 C2, C3 — 0.8–8.0 pF Johanson Capacitor  
 C7, C10 — 10  $\mu$ F Tantalum Capacitor  
 R1 — 1.0 kOhms Res.  
 RFC — VK-200, Ferroxcube  
 FB — Ferrite Bead, Ferroxcube 56-590-65/3B  
 Board Material — 0.0625" Glass Teflon,  $\epsilon_r = 2.55$

TL1, TL7, TL8 — Microstrip 0.162" x 0.600"  
 TL2 — Microstrip 0.162" x 1.060"  
 TL3 — Microstrip 0.162" x 0.700"  
 TL4, TL5 — Microstrip 0.162" x 0.440"  
 TL6 — Microstrip 0.162" x 1.140"  
 TL8, TL9 — Microstrip 0.020" x 2.130"

**Figure 33. MRF571 Test Circuit Schematic**

## PACKAGE DIMENSIONS

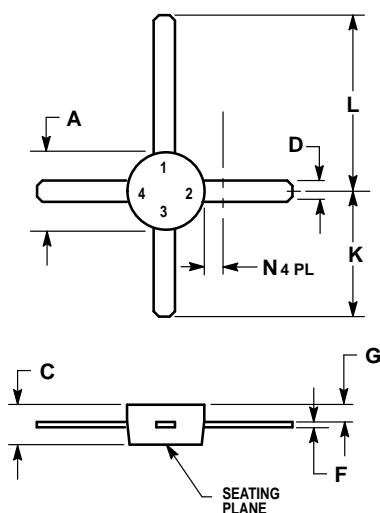


NOTES:  
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.  
 2. CONTROLLING DIMENSION: INCH.  
 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.1102	0.1197	2.80	3.04
B	0.0472	0.0551	1.20	1.40
C	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
H	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
V	0.0177	0.0236	0.45	0.60

STYLE 6:  
 PIN 1. BASE  
 2. Emitter  
 3. Collector

**CASE 318-08**  
**ISSUE AF**  
**MMBR571LT1**

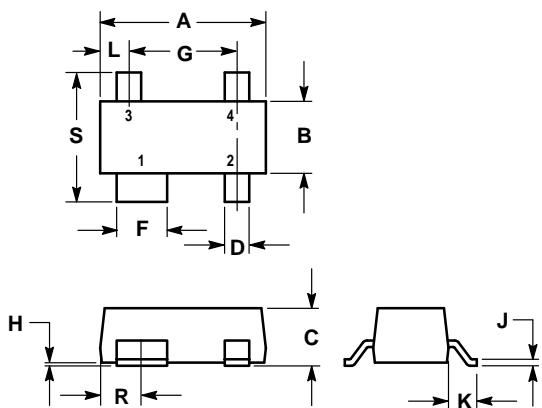


NOTES:  
 1. DIMENSION D NOT APPLICABLE IN ZONE N.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.44	5.21	0.175	0.205
C	1.90	2.54	0.075	0.100
D	0.84	0.99	0.033	0.039
F	0.20	0.30	0.080	0.012
G	0.76	1.14	0.030	0.045
K	7.24	8.13	0.285	0.320
L	10.54	11.43	0.415	0.450
N	—	1.65	—	0.065

STYLE 2:  
 PIN 1. COLLECTOR  
 2. Emitter  
 3. BASE  
 4. Emitter

**CASE 317-01**  
**ISSUE E**  
**MRF571**



NOTES:

4. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
5. CONTROLLING DIMENSION: MILLIMETER.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.80	3.04	0.110	0.120
B	1.20	1.39	0.047	0.055
C	0.84	1.14	0.033	0.045
D	0.39	0.50	0.015	0.020
F	0.79	0.93	0.031	0.037
G	1.78	2.03	0.070	0.080
H	0.013	0.10	0.0005	0.004
J	0.08	0.15	0.003	0.006
K	0.46	0.60	0.018	0.024
L	0.445	0.60	0.0175	0.024
R	0.72	0.83	0.028	0.033
S	2.11	2.48	0.083	0.098

STYLE 1:

- PIN 1. COLLECTOR
2. Emitter
3. Emitter
4. BASE

**CASE 318A-05**  
**ISSUE R**  
**MRF571LT1**

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