# **SWITCHMODE™ Power Rectifier**

# **Dual Schottky Rectifier**

This device uses the Schottky Barrier technology with a platinum barrier metal. This state–of–the–art device is designed for use in high frequency switching power supplies and converters with up to 48 V outputs. They block up to 200 V and offer improved Schottky performance at frequencies from 250 kHz to 5.0 MHz.

#### **Features**

- 200 V Blocking Voltage
- Low Forward Voltage Drop
- Guardring for Stress Protection and High dv/dt Capability (10,000 V/μs)
- Dual Diode Construction Terminals 1 and 3 Must be Connected for Parallel Operation at Full Rating
- Pb-Free Packages are Available

#### **Mechanical Characteristics:**

- Case: Epoxy, Molded, Epoxy Meets UL 94 V-0
- Weight: 1.7 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Device Meets MSL1 Requirements

#### MAXIMUM RATINGS (Per Leg)

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	200	V
Average Rectified Forward Current (At Rated V <sub>R</sub> , T <sub>C</sub> = 134°C)  Per Leg	I <sub>F(AV)</sub>	10	Α
Per Device		20	
Peak Repetitive Forward Current (At Rated $V_R$ , Square Wave, 20 kHz, $T_C = +137^{\circ}C$ ) Per Leg	I <sub>FRM</sub>	20	A
Nonrepetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I <sub>FSM</sub>	150	Α
Peak Repetitive Reverse Surge Current (2.0 μs, 1.0 kHz)	I <sub>RRM</sub>	1.0	Α
Storage Temperature Range	T <sub>stg</sub>	-65 to +175	°C
Operating Junction Temperature	$T_J$	-65 to +150	°C
Voltage Rate of Change (Rated V <sub>R</sub> )	dv/dt	10,000	V/μs

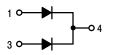
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

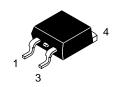


### ON Semiconductor®

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# SCHOTTKY BARRIER RECTIFIER 20 AMPERES, 200 V





D<sup>2</sup>PAK CASE 418B PLASTIC

#### **MARKING DIAGRAM**



A = Assembly Location

Y = Year

WW = Work Week

B20200 = Device Code

G = Pb-Free Package

AKA = Diode Polarity

#### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

# THERMAL CHARACTERISTICS (Per Leg)

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	2.0	°C/W

# **ELECTRICAL CHARACTERISTICS** (Per Leg)

Characteristic	Symbol	Value	Unit
Maximum Instantaneous Forward Voltage (Note 1) $ \begin{aligned} &(I_F=10~A,~T_C=25^\circ\text{C})\\ &(I_F=10~A,~T_C=125^\circ\text{C})\\ &(I_F=20~A,~T_C=25^\circ\text{C})\\ &(I_F=20~A,~T_C=125^\circ\text{C}) \end{aligned} $	V <sub>F</sub>	0.9 0.8 1.0 0.9	V
Maximum Instantaneous Reverse Current (Note 1) (Rated dc Voltage, $T_C = 25^{\circ}C$ ) (Rated dc Voltage, $T_C = 125^{\circ}C$ )	I <sub>R</sub>	1.0 50	mA

# **DYNAMIC CHARACTERISTICS** (Per Leg)

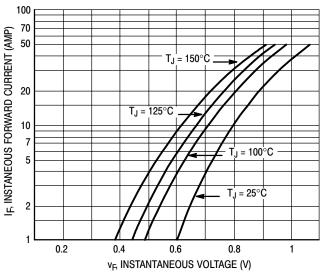
Capacitance ( $V_R = -5.0 \text{ V}$ , $T_C = 25^{\circ}\text{C}$ , Frequency = 1.0 MHz)	C <sub>T</sub>	500	pF

<sup>1.</sup> Pulse Test: Pulse Width = 300  $\mu$ s, Duty Cycle  $\leq$  2.0%.

# **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MBRB20200CT	D <sup>2</sup> PAK	50 Units / Rail
MBRB20200CTG	D <sup>2</sup> PAK (Pb-Free)	50 Units / Rail
MBRB20200CTT4	D <sup>2</sup> PAK	800 Units / Tape & Reel
MBRB20200CTT4G	D <sup>2</sup> PAK (Pb-Free)	800 Units / Tape & Reel

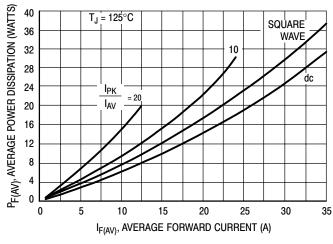
<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



10,000  $T_{J} = 150^{\circ}C$ 1,000 3, REVERSE CURRENT (μ A)
1 00 001 T<sub>J</sub> = 125°C Г<sub>.I</sub> = 100°С <u>~</u> 0.1  $T_J = 25^{\circ}C$ 0.01 20 0 40 120 160 180 200 V<sub>R</sub>, REVERSE CURRENT (V)

Figure 1. Typical Forward Voltage (Per Leg)

Figure 2. Typical Reverse Current (Per Leg)



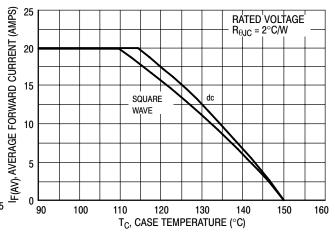
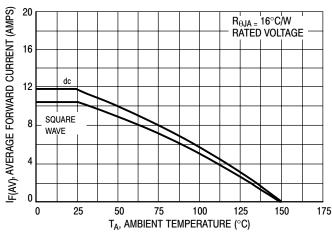


Figure 3. Forward Power Dissipation

Figure 4. Current Derating, Case



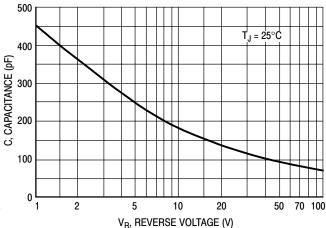
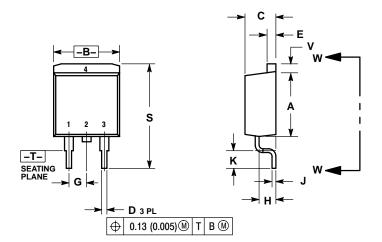


Figure 5. Current Derating, Ambient

Figure 6. Typical Capacitance (Per Leg)

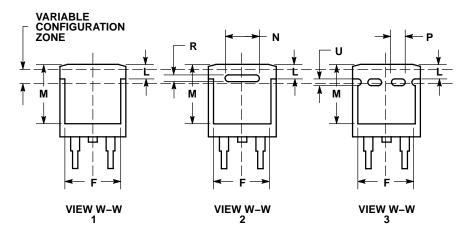
# **PACKAGE DIMENSIONS**

### D<sup>2</sup>PAK 3 CASE 418B-04 ISSUE J

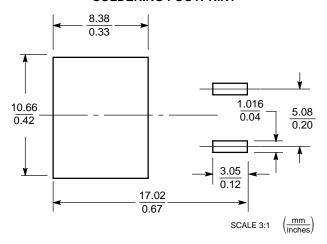


- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. 418B-01 THRU 418B-03 OBSOLETE, NEW STANDARD 418B-04.

	INC	HES	MILLIM	IETERS
ДΙΜ	MIN MAX		MIN MAX	
DIN	IVIIIV			
Α	0.340	0.380	8.64	9.65
В	0.380	0.405	9.65	10.29
С	0.160	0.190	4.06	4.83
D	0.020	0.035	0.51	0.89
E	0.045	0.055	1.14	1.40
F	0.310	0.350	7.87	8.89
G	0.100 BSC		2.54 BSC	
Н	0.080	0.110	2.03	2.79
J	0.018	0.025	0.46	0.64
K	0.090	0.110	2.29	2.79
L	0.052	0.072	1.32	1.83
М	0.280	0.320	7.11	8.13
N	0.197 REF		5.00 REF	
Р	0.079 REF		2.00 REF	
R	0.039	REF	0.99 REF	
S	0.575	0.625	14.60	15.88
V	0.045	0.055	1.14	1.40



# **SOLDERING FOOTPRINT\***



<sup>\*</sup>For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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