#### General Description

The MAX3370/MAX3371 logic level translators are ideal for applications interfacing low-voltage devices to other logic levels. Externally applied voltages set the logic levels of the MAX3370/MAX3371. The devices accept  $V_{CC}$  from +2.5V to +5.5V and  $V_L$  from +1.6V to +5.5V, allowing data transfer between low-voltage ASICs and higher voltage devices. The MAX3371 features a shutdown mode that reduces supply current to <1µA and puts the I/O pins in a high-impedance state.

The MAX3370/MAX3371 are bidirectional level shifters, allowing data transfer from the V<sub>CC</sub> side to the V<sub>L</sub> side and from the V<sub>L</sub> side to the V<sub>CC</sub> side. Both devices operate at speeds up to 2Mbps with an active driver and up to 500kbps with an open-drain driver.

The MAX3370/MAX3371 are available in space-saving 5-pin and 6-pin SC70 packages, respectively.

#### **Applications**

Cell Phone Cradles

Cell Phone Hands-Free Kits

Portable POS Systems

Portable Communication Devices

Smart Card Readers

SPI™, MICROWIRE™, and I<sup>2</sup>C™ Level Translation

Low-Voltage ASIC Level Translation

RS-232-Compatible Translation

 $I^2C$  is a trademark of Philips Corp.

SPI is a trademark of Motorola, Inc.

MICROWIRE is a trademark of National Semiconductor Corp.



\*MAX3370/MAX3371 covered by U.S. Patent number 5,894,240.

#### 

For price, delivery, and to place orders, please contact Maxim Distribution at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

#### **Features**

- Allow Bidirectional Level Translation
- Miniature SC70 Packages
- Operational Down to 1.6V
- Low Quiescent Current (<100µA)</li>
- Ultra-Low (<1µA) Shutdown Supply Current (MAX3371)
- Three-State Outputs in Shutdown (MAX3371)
- + 2Mbps (10pF Load) Push-Pull Driving
- + 1Mbps (50pF Load) Push-Pull Driving
- 500kbps (30pF Load) Open-Drain Driving

#### \_Ordering Information

| PART         | TEMP.<br>RANGE | PIN-<br>PACKAGE | SHDN | TOP<br>MARK |  |
|--------------|----------------|-----------------|------|-------------|--|
| MAX3370EXK-T | -40°C to +85°C | 5 SC70-5        | NO   | ABV         |  |
| MAX3371EXT-T | -40°C to +85°C | 6 SC70-6        | YES  | AAO         |  |

#### Pin Configurations



#### Maxim Integrated Products 1

#### **ABSOLUTE MAXIMUM RATINGS**

| V <sub>CC</sub> to GND                 | 0.3V to +7V                      |
|--|----------------------------------|
| SHDN to GND                            | 0.3V to (VI + 0.3V)              |
| I/O V <sub>L</sub> to GND              |                                  |
| VL, I/O V <sub>CC</sub> to GND         | 0.3V to (V <sub>CC</sub> + 0.3V) |
| Short-Circuit Duration: I/O VL, I/O VC | CC to GNDContinuous              |

Continuous Power Dissipation ( $T_A = +70^{\circ}C$ )

| 5/6-Pin SC70 (derate 3.1mW/°C above +70 | )°C)245mW      |
|---|----------------|
| Operating Temperature Range             | 40°C to +85°C  |
| Storage Temperature Range               | 65°C to +150°C |
| Lead Temperature (soldering, 10s)       | +300°C         |

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### ELECTRICAL CHARACTERISTICS

(V<sub>CC</sub> = +2.5V to +5.5V, V<sub>L</sub> = +1.6V to +5.5V (Note 1), GND = 0; I/O V<sub>L</sub>, I/O V<sub>CC</sub> unconnected;  $T_A = -40^{\circ}$ C to +85°C, unless otherwise noted. Typical values are at  $T_A = +25^{\circ}$ C.) (Note 2)

| PARAMETER   | SYMBOL           | CONDITIONS   | MIN                      | TYP  | MAX                   | UNITS |
|---|------------------|--|--------------------------|------|-----------------------|-------|
| POWER SUPPLIES                                      |                  |  | •                        |      |                       |       |
| V <sub>L</sub> Supply Range                         | VL               | (Note 1)   | 1.6                      |      | 5.5                   | V     |
| V <sub>CC</sub> Supply Range                        | V <sub>CC</sub>  |  | 2.5                      |      | 5.5                   | V     |
| Supply Current from V <sub>CC</sub>                 | IQVCC            |  |                          | 70   | 100                   | μΑ    |
| Supply Current from $V_L$                           | IQVL             |  |                          | 5    | 100                   | μA    |
| V <sub>CC</sub> Shutdown Supply Current             |                  | $\overline{\text{SHDN}} = \text{GND}, \text{T}_{\text{A}} = +25^{\circ}\text{C}, \text{MAX3371}$       |                          | 0.03 | 1                     | μΑ    |
| V <sub>L</sub> Shutdown Supply Current              |                  | $\overline{\text{SHDN}} = \text{GND}, \text{T}_{\text{A}} = +25^{\circ}\text{C}, \text{MAX3371}$       |                          | 0.03 | 1                     | μΑ    |
| Three-State Output Leakage<br>Current               |                  | I/O V <sub>L</sub> , I/O V <sub>CC</sub> ; <del>SHDN</del> = GND, T <sub>A</sub> = +25°C,<br>MAX3371   |                          | 0.02 | 1                     | μA    |
| LOGIC LEVEL THRESHOLDS                              |                  |  |                          |      |                       |       |
| I/O V <sub>L</sub> Input Voltage High<br>Threshold  | VIHL             |  |                          |      | V <sub>L</sub> - 0.2  | V     |
| I/O V <sub>L</sub> Input Voltage Low<br>Threshold   | VILL             |  | 0.15                     |      |                       | V     |
| I/O V <sub>CC</sub> Input Voltage High<br>Threshold | V <sub>IHC</sub> |  |                          |      | V <sub>CC</sub> - 0.4 | V     |
| I/O V <sub>CC</sub> Input Voltage Low<br>Threshold  | VILC             |  | 0.2                      |      |                       | V     |
| I/O V <sub>L</sub> Output Voltage High              | Vohl             | I/O V <sub>L</sub> sink current = $20\mu$ A,<br>I/O V <sub>CC</sub> ≥ V <sub>CC</sub> - 0.4V (Note 3)  | 2/3 ×<br>VL              |      |                       | V     |
| I/O V <sub>L</sub> Output Voltage Low               | Voll             | I/O V <sub>L</sub> sink current = 1mA, I/O V <sub>CC</sub> $\leq$ 0.2V (Note 3)                        |                          |      | 0.4                   | V     |
| I/O V <sub>CC</sub> Output Voltage High             | V <sub>OHC</sub> | I/O V <sub>CC</sub> source current = $20\mu$ A,<br>I/O V <sub>L</sub> ≥ V <sub>L</sub> - 0.2V (Note 3) | 2/3 ×<br>V <sub>CC</sub> |      |                       | V     |
| I/O V <sub>CC</sub> Output Voltage Low              | Volc             | I/O V <sub>CC</sub> sink current = 1mA,<br>I/O V <sub>L</sub> $\leq$ 0.15V (Note 3)                    |                          |      | 0.4                   | V     |
| SHDN Input Voltage High                             | VIH-SHDN         |  | 2/3 ×<br>VL              |      |                       | V     |
|   |                  | $V_L \ge +1.8V$  |                          |      | 0.4                   |       |
| SHDN Input Voltage Low                              | VIL-SHDN         | $V_{L} \ge +1.6V$  |                          |      | 0.2                   | V     |

#### **ELECTRICAL CHARACTERISTICS (continued)**

(V<sub>CC</sub> = +2.5V to +5.5V, V<sub>L</sub> = +1.6V to +5.5V (Note 1), GND = 0; I/O V<sub>L</sub>, I/O V<sub>CC</sub> unconnected;  $T_A = -40^{\circ}$ C to +85°C, unless otherwise noted. Typical values are at  $T_A = +25^{\circ}$ C.) (Note 2)

| PARAMETER   | SYMBOL            | CONDITIONS   | MIN | ТҮР | МАХ | UNITS |  |
|---|-------------------|--|-----|-----|-----|-------|--|
| <b>TIMING CHARACTERISTICS</b> ( $R_{LOAD} = 1M\Omega$ , $V_{CC} = +2.5V$ , $V_L = +1.6V$ , I/O test signal Rail-to-Rail® unless otherwise noted, Figure 1) (Note 3) |                   |  |     |     |     |       |  |
| I/O V <sub>CC</sub> Rise Time   |                   | C <sub>LOAD</sub> = 10pF, data rate = 2Mbps (Note 5)               |     |     | 100 |       |  |
|   | touco             | C <sub>LOAD</sub> = 50pF, data rate = 1Mbps                        |     |     | 200 | ns    |  |
| (Note 4)  | t <sub>RVCC</sub> | C <sub>LOAD</sub> = 30pF, data rate = 500kbps,<br>open-drain drive |     |     | 400 | 115   |  |
|   |                   | C <sub>LOAD</sub> = 10pF, data rate = 2Mbps (Note 5)               |     |     | 50  |       |  |
| I/O V <sub>CC</sub> Fall Time   | truco             | C <sub>LOAD</sub> = 50pF, data rate = 1Mbps                        |     |     | 200 | ns    |  |
| (Note 4)  | tfvcc             | C <sub>LOAD</sub> = 30pF, data rate = 500kbps,<br>open-drain drive |     |     | 400 | 115   |  |
|   |                   | C <sub>LOAD</sub> = 10pF, data rate = 2Mbps (Note 5)               |     |     | 100 |       |  |
| I/O V <sub>L</sub> Rise Time  | t <sub>RVL</sub>  | C <sub>LOAD</sub> = 50pF, data rate = 1Mbps                        |     |     | 200 | ns    |  |
| (Note 4)  | 'RVL              | C <sub>LOAD</sub> = 30pF, data rate = 500kbps,<br>open-drain drive |     |     | 400 | 115   |  |
|   |                   | C <sub>LOAD</sub> = 10pF, data rate = 2Mbps (Note 5)               |     |     | 50  | - ns  |  |
| I/O V <sub>L</sub> Fall Time  | tryu              | C <sub>LOAD</sub> = 50pF, data rate = 1Mbps                        |     |     | 200 |       |  |
| (Note 4)  | tfvl              | C <sub>LOAD</sub> = 30pF, data rate = 500kbps,<br>open-drain drive |     |     | 400 |       |  |
|   | tpd-vcc-hl        | C <sub>LOAD</sub> = 10pF, data rate = 2Mbps (Note 5)               |     |     | 50  | - ns  |  |
| HIGH-to-LOW Transition<br>Propagation Delay   |                   | C <sub>LOAD</sub> = 50pF, data rate = 1Mbps                        |     |     | 200 |       |  |
| (Driving I/O $V_L$ )  | IPD-VCC-HL        | C <sub>LOAD</sub> = 30pF, data rate = 500kbps,<br>open-drain drive |     |     | 400 |       |  |
|   |                   | C <sub>LOAD</sub> = 10pF, data rate = 2Mbps (Note 5)               |     |     | 200 |       |  |
| LOW-to-HIGH Transition<br>Propagation Delay   |                   | C <sub>LOAD</sub> = 50pF, data rate = 1Mbps                        |     |     | 400 | - ns  |  |
| (Driving I/O V <sub>L</sub> )   | tpd-vcc-lh        | C <sub>LOAD</sub> = 30pF, data rate = 500kbps,<br>open-drain drive |     |     | 800 |       |  |
|   |                   | C <sub>LOAD</sub> = 10pF, data rate = 2Mbps (Note 5)               |     |     | 50  |       |  |
| HIGH-to-LOW Transition<br>Propagation Delay   | tpd-vl-hl         | C <sub>LOAD</sub> = 50pF, data rate = 1Mbps                        |     |     | 200 | ns    |  |
| (Driving I/O $V_{CC}$ )   | IPD-VL-HL         | C <sub>LOAD</sub> = 30pF, data rate = 500kbps,<br>open-drain drive |     |     | 400 | 115   |  |
| LOW-to-HIGH Transition<br>Propagation Delay<br>(Driving I/O V <sub>CC</sub> )   | tpd-vl-lh         | C <sub>LOAD</sub> = 10pF, data rate = 2Mbps (Note 5)               |     |     | 200 | ns    |  |
|   |                   | C <sub>LOAD</sub> = 50pF, data rate = 1Mbps                        |     |     | 400 |       |  |
|   |                   | C <sub>LOAD</sub> = 30pF, data rate = 500kbps,<br>open-drain drive |     |     | 800 | - ns  |  |
| HIGH-to-LOW Transition<br>Propagation Delay<br>Device-to-Device Skew<br>(Driving I/O V <sub>L</sub> )   |                   | C <sub>LOAD</sub> = 10pF, data rate = 2Mbps (Note 3)               |     | 2   |     |       |  |
|   |                   | $C_{LOAD} = 50 pF$ , data rate = 1Mbps                             |     | 4   |     | ns    |  |
|   |                   | C <sub>LOAD</sub> = 30pF, data rate = 500kbps,<br>open-drain drive |     | 5   |     |       |  |

Rail-to-Rail is a registered trademark of Nippon Motorola Ltd.



#### **ELECTRICAL CHARACTERISTICS (continued)**

 $(V_{CC} = +2.5V \text{ to } +5.5V, V_L = +1.6V \text{ to } +5.5V \text{ (Note 1), GND} = 0; I/O V_L, I/O V_{CC} \text{ unconnected}; T_A = -40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}, \text{ unless otherwise noted}$ . Typical values are at  $T_A = +25^{\circ}\text{C}$ .) (Note 2)

| PARAMETER  | SYMBOL | CONDITIONS   | MIN | ТҮР | MAX | UNITS |
|--|--------|--|-----|-----|-----|-------|
| LOW-to-HIGH Transition   |        | C <sub>LOAD</sub> = 10pF, data rate = 2Mbps (Note 3)               |     | 5   |     |       |
| Propagation Delay  |        | $C_{LOAD} = 50 pF$ , data rate = 1Mbps                             |     | 8   |     | ns    |
| Device-to-Device Skew<br>(Driving I/O V <sub>L</sub> )   |        | C <sub>LOAD</sub> = 30pF, data rate = 500kbps,<br>open-drain drive |     | 80  |     |       |
| HIGH-to-LOW Transition   |        | C <sub>LOAD</sub> = 10pF, data rate = 2Mbps (Note 3)               |     | 2   |     |       |
| Propagation Delay<br>Device-to-Device Skew<br>(Driving I/O V <sub>CC</sub> )<br>LOW-to-HIGH Transition<br>Propagation Delay<br>Device-to-Device Skew<br>(Driving I/O V <sub>CC</sub> ) |        | C <sub>LOAD</sub> = 50pF, data rate = 1Mbps                        |     | 4   |     | ns    |
|  |        | C <sub>LOAD</sub> = 30pF, data rate = 500kbps,<br>open-drain drive |     | 5   |     |       |
|  |        | C <sub>LOAD</sub> = 10pF, data rate = 2Mbps (Note 3)               |     | 7   |     |       |
|  |        | C <sub>LOAD</sub> = 50pF, data rate = 1Mbps                        |     | 8   |     | ns    |
|  |        | C <sub>LOAD</sub> = 30pF, data rate = 500kbps,<br>open-drain drive |     | 50  |     |       |
| Maximum Data Rate  |        | C <sub>LOAD</sub> = 10pF (Note 5)                                  | 2   | 3   |     |       |
|  |        | $C_{LOAD} = 50 pF$   | 1   | 2   |     | Mbps  |
|  |        | C <sub>LOAD</sub> = 30pF, open-drain drive                         | 0.5 | 1   |     |       |

**Note 1:**  $V_L$  must always be less than or equal to  $V_{CC}$ .

Note 2: All units are 100% production tested at  $T_A = +25$  °C. Limits over the operating temperature range are guaranteed by design and not production tested.

Note 3: Tested only at worst case:  $V_{CC}$  = +2.5V,  $V_L$  = +1.6V.

Note 4: 10% to 90%.

Note 5: Guaranteed by correlation to  $C_{LOAD} = 50 pF$ .





Figure 1b. Rail-to-Rail Driving I/O V<sub>CC</sub>

Figure 1a. Rail-to-Rail Driving I/O VL



Figure 1c. Open-Drain Driving I/O VL



Figure 1d. Open-Drain Driving I/O V<sub>CC</sub>

# MAX3370/MAX3371



#### **Typical Operating Characteristics**

MAX3370/MAX337

#### Typical Operating Characteristics (continued)

(Driving I/O V<sub>L</sub> rail-to-rail,  $R_L = 1M\Omega$ ,  $T_A = +25^{\circ}C$ , unless otherwise noted.)



///XI//

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#### **Pin Description**

| F       | PIN     |                                    | FUNCTION  |  |  |
|---------|---------|------------------------------------|---|--|--|
| MAX3370 | MAX3371 | NAME                               | FUNCTION  |  |  |
| 1       | 1       | VL                                 | Logic Supply Voltage  |  |  |
| 2       | 2       | GND                                | Ground  |  |  |
| 3       | 3       | I/O VL Input/Output Referred to VL |   |  |  |
| 4       | 4       | I/O V <sub>CC</sub>                | Input/Output Referred to V <sub>CC</sub>  |  |  |
| 5       | 6       | V <sub>CC</sub>                    | Power-Supply Voltage  |  |  |
| _       | 5       | SHDN                               | Shutdown. A high turns on the device. A low shuts down the device. I/O $V_{CC}$ and I/O $V_L$ are high impedance in shutdown. |  |  |

#### **Detailed Description**

The MAX3370/MAX3371 provide the necessary level translation to allow data transfer in a multivoltage system. These devices transmit data between an I/O pin referenced to  $V_{CC}$  and an I/O pin referenced to  $V_L$ . The V<sub>CC</sub> supply voltage range is from +2.5V to +5.5V, and the V<sub>L</sub> supply voltage range is between +1.6V and +5.5V. The MAX3371 features a shutdown mode in which I/O V<sub>CC</sub> and I/O V<sub>L</sub> are placed in a high-impedance state and supply current drops to 1µA.

The MAX3370/MAX3371 are bidirectional level shifters allowing data transfer from the V<sub>CC</sub> side to the V<sub>L</sub> side, and from the  $V_L$  side to the  $V_{CC}$  side. Both devices operate at speeds up to 2Mbps with an active driver and up to 500kbps with an open-drain driver.

#### Level Translation

The MAX3370/MAX3371 provide bidirectional level translation between I/O pins referred to V<sub>CC</sub> and V<sub>L</sub>. I/O V<sub>CC</sub> and I/O V<sub>L</sub> are internally pulled up to their respective power-supply rails through  $10k\Omega$  resistors. V<sub>CC</sub> must be between +2.5V and +5.5V, and V<sub>L</sub> must be between +1.6V and +5.5V. For proper operation, V can not exceed Vcc.

The MAX3370/MAX3371 can operate at data rates up to 2Mbps when driven by an active (push-pull) driver with a 10pF load, 1Mbps when driven by an active driver with a 50pF load, or 500kbps when driven by an open-drain driver with a 30pF load. The internal pullups allow these devices to be driven by open-drain drivers.

#### MAX3371 Shutdown Mode

The MAX3371 enters a low-power shutdown mode when SHDN is driven low. Connect SHDN to VL or drive high for normal operation. Activating shutdown mode disconnects the internal  $10k\Omega$  pullup resistors on I/O

#### Functional Diagram Vcc /M/IXI/M MAX3370



V<sub>CC</sub> and I/O V<sub>L</sub>. As a result, the supply current decreases to <1µA, and the I/O lines are high impedance. The high impedance I/O lines in shutdown allow use in a multidrop network. When in shutdown, I/O VL can be driven to V<sub>L</sub> and I/O V<sub>CC</sub> can be driven to V<sub>CC</sub>.

#### Speed-Up

The speed-up circuit is a one-shot generator that helps the rise time of the output waveform in the low-to-high transition. When triggered, switches S1 and S2 turn on for 320ns to pull up I/O VL and I/O VCC. This greatly reduces the rise time and propagation delay for the low-to-high transition as well as improves the duty cycle (closer to 50% for an ideal square-wave input). See the scope plots in the Typical Operating Characteristics for the speed-up circuitry in operation.



#### **Applications Information**

#### **Power-Supply Decoupling**

To reduce ripple and the chance of transmitting incorrect data, decouple V<sub>CC</sub> and V<sub>L</sub> to ground with a  $0.1\mu$ F capacitor as close to the device as possible.

#### I<sup>2</sup>C Level Translation

The MAX3370/MAX3371 are ideal for level translation between a low-voltage ASIC and an I<sup>2</sup>C device. The devices' bidirectional natures allow their use in the data line of I<sup>2</sup>C communications. A typical application is interfacing a low-voltage microprocessor to a 3V or 5V D/A converter, such as the MAX517.

The I/O lines on the MAX3370/MAX3371 are bidirectional, can be level-shifted up to +5.5V, and contain internal 10k $\Omega$  pullup resistors to allow open-drain driving (see the *Typical Operating Circuit*).

#### Push-Pull vs. Open-Drain Driving

The MAX3370/MAX3371 I/O pins can be driven by a push-pull or open-drain device. When using a push-pull driver, the MAX3370/MAX3371 operate up to 2Mbps with a 10pF load or 1Mbps with a 50pF load. The internal pullup resistors on the I/O pins allow use with opendrain devices. The MAX3370/MAX3371 operate up to 500kbps with a 30pF load when driven by an opendrain device.

Data rates higher than those listed in the *Electrical Characteristics* table can be achieved. The maximum data rate is limited to 3Mbps by the speed-up circuitry.

#### Unidirectional vs. Bidirectional Level Translator

The MAX3370/MAX3371 may also be used to translate unidirectional signals without signal inversion. The devices provide the smallest solution (SC70 package) for unidirectional level translation without inversion.

#### Chip Information

TRANSISTOR COUNT: 75 PROCESS: BICMOS



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