

#### Wide bandwidth dual JFET operational amplifiers

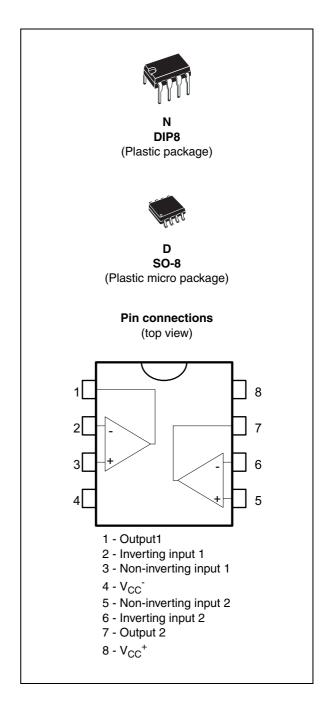
#### **Features**

- Low power consumption
- Wide common-mode (up to V<sub>CC</sub><sup>+</sup>) and differential voltage range
- Low input bias and offset current
- Output short-circuit protection
- High input impedance JFET input stage
- Internal frequency compensation
- Latch up free operation
- High slew rate 16 V/µs (typical)

#### **Description**

These circuits are high speed JFET input dual operational amplifiers incorporating well matched, high voltage JFET and bipolar transistors in a monolithic integrated circuit.

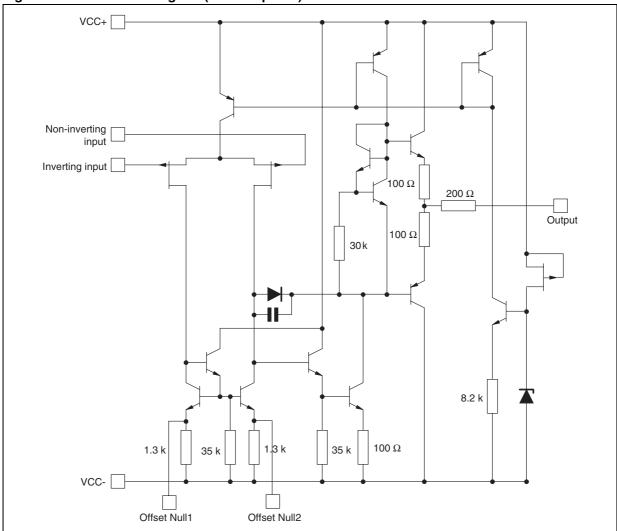
The devices feature high slew rates, low input bias and offset currents, and low offset voltage temperature coefficient.



Schematics LF253, LF353

## 1 Schematics

Figure 1. Schematic diagram (each amplifier)



#### 2 Absolute maximum ratings and operating conditions

Table 1. Absolute maximum ratings

Symbol	Parameter	Value	Unit
$V_{CC}$	Supply voltage <sup>(1)</sup>	±18	V
V <sub>i</sub>	Input voltage <sup>(2)</sup>	±15	V
V <sub>id</sub>	Differential input voltage <sup>(3)</sup>	±30	V
R <sub>thja</sub>	Thermal resistance junction to ambient <sup>(4)</sup> SO-8 DIP8	125 85	°C/W
R <sub>thjc</sub>	Thermal resistance junction to case <sup>(4)</sup> SO-8 DIP8	40 41	°C/W
	Output short-circuit duration <sup>(5)</sup>	Infinite	
T <sub>stg</sub>	Storage temperature range	-65 to +150	°C
	HBM: human body model <sup>(6)</sup>	1	kV
ESD	MM: machine model <sup>(7)</sup>	200	V
	CDM: charged device model <sup>(8)</sup>	1.5	kV

All voltage values, except differential voltage, are with respect to the zero reference level (ground) of the supply voltages where the zero reference level is the midpoint between V<sub>CC</sub><sup>+</sup> and V<sub>CC</sub><sup>-</sup>.

- 2. The magnitude of the input voltage must never exceed the magnitude of the supply voltage or 15 volts, whichever is less.
- 3. Differential voltages are the non-inverting input terminal with respect to the inverting input terminal.
- 4. Short-circuits can cause excessive heating and destructive dissipation. Values are typical.
- 5. The output may be shorted to ground or to either supply. Temperature and/or supply voltages must be limited to ensure that the dissipation rating is not exceeded
- 6. Human body model: a 100 pF capacitor is charged to the specified voltage, then discharged through a 1.5 kΩ resistor between two pins of the device. This is done for all couples of connected pin combinations while the other pins are floating.
- Machine model: a 200 pF capacitor is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5 Ω). This is done for all couples of connected pin combinations while the other pins are floating.
- 8. Charged device model: all pins and the package are charged together to the specified voltage and then discharged directly to the ground through only one pin. This is done for all pins.

Table 2. Operating conditions

Symbol	Parameter	LF253	LF353	Unit
V <sub>CC</sub>	Supply voltage	6 to 36		V
T <sub>oper</sub>	Operating free-air temperature range	-40 to +105	0 to +70	°C



Electrical characteristics LF253, LF353

#### 3 Electrical characteristics

Table 3. Electrical characteristics at  $V_{CC} = \pm 15 \text{ V}$ ,  $T_{amb} = +25^{\circ}\text{C}$  (unless otherwise specified)

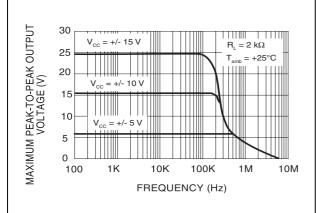
Symbol	Parameter	Min.	Тур.	Max.	Unit	
V <sub>io</sub>	Input offset voltage ( $R_s = 10k\Omega$ )		3	10	mV	
<b>V</b> 10	$T_{min} \le T_{amb} \le T_{max}$			13	1117	
DV <sub>io</sub>	Input offset voltage drift		10		μV/°C	
I <sub>io</sub>	Input offset current (1)		5	100	pA	
'10	$T_{min} \le T_{amb} \le T_{max}$			4	nA	
l <sub>ib</sub>	Input bias current (1)		20	200	pA	
-ib	$T_{min} \le T_{amb} \le T_{max}$			20	nA	
A <sub>vd</sub>	Large signal voltage gain $(R_L = 2k\Omega, V_0 = \pm 10V)$ 50 200				V/mV	
- vu	$T_{min} \le T_{amb} \le T_{max}$	25			.,	
SVR	Supply voltage rejection ratio ( $R_S = 10k\Omega$ )	80	86		dB	
	$T_{min} \le T_{amb} \le T_{max}$	80			-	
I <sub>CC</sub>	Supply current, no load		1.4	3.2	mA	
	$T_{min} \le T_{amb} \le T_{max}$			3.2		
V <sub>icm</sub>	Input common mode voltage range	±11	+15 -12		V	
CMR	Common mode rejection ratio ( $R_S = 10k\Omega$ )	70	86		dB	
Civil t	$T_{min} \le T_{amb} \le T_{max}$	70			ub	
laa	Output short-circuit current	10	40	60	mA	
los	$T_{min} \le T_{amb} \le T_{max}$	10		60	ША	
	Output voltage swing					
	$R_L = 2k\Omega$	10	12			
$\pm V_{opp}$	$R_{L} = 10k\Omega$ $T_{min} \le T_{amb} \le T_{max}$	12	13.5		V	
	$R_1 = 2k\Omega$	10				
	$R_L^2 = 10k\Omega$	12				
SR	Slew rate, $V_i = 10V$ , $R_L = 2k\Omega$ , $C_L = 100pF$ , unity gain	12	16		V/µs	
t <sub>r</sub>	Rise time, $V_i = 20$ mV, $R_L = 2$ k $\Omega$ , $C_L = 100$ pF, unity gain		0.1		μs	
K <sub>ov</sub>	Overshoot, $V_i = 20$ mV, $R_L = 2$ k $\Omega$ , $C_L = 100$ pF, unity gain		10		%	
GBP	Gain bandwidth product, f = 100kHz, $V_{in}$ = 10mV, $R_{L}$ = 2k $\Omega$ , $C_{L}$ = 100pF	2.5	4		MHz	
R <sub>i</sub>	Input resistance		10 <sup>12</sup>		Ω	
THD	Total harmonic distortion, f= 1kHz, $A_{\rm V}$ = 20dB, $R_{\rm L}$ = 2k $\Omega$ , $C_{\rm L}$ =100pF, $V_{\rm o}$ = 2V $_{\rm pp}$		0.01		%	
	Equivalent input noise voltage		15		nV	
e <sub>n</sub>	$R_S = 100\Omega$ , $f = 1KHz$		15		$\frac{\text{nV}}{\sqrt{\text{Hz}}}$	
Øm	Phase margin		45		Degrees	
V <sub>01</sub> /V <sub>02</sub>	Channel separation (A <sub>V</sub> = 100)		120		dB	
		-				

<sup>1.</sup> The input bias currents are junction leakage currents which approximately double for every 10°C increase in the junction temperature.

LF253, LF353 Electrical characteristics

Figure 2. Maximum peak-to-peak output voltage vs. frequency,  $R_L = 2 k\Omega$ 

Figure 3. Maximum peak-to-peak output voltage vs. frequency,  $R_1 = 10 \text{ k}\Omega$ 



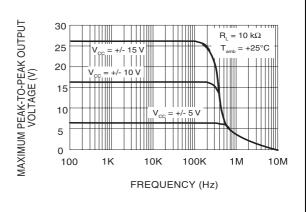
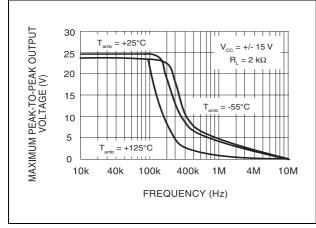


Figure 4. Maximum peak-to-peak output voltage versus frequency

Figure 5. Maximum peak-to-peak output voltage versus free air temperature



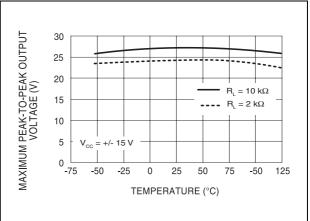
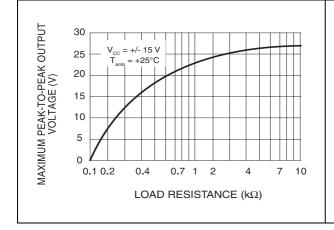
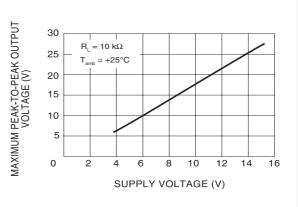


Figure 6. Maximum peak-to-peak output voltage versus load resistance

Figure 7. Maximum peak-to-peak output voltage versus supply voltage

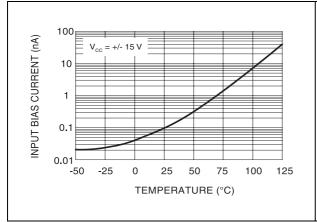




Electrical characteristics LF253, LF353

Figure 8. Input bias current versus free air temperature

Figure 9. Large signal differential voltage amplification versus free air temp.



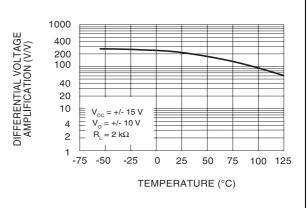
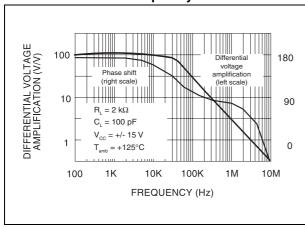


Figure 10. Large signal differential voltage amplification and phase shift versus frequency

Figure 11. Total power dissipation versus free air temperature



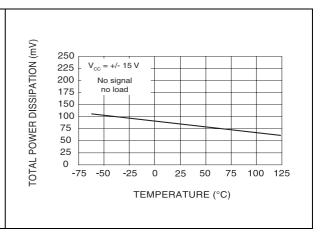
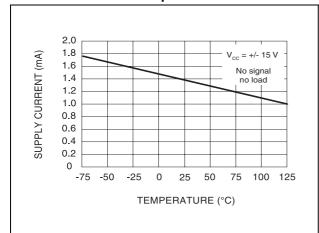
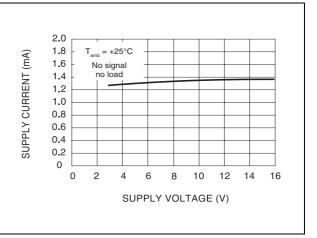


Figure 12. Supply current per amplifier versus Figure 13. Supply current per amplifier versus free air temperature supply voltage





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LF253, LF353 Electrical characteristics

Figure 14. Common mode rejection ratio versus free air temperature

Figure 15. Voltage follower large signal pulse response

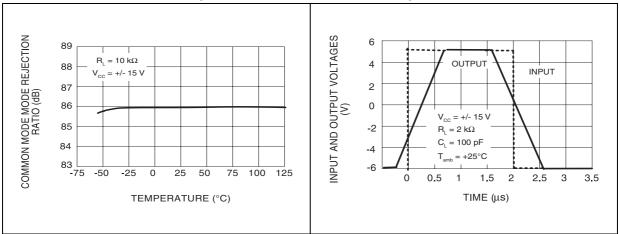


Figure 16. Output voltage versus elapsed time Figure 17. Equivalent input noise voltage versus frequency

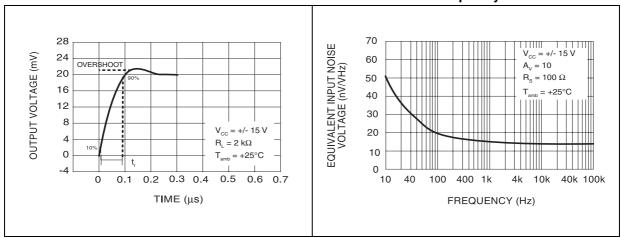
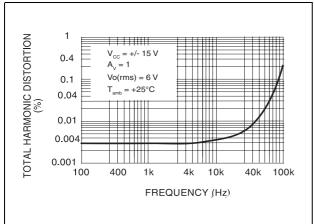


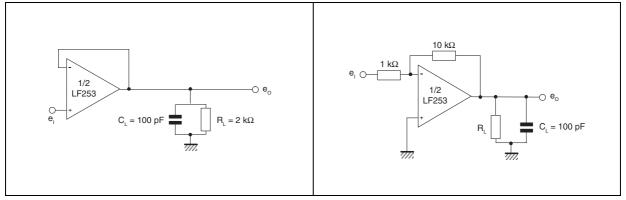
Figure 18. Total harmonic distortion versus frequency



## 4 Parameter measurement information

Figure 19. Voltage follower

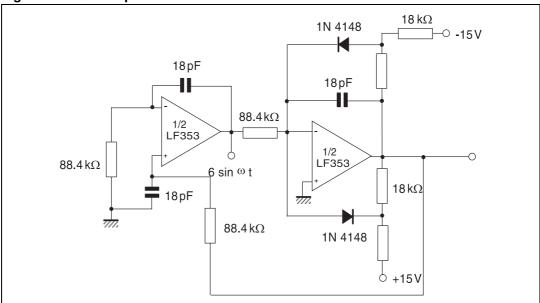
Figure 20. Gain of 10 inverting amplifier



LF253, LF353 Typical application

## 5 Typical application

Figure 21. Quadruple oscillator



Package information LF253, LF353

## 6 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

LF253, LF353 Package information

## 6.1 DIP8 package information

Figure 22. DIP8 package mechanical drawing

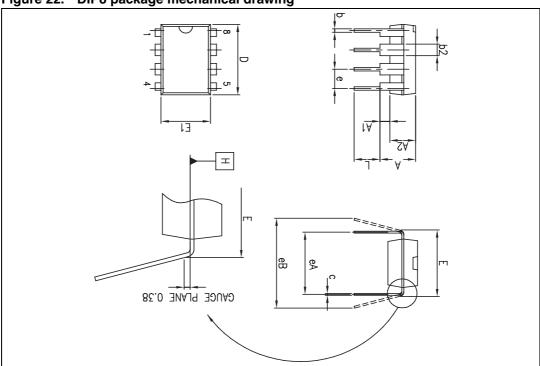


Table 4. DIP8 package mechanical data

	Dimensions					
Ref.		Millimeters			Inches	
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α			5.33			0.210
A1	0.38			0.015		
A2	2.92	3.30	4.95	0.115	0.130	0.195
b	0.36	0.46	0.56	0.014	0.018	0.022
b2	1.14	1.52	1.78	0.045	0.060	0.070
С	0.20	0.25	0.36	0.008	0.010	0.014
D	9.02	9.27	10.16	0.355	0.365	0.400
E	7.62	7.87	8.26	0.300	0.310	0.325
E1	6.10	6.35	7.11	0.240	0.250	0.280
е		2.54			0.100	
eA		7.62			0.300	
eB			10.92			0.430
L	2.92	3.30	3.81	0.115	0.130	0.150

Package information LF253, LF353

## 6.2 SO-8 package information

Figure 23. SO-8 package mechanical drawing

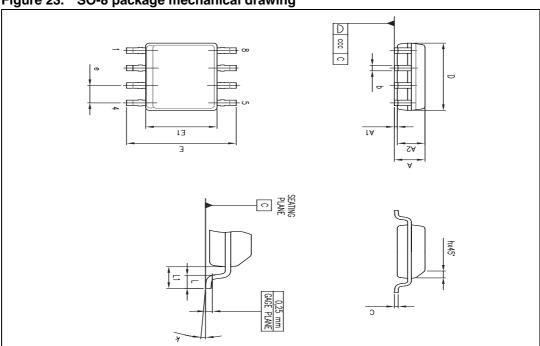


Table 5. SO-8 package mechanical data

	Dimensions					
Ref.		Millimeters			Inches	
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α			1.75			0.069
A1	0.10		0.25	0.004		0.010
A2	1.25			0.049		
b	0.28		0.48	0.011		0.019
С	0.17		0.23	0.007		0.010
D	4.80	4.90	5.00	0.189	0.193	0.197
E	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.154	0.157
е		1.27			0.050	
h	0.25		0.50	0.010		0.020
L	0.40		1.27	0.016		0.050
L1		1.04			0.040	
k	1°		8°	1°		8°
ccc			0.10			0.004

# 7 Ordering information

Table 6. Order codes

Order code	Temperature range	Package	Packing	Marking
LF253N		DIP8	Tube	LF253N
LF253D LF253DT	-40°C, +105°C	SO-8	Tube or Tape & reel	253
LF353N		DIP8	Tube	LF353N
LF353D LF353DT	0°C, +70°C	SO-8	Tube or Tape & reel	353

Revision history LF253, LF353

# 8 Revision history

Table 7. Document revision history

Date	Revision	Changes
01-Mar-2001	1	Initial release.
08-Sep-2008	2	Updated document format.  Removed information concerning military temperature range (LF153).  Added L1 parameter dimensions in <i>Table 5: SO-8 package mechanical data</i> .
25-Mar-2010	3	Corrected error in <i>Table 6: Order codes</i> : LF253N, LF253D, LF353N and LF353D proposed in tube packing.

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