

ESDALC6V1Px

ASD (Application Specific Devices)

Low capacitance TRANSIL™ arrays for ESD protection

Main applications

Where transient overvoltage protection in ESD sensitive equipment is required, such as:

- Computers
- Printers
- Communication systems
- Cellular phone handsets and acessories
- Wireline and wireless telephone sets
- Set top boxes

Features

- 2 to 4 unidirectional Transil functions
- Breakdown voltage V_{BR} = 6.1 V min.
- Low leakage current < 100 nA</p>
- Low capacitance (7.5 pF @ 3 V)
- Very small PCB area < 2.6 mm²

Description

The ESDALC6V1Px are monolithic suppressors designed to protect components connected to data and transmission lines against ESD.

These devices clamp the voltage just above the logic level supply for positive transients and to a diode drop below ground for negative transients.

Benefits

- High ESD protection level
- High integration

Complies with the following standards:

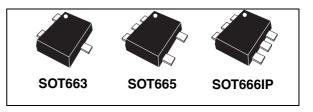
IEC61000-4-2 level 4:

15 kV(air discharge) 8 kV(contact discharge)

MIL STD 883E-Method 3015-7: class3

25 kV HBM (Human Body Model)

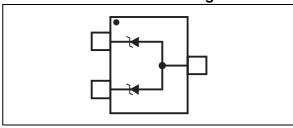
TM: TRANSIL is a trademark of STMicroelectronics



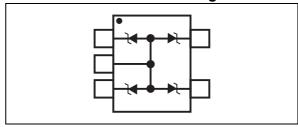
Order code

Part number	Marking
ESDALC6V1P3	A2
ESDALC6V1P5	A1
ESDALC6V1P6	D

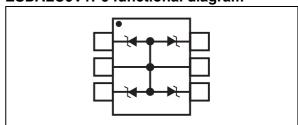
ESDALC6V1P3 functional diagram



ESDALC6V1P5 functional diagram



ESDALC6V1P6 functional diagram



Characteristics ESDALC6V1Px

1 Characteristics

Table 1. Absolute ratings $(T_{amb} = 25^{\circ} C)$

Symbol		Parameter		
V _{PP} ⁽¹⁾	Peak pulse voltage	IEC 61000-4-2 contact discharge IEC 61000-4-2 air discharge	±8 ±15	kV
P _{PP}	Peak pulse power (8/20 μs) ⁽¹⁾	T_j initial = T_{amb}	30	W
T _j	Junction temperature	150	°C	
T _{stg}	Storage temperature range	-55 to +150	°C	
T _L	Maximum lead temperature fo	260	°C	
T _{op}	Operating temperature range		-40 to +150	°C

^{1.} For a surge greater than the maximum values, the diode will fail in short-circuit.

Table 2. Electrical characteristics ($T_{amb} = 25^{\circ} C$)

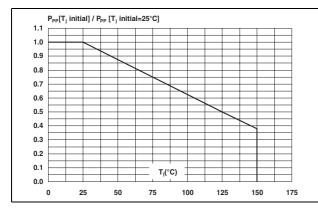
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Symbol	Parameter	* I
V _{RM}	Stand-off voltage	I _F
V_{BR}	Breakdown voltage	
V _{CL}	Clamping voltage	V _F
I _{RM}	Leakage current	V _{CL} V _{BR} V _{RM}
I _{PP}	Peak pulse current	I _{RM}
αТ	Voltage temperature coefficient	
V _F	Forward voltage drop	
С	Capacitance	Slope: 1/R _d
R _d	Dynamic resistance	1

	V _{BR} @ I _R		I _{RM} @ V _{RM}		R_d	αΤ	С	
Part Number	min.	max.		max.		typ.	typ.	typ.@ 3V
	V	V	mA	μΑ	V	Ω	10 ⁻⁴ /°C	pF
ESDALC6V1P3 ESDALC6V1P5 ESDALC6V1P6	6.1	7.2	1	0.1	3	1.5	4.5	7.5

ESDALC6V1Px Characteristics

Figure 1. Peak power dissipation versus initial junction temperature

Figure 2. Peak pulse power versus exponential pulse duration $(T_i initial = 25^{\circ} C)$



Ppp(W)

1000

100

100

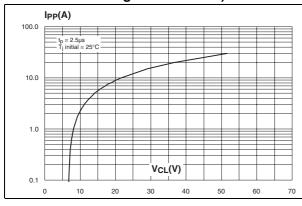
100

100

100

Figure 3. Clamping voltage versus peak pulse current (typical values, rectangular waveform)

Figure 4. Forward voltage drop versus peak forward current (typical values)



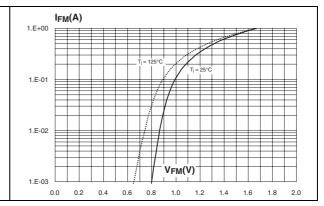
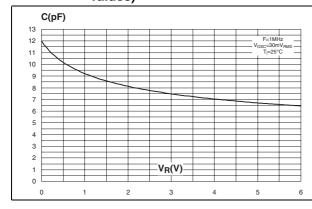


Figure 5. Junction capacitance versus reverse applied voltage (typical values)

Figure 6. Relative variation of leakage current versus junction temperature (typical values)



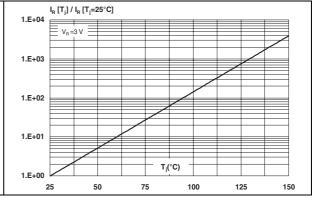
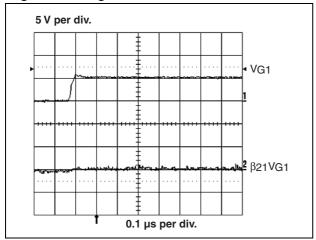
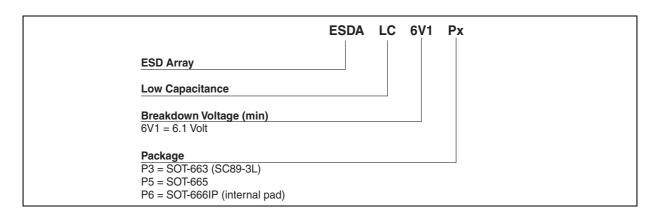


Figure 7. ESD response to IEC 61000-4-2 (air Figure 8. Analog crosstalk measurement discharge 15 kV positive surge)





2 Ordering information scheme

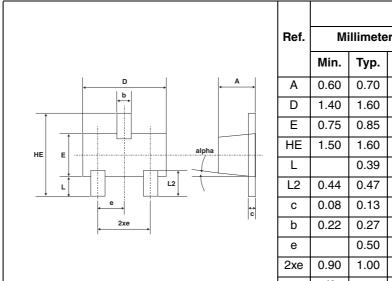


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ESDALC6V1Px Package information

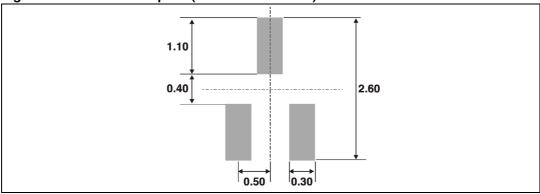
3 Package information

Table 3. SOT-663 dimensions



	Dimensions					
Ref	. М	illimete	rs		Inches	
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	0.60	0.70	0.80	0.024	0.028	0.031
D	1.40	1.60	1.80	0.055	0.063	0.071
Е	0.75	0.85	0.95	0.030	0.033	0.037
HE	1.50	1.60	1.70	0.059	0.063	0.067
L		0.39			0.015	
L2	0.44	0.47	0.50	0.017	0.018	0.020
С	0.08	0.13	0.18	0.003	0.005	0.007
b	0.22	0.27	0.37	0.009	0.011	0.015
е		0.50			0.020	
2xe	0.90	1.00	1.10	0.035	0.040	0.043
α	4°		7°	4°		7°

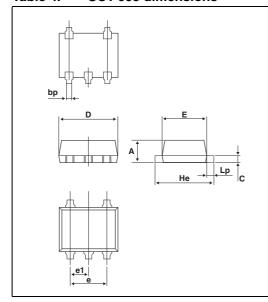
Figure 10. SOT-663 footprint (dimensions in mm)



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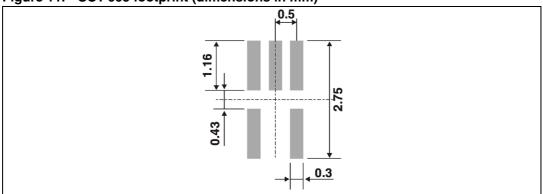
Package information ESDALC6V1Px

Table 4. SOT-665 dimensions



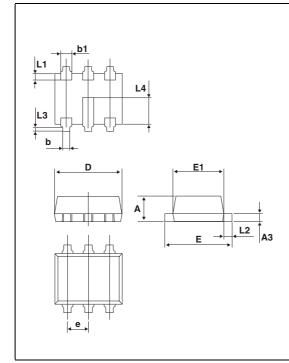
	Dimensions						
Ref.	Millim	neters	Inches				
	Min.	Max.	Min.	Max.			
Α	0.50	0.60	0.020	0.024			
BP	0.17	0.27	0.007	0.011			
С	0.08	0.18	0.003	0.007			
D	1.50	1.70	0.060	0.067			
Е	1.10	1.30	0.043	0.051			
е	1.00		0.040				
e1	0.50		0.020				
He	1.50	1.70	0.059	0.067			
Lp	0.10	0.30	0.004	0.012			

Figure 11. SOT-665 footprint (dimensions in mm)



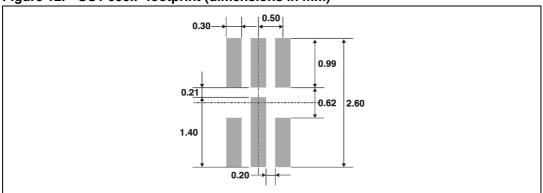
ESDALC6V1Px Package information

Table 5. SOT-666IP dimensions



	Dimensions							
Ref.	Millimeters			Inches				
	Min.	Тур.	Max.	Min.	Тур.	Max.		
Α	0.45		0.60	0.018		0.024		
А3	0.08		0.18	0.003		0.007		
b	0.17		0.34	0.007		0.013		
b1	0.19	0.27	0.34	0.007	0.011	0.013		
D	1.50		1.70	0.059		0.067		
Е	1.50		1.70	0.059		0.067		
E1	1.10		1.30	0.043		0.051		
е		0.50			0.020			
L1		0.19			0.007			
L2	0.10		0.30	0.004		0.012		
L3		0.10			0.004			
L4		0.60			0.024			

Figure 12. SOT-666IP footprint (dimensions in mm)



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

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Ordering information ESDALC6V1Px

4 Ordering information

Part number	Marking	Package	Weight	Base qty	Delivery mode
ESDALC6V1P3	A2	SOT663	2.9 mg	3000	Tape and reel
ESDALC6V1P5	A1	SOT665	2.9 mg	3000	Tape and reel
ESDALC6V1P6	D	SOT666IP	2.9 mg	3000	Tape and reel

5 Revision history

Date	Revision	Description of changes
16-Aug-2006	1	ESDALC6V1P3, ESDALC6V1P5, and ESDALC6V1P6 merged and reformatted to current standards.
23-Aug-2006	2	Table 1 on page 2: Temperature range upgraded to T _j max = 150° C
11-Oct-2006	3	Added values for V _{PP} in Table 1.

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