BCW65ALT1G, BCW65CLT1G

General Purpose Transistor

NPN Silicon

Features

 These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant



Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	32	Vdc
Collector – Base Voltage	V_{CBO}	60	Vdc
Emitter – Base Voltage	V_{EBO}	5.0	Vdc
Collector Current – Continuous	I _C	800	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit	
Total Device Dissipation FR-5 Board (Note 1), T _A = 25°C Derate above 25°C	P _D	225 1.8	mW mW/°C	
Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$	556	°C/W	
Total Device Dissipation Alumina Substrate, (Note 2) T _A = 25°C Derate above 25°C	P _D	300 2.4	mW mW/°C	
Thermal Resistance, Junction–to–Ambient	$R_{ heta JA}$	417	°C/W	
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C	

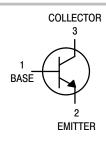
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.
- 2. Alumina = $0.4 \times 0.3 \times 0.024$ in 99.5% alumina.



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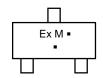
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SOT-23 CASE 318 STYLE 6

MARKING DIAGRAMS



Ex = Device Code x = A or C M = Date Code* • Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
BCW65ALT1G	SOT-23 (Pb-Free)	3000/Tape & Reel
BCW65CLT1G	SOT-23 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

BCW65ALT1G, BCW65CLT1G

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

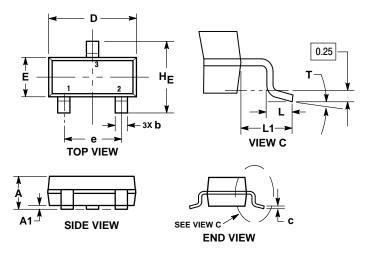
Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage $(I_C = 10 \text{ mAdc}, I_B = 0)$	V _{(BR)CEO}	32	-	-	Vdc
Collector – Emitter Breakdown Voltage ($I_C = 10 \mu Adc$, $V_{EB} = 0$)	V _{(BR)CES}	60	-	_	Vdc
Emitter – Base Breakdown Voltage ($I_E = 10 \mu Adc$, $I_C = 0$)	V _{(BR)EBO}	5.0	-	_	Vdc
Collector Cutoff Current $(V_{CE} = 32 \text{ Vdc}, I_E = 0)$ $(V_{CE} = 32 \text{ Vdc}, I_E = 0, T_A = 150^{\circ}\text{C})$	I _{CES}		_ _	20 20	nAdc μAdc
Emitter Cutoff Current $(V_{EB} = 4.0 \text{ Vdc}, I_{C} = 0)$	I _{EBO}	-	-	20	nAdc
ON CHARACTERISTICS		-	-	-	•
DC Current Gain BCW65ALT1 $ \begin{aligned} &(I_C = 100 \ \mu Adc, \ V_{CE} = 10 \ Vdc) \\ &(I_C = 10 \ mAdc, \ V_{CE} = 1.0 \ Vdc) \\ &(I_C = 100 \ mAdc, \ V_{CE} = 1.0 \ Vdc) \\ &(I_C = 500 \ mAdc, \ V_{CE} = 2.0 \ Vdc) \end{aligned} $	h _{FE}	35 75 100 35	- - -	- - 250 -	-
DC Current Gain BCW65CLT1 $ \begin{aligned} &(I_C = 100 \ \mu Adc, \ V_{CE} = 10 \ Vdc) \\ &(I_C = 10 \ mAdc, \ V_{CE} = 1.0 \ Vdc) \\ &(I_C = 100 \ mAdc, \ V_{CE} = 1.0 \ Vdc) \\ &(I_C = 500 \ mAdc, \ V_{CE} = 2.0 \ Vdc) \end{aligned} $	h _{FE}	80 180 250 100	- - -	- - 630 -	-
Collector – Emitter Saturation Voltage (I _C = 500 mAdc, I _B = 50 mAdc) (I _C = 100 mAdc, I _B = 10 mAdc)	V _{CE(sat)}	_ _	0.7 0.3	_ _	Vdc
Base – Emitter Saturation Voltage (I _C = 500 mAdc, I _B = 50 mAdc)	V _{BE(sat)}	-	_	2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS				•	
Current – Gain — Bandwidth Product (I _C = 20 mAdc, V _{CE} = 10 Vdc, f = 100 MHz)	f _T	100	-	_	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)	C_{obo}	-	-	12	pF
Input Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, f = 1.0 MHz)	C _{ibo}	-	-	80	pF
Noise Figure (V _{CE} = 5.0 Vdc, I _C = 0.2 mAdc, R _S = 1.0 k Ω , f = 1.0 kHz, BW = 200 Hz)	NF	-	-	10	dB
SWITCHING CHARACTERISTICS					
Turn-On Time $(I_{B1} = I_{B2} = 15 \text{ mAdc})$	t _{on}	_	-	100	ns
Turn–Off Time (I_C = 150 mAdc, R_L = 150 Ω)	t _{off}	-	_	400	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

BCW65ALT1G, BCW65CLT1G

PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 **ISSUE AR**



- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF
- THE BASE MATERIAL.

 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,
 PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
С	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
Т	0°		10°	0°		10°

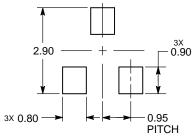
STYLE 6:

PIN 1. BASE

EMITTER

COLLECTOR

RECOMMENDED SOLDERING FOOTPRINT*



DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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