

2.5-V 460-kbps RS-232 TRANSCEIVER WITH ±15-kV ESD PROTECTION

Check for Samples: TRS3318E

FEATURES

- ESD Protection for RS-232 I/O Pins
 - ±15 kV (Human-Body Model)
 - ±8 kV (IEC 61000-4-2, Contact Discharge)
 - ±15 kV (IEC 61000-4-2, Air-Gap Discharge)
- 300-µA Operating Supply Current
- 1-μA Low-Power Standby Mode (With Receivers Active)
- Designed to Transmit at a Data Rate of 460 kbps
- Auto-Powerdown Plus Option Features Flexible Power-Saving Mode
- Operates From a Single 2.25-V to 3-V V_{CC} Supply
- Designed to be Interchangeable With Industry Standard '3318 Devices

APPLICATIONS

- Battery-Powered Systems
- PDAs
- Cellular Phones
- Notebooks
- Hand-Held Equipment
- Pagers

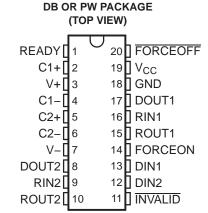
DESCRIPTION/ORDERING INFORMATION

The TRS3318E is a dual-driver, dual-receiver, RS-232-compatible transceiver. The device features auto-powerdown plus and enhanced electrostatic discharge (ESD) protection integrated into the chip. Driver output and receiver input are protected to ±15 kV using the IEC 61000-4-2 Air-Gap Discharge method, ±8 kV using the IEC 61000-4-2 Contact Discharge method, and ±15 kV using the Human-Body Model (HBM).

The device operates at a data rate of 460 kbps. The transceiver has a proprietary low-dropout driver output stage, enabling RS-232-compatible operation from a 2.25-V to 3-V supply with a dual charge pump. The charge pump requires only four $0.1-\mu F$ capacitors and features a logic-level output (READY) that asserts when the charge pump is regulating and the device is ready to begin transmitting.

The TRS3318E achieves a 1-µA supply current using the auto-powerdown feature. This device automatically enters a low-power power-down mode when the RS-232 cable is disconnected or the drivers of the connected peripherals are inactive for more than 30 s. The device turns on again when it senses a valid transition at any driver or receiver input. Auto-powerdown saves power without changes to the existing BIOS or operating system.

This device is available in two space-saving packages: 20-pin SSOP and 20-pin TSSOP.





Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



ORDERING INFORMATION⁽¹⁾

T _A	PACK	AGE ⁽²⁾	ORDERABLE PART NUMBER	TOP-SIDE MARKING
	SSOP – DB	Tube of 70 TRS3318ECDB		DV40EC
000 to 7000	330P - DB	Reel of 2000	TRS3318ECDBR	RV18EC
0°C to 70°C	TCCOR DW	Tube of 70	TRS3318ECPW	DVAOEC
	TSSOP – PW	Reel of 2000	TRS3318ECPWR	RV18EC
	CCOD DD	Tube of 70	TRS3318EIDB	DVAOEL
4000 to 0500	SSOP – DB	Reel of 2000	TRS3318EIDBR	RV18EI
–40°C to 85°C	TCCOR DW	Tube of 70	TRS3318EIPW	DVAOEL
	TSSOP – PW Reel of 2000		TRS3318EIPWR	RV18EI

⁽¹⁾ For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at www.ti.com.

DETAILED DESCRIPTION

Flexible control options for power management are featured when the <u>serial port and</u> driver inputs are inactive. The auto-powerdown plus feature functions when FORCEON is low and FORCEOFF is high. During this mode of operation, if the device does not sense valid signal transitions on all receiver and driver inputs for approximately 30 s, the built-in charge pump and drivers are powered down, reducing the supply current to 1 μ A. By disconnecting the <u>serial port</u> or placing the peripheral drivers off, auto-powerdown plus can be disabled when FORCEON and FORCEOFF are high. With auto-powerdown <u>plus enabled</u>, the device activates automatically when a valid signal is applied to any receiver or driver input. INVALID is high (valid data) if any receiver input voltage is greater than 2.7 V or less than -2.7 V, or has been between -0.3 V and 0.3 V for less than 30 μ s (typical number). INVALID is low (invalid data) if all receiver input voltage are between -0.3 V and 0.3 V for more than 30 μ s (typical number).

⁽²⁾ Package drawings, thermal data, and symbolization are available at www.ti.com/packaging.



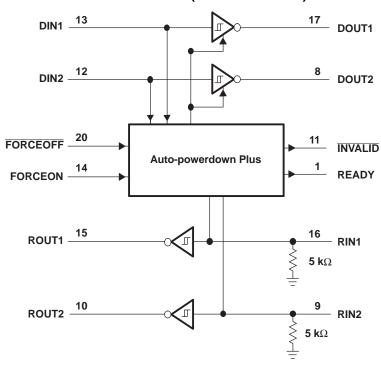
FUNCTION TABLE(1)

				ION TABL				
	INPUT CO	ONDITIONS			OUTPUT	STATES		
FORCEON	FORCEOFF	RECEIVER OR DRIVER EDGE WITHIN 30 s	VALID RS-232 LEVEL PRESENT AT RECEIVER	DRIVER	RECEIVER	INVALID	READY	OPERATING MODE
			Auto-Powerd	own Plus Co	nditions			
Н	Н	No	No	Active	Active	L	Н	Normal operation, auto-powerdown plus disabled
Н	Н	No	Yes	Active	Active	Н	Н	Normal operation, auto-powerdown plus disabled
L	Н	Yes	No	Active	Active	L	Н	Normal operation, auto-powerdown plus enabled
L	Н	Yes	Yes	Active	Active	н	Н	Normal operation, auto-powerdown plus enabled
L	Н	No	No	Z	Active	L	L	Power down, auto-powerdown plus enabled
L	Н	No	Yes	Z	Active	Н	L	Power down, auto-powerdown plus enabled
X	L	X	No	Z	Active	L	L	Manual power down
Х	L	X	Yes	Z	Active	Н	L	Manual power down
			Auto-Powe	erdown Cond	itions			
ĪNVALID	ĪNVALID	х	No	Z	Active	L	L	Power down, auto-powerdown enabled
ĪNVALID	ĪNVALID	X	Yes	Active	Active	Н	Н	Normal operation, auto-powerdown enabled

⁽¹⁾ H = high level, L = low level, X = irrelevant, Z = high impedance



LOGIC DIAGRAM (POSITIVE LOGIC)



TERMINAL FUNCTIONS

TERMI	NAL	DECORPORTION
NAME	NO.	DESCRIPTION
C1+	2	Positive voltage-doubler charge-pump capacitor
C1-	4	Negative voltage-doubler charge-pump capacitor
C2+	5	Positive inverting charge-pump capacitor
C2-	6	Negative inverting charge-pump capacitor
DIN	12, 13	CMOS driver inputs
DOUT	8, 17	RS-232 driver outputs
FORCEOFF	20	Force-off input, active low. Drive low to power down transmitters, receivers, and charge pump. This overrides auto-powerdown and FORCEON (see Function Table).
FORCEON	14	Force-on input, active high. Drive high to override auto-powerdown, keeping transmitters and receivers on (FORCEOFF must be high) (see Function Table).
GND	18	Ground
INVALID	11	Valid signal detector output, active low. A logic high indicates that a valid RS-232 level is present on a receiver input.
READY	1	Ready to transmit output, active high. READY is enabled high when V- goes below -3.5 V and the device is ready to transmit.
RIN	9, 16	RS-232 receiver inputs
ROUT	10, 15	CMOS receiver outputs
V+	3	2 × V _{CC} generated by the charge pump
V-	7	−2 × V _{CC} generated by the charge pump
V _{CC}	19	2.25-V to 3-V single-supply voltage



Absolute Maximum Ratings(1)

over operating free-air temperature range (unless otherwise noted)

		MIN	MAX	UNIT	
V _{CC} to GND		-0.3	6	V	
V+ to GND ⁽²⁾		-0.3	7	V	
V- to GND ⁽²⁾		-7	-7 0.3		
V+ + IV-I ⁽²⁾			-0.3 6		
Input voltage	DIN, FORCEON, FORCEOFF to GND	-0.3	6	V	
input voitage	RIN to GND	-0.3 7 -7 0.3 13 -0.3 6 ±25 ±13.2 -0.3 V _{CC} + 0.3 Continuous 640 559 -65 150	V		
Outrotuska	DOUT to GND		±25 ±13.2 V _{CC} + 0.3		
Output voltage	ROUT, INVALID, READY to GND	-0.3 7 -7 0.3 13 -0.3 6 ±25 ±13.2 -0.3 V _{CC} + 0.3 Continuous 640 559	V		
Short-circuit duration	DOUT to GND		Continuous		
Ocationary manufacturation (T. 7000)	20-pin SSOP (derate 8 mW/°C above 70°C)	±25 ±13.2 -0.3 V _{CC} + 0.3 Continuous 640	\^/		
Continuous power dissipation (T _A = 70°C)	20-pin TSSOP (derate 7 mW/°C above 70°C)		559	mW	
Storage temperature range			150	°C	
Lead temperature (soldering, 10 s)			300	°C	

⁽¹⁾ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating" conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. V+ and V- can have maximum magnitudes of 7 V, but their absolute difference cannot exceed 13 V.

Recommended Operating Conditions

See Figure 4

				MIN	NOM	MAX	UNIT
	Supply voltage			2.25	2.5	3	V
V_{IH}	Driver and control high-level input voltage	DIN, FORCEOFF, FORCEON	V _{CC} = 2.5 V to 3 V	0.7 × V _{CC}		5.5	٧
V_{IL}	Driver and control low-level input voltage	DIN, FORCEOFF, FORCEON	V _{CC} = 2.5 V to 3 V	0		0.3 × V _{CC}	٧
V_{I}	Receiver input voltage	•	•	-25		25	V
_	Operating free-air temperature	TRS3318EC		0		70	
IA		TRS3318EI		-40		85	°C

Product Folder Link(s): TRS3318E



Supply Current Section

Electrical Characteristics

over recommended ranges of supply voltage and operating free-air temperature,

 V_{CC} = 2.25 V to 3 V, C1–C4 = 0.1 $\mu F,\, T_A$ = T_{MIN} to $\underline{T_{MAX}}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP ⁽¹⁾	MAX	UNIT
DC Characteristics (V _{CC} = 2.5 V, T _A	= 25°C)				
Auto-powerdown plus supply current	FORCEON = GND, FORCEOFF = V _{CC} , All RIN and DIN idle		1	10	μΑ
Auto-powerdown supply current	FORCEOFF = GND		1	10	μΑ
Supply current	FORCEON = FORCEOFF = V _{CC} , No load		0.3	2	mA

⁽¹⁾ Typical values are at V_{CC} = 2.5 V, T_A = 25°C.

ESD Protection

PARAMETER	TEST CONDITIONS	TYP	UNIT
	HBM	±15	
RIN, DOUT	IEC 61000-4-2 Air-Gap Discharge method	±15	kV
	IEC 61000-4-2 Contact Discharge method	±8	



Driver Section

Electrical Characteristics

over recommended ranges of supply voltage and operating free-air temperature,

 $V_{CC} = 2.25 \text{ V}$ to 3 V, C1–C4 = 0.1 μ F, $T_A = T_{MIN}$ to T_{MAX} (unless otherwise noted) (see Figure 4)

PARAMETER	TEST CONDITIONS	MIN	TYP ⁽¹⁾	MAX	UNIT
Driver input hysteresis			0.3		V
Input leakage current	FORCEON, DIN, FORCEOFF		±0.01	±1	μΑ
Output voltage swing	All driver outputs loaded with 3 $k\Omega$ to ground	±3.7	±4		V
Output resistance	V _{CC} = 0, Driver output = ±2 V	300	10M		Ω
Output short-circuit current (2)			±25	±60	mA
Output leakage current	V _{CC} = 0 or 2.25 V to 3 V, V _{OUT} = ±12 V, Drivers disabled			±25	μΑ

Switching Characteristics

over recommended ranges of supply voltage and operating free-air temperature,

 V_{CC} = 2.25 V to 3 V, C1–C4 = 0.1 μ F, T_A = T_{MIN} to T_{MAX} (unless otherwise noted) (see Figure 1)

	PARAMETER	TEST CONDITIONS	MIN	TYP ⁽¹⁾	MAX	UNIT
	Maximum data rate	$R_L = 3 \text{ k}\Omega$, $C_L = 1000 \text{ pF}$, One transmitter switching	460			kbps
t _{PHL} - t _{PLH}	Driver skew ⁽²⁾			100		ns
	Transition-region slew rate	V_{CC} = 2.5 V, T_A = 25°C, R_L = 3 k Ω to 7 k Ω , Measured from 3 V to -3 V or -3 V to 3 V, C_L = 150 pF to 2500 pF	4		30	V/µs

Product Folder Link(s): TRS3318E

Typical values are at $V_{CC} = 2.5 \text{ V}$, $T_A = 25^{\circ}\text{C}$. Short-circuit durations should be controlled to prevent exceeding the device absolute power dissipation ratings, and not more than one output should be shorted at a time.

⁽¹⁾ Typical values are at V_{CC} = 2.5 V, T_A = 25°C. (2) Pulse skew is defined as $|t_{PLH}|$ – t_{PHL} of each channel of the same device.



Receiver Section

Electrical Characteristics

over recommended ranges of supply voltage and operating free-air temperature,

 $V_{CC} = 2.25 \text{ V}$ to 3 V, C1–C4 = 0.1 μ F, $T_A = T_{MIN}$ to T_{MAX} (unless otherwise noted) (see Figure 4)

PARAMETER	TEST CONDITIONS	MIN	TYP ⁽¹⁾	MAX	UNIT
Input voltage range		-25		25	V
Input threshold low	T _A = 25°C			$0.3 \times V_{CC}$	V
Input threshold high	T _A = 25°C	$0.7 \times V_{CC}$			V
Input hysteresis			0.3		V
Input resistance	T _A = 25°C	3	5	7	kΩ
Output leakage current			±0.05	±10	μΑ
Output voltage low	$I_{OUT} = 0.5 \text{ mA}$			$0.1 \times V_{CC}$	V
Output voltage high	$I_{OUT} = -0.5 \text{ mA}$	0.9 × V _{CC}			V

⁽¹⁾ Typical values are at $V_{CC} = 2.5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

Switching Characteristics

over recommended ranges of supply voltage and operating free-air temperature,

 V_{CC} = 2.25 V to 3 V, C1–C4 = 0.1 μ F (unless otherwise noted) (see Figure 4)

	PARAMETER	TEST CONDITIONS	TYP ⁽¹⁾	UNIT
t _{PHL}	Description delay	DIN to DOUT C 150 of	0.175	
t _{PLH}	Receiver propagation delay	RIN to ROUT, C _L = 150 pF	0.175	μs
t _{PHL} - t _{PLH}	Receiver skew ⁽²⁾		50	ns

⁽¹⁾ Typical values are at V_{CC} = 2.5 V, T_A = 25°C. (2) Pulse skew is defined as $|t_{PLH} - t_{PHL}|$ of each channel of the same device.



Auto-Powerdown Plus Section

Electrical Characteristics

over recommended ranges of supply voltage and operating free-air temperature,

 V_{CC} = 2.25 V to 3 V, C1–C4 = 0.1 μ F, T_A = T_{MIN} to T_{MAX} (unless otherwise noted) (see Figure 4)

PARAMETER	TEST CONDITIONS	MIN	MAX	UNIT
Receiver input threshold to INVALID high	Positive threshold		2.7	V
Receiver input threshold to invalid high	Negative threshold —2.7		V	
Receiver input threshold INVALID low		-0.3	0.3	V
INVALID, READY voltage low	I _{OUT} = 0.5 mA		0.1 × V _{CC}	V
ĪNVALID, READY voltage high	$I_{OUT} = -0.5 \text{ mA}$	0.8 × V _{CC}		V

Switching Characteristics

over recommended ranges of supply voltage and operating free-air temperature,

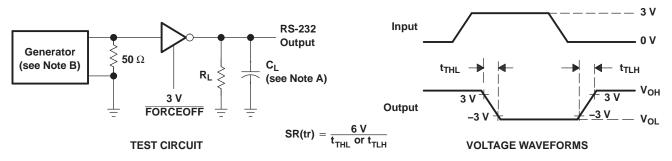
 V_{CC} = 2.25 V to 3 V, C1–C4 = 0.1 μ F, T_A = T_{MIN} to T_{MAX} (unless otherwise noted) (see Figure 4)

	PARAMETER	TEST CONDITIONS	MIN	TYP ⁽¹⁾	MAX	UNIT
t _{INVH}	Receiver positive or negative threshold to INVALID high	V _{CC} = 2.5 V		1		μs
t _{INVL}	Receiver positive or negative threshold to INVALID low	V _{CC} = 2.5 V		30		μs
t_{WU}	Receiver or driver edge to driver enabled	V _{CC} = 2.5 V		100		μs
t _{AUTOPRDN}	Receiver or driver edge to driver shutdown	V _{CC} = 2.5 V	15	30	60	s

⁽¹⁾ Typical values are at $V_{CC} = 2.5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.



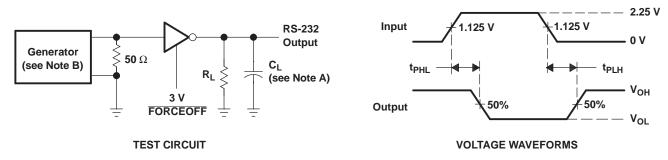
PARAMETER MEASUREMENT INFORMATION



NOTES: A. C_L includes probe and jig capacitance.

B. The pulse generator has the following characteristics: PRR = 250 kbit/s, Z_O = 50 Ω , 50% duty cycle, $t_r \le 10$ ns.

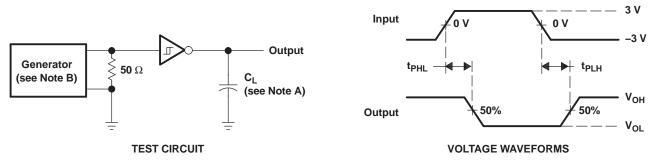
Figure 1. Driver Slew Rate



NOTES: A. C_L includes probe and jig capacitance.

B. The pulse generator has the following characteristics: PRR = 250 kbit/s, $Z_O = 50 \Omega$, 50% duty cycle, $t_r \le 10$ ns. $t_f \le 10$ ns.

Figure 2. Driver Pulse Skew



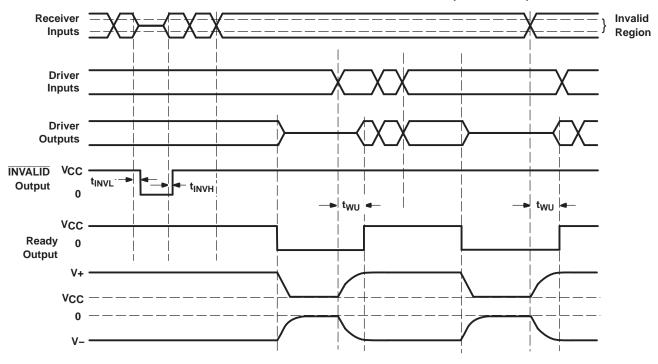
NOTES: A. C_L includes probe and jig capacitance.

B. The pulse generator has the following characteristics: $Z_0 = 50 \ \Omega$, 50% duty cycle, $t_f \le 10 \ ns$.

Figure 3. Receiver Propagation Delay Times



PARAMETER MEASUREMENT INFORMATION (continued)



VOLTAGE WAVEFORMS

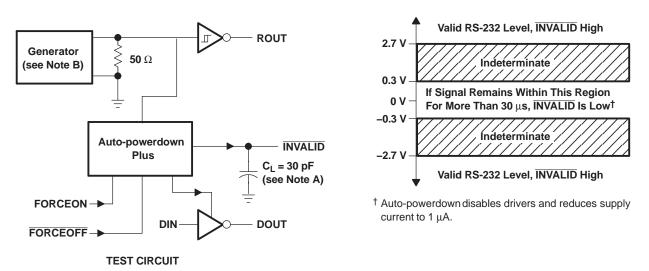


Figure 4. INVALID Propagation Delay Times and Supply Enabling Time



APPLICATION INFORMATION

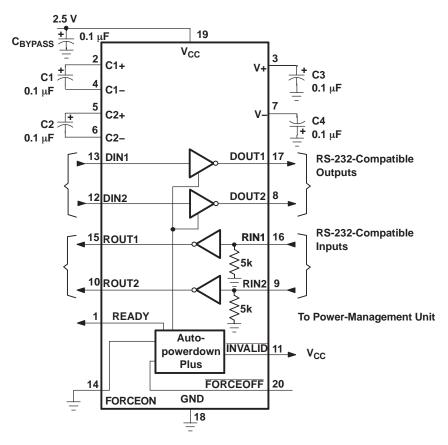


Figure 5. Typical Application Circuit

PACKAGE OPTION ADDENDUM

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PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp (3)
TRS3318ECDB	ACTIVE	SSOP	DB	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TRS3318ECDBG4	ACTIVE	SSOP	DB	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TRS3318ECDBR	ACTIVE	SSOP	DB	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TRS3318ECDBRG4	ACTIVE	SSOP	DB	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TRS3318ECPW	ACTIVE	TSSOP	PW	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TRS3318ECPWG4	ACTIVE	TSSOP	PW	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TRS3318ECPWR	ACTIVE	TSSOP	PW	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TRS3318ECPWRG4	ACTIVE	TSSOP	PW	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TRS3318EIDB	ACTIVE	SSOP	DB	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TRS3318EIDBG4	ACTIVE	SSOP	DB	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TRS3318EIDBR	ACTIVE	SSOP	DB	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TRS3318EIDBRG4	ACTIVE	SSOP	DB	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TRS3318EIPW	ACTIVE	TSSOP	PW	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TRS3318EIPWG4	ACTIVE	TSSOP	PW	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TRS3318EIPWR	ACTIVE	TSSOP	PW	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TRS3318EIPWRG4	ACTIVE	TSSOP	PW	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)



PACKAGE OPTION ADDENDUM

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(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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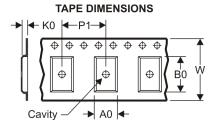
In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

PACKAGE MATERIALS INFORMATION

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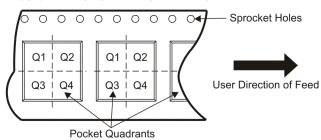
TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

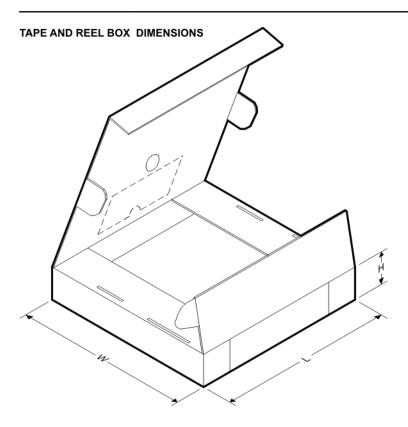
QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

All differsions are nominal												
Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TRS3318ECDBR	SSOP	DB	20	2000	330.0	16.4	8.2	7.5	2.5	12.0	16.0	Q1
TRS3318ECPWR	TSSOP	PW	20	2000	330.0	16.4	6.95	7.1	1.6	8.0	16.0	Q1
TRS3318EIDBR	SSOP	DB	20	2000	330.0	16.4	8.2	7.5	2.5	12.0	16.0	Q1
TRS3318EIPWR	TSSOP	PW	20	2000	330.0	16.4	6.95	7.1	1.6	8.0	16.0	Q1

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*All dimensions are nominal

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Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TRS3318ECDBR	SSOP	DB	20	2000	346.0	346.0	33.0
TRS3318ECPWR	TSSOP	PW	20	2000	346.0	346.0	33.0
TRS3318EIDBR	SSOP	DB	20	2000	346.0	346.0	33.0
TRS3318EIPWR	TSSOP	PW	20	2000	346.0	346.0	33.0

DB (R-PDSO-G**)

PLASTIC SMALL-OUTLINE

28 PINS SHOWN



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.

D. Falls within JEDEC MO-150

PW (R-PDSO-G**)

14 PINS SHOWN

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.

D. Falls within JEDEC MO-153

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