

NanoPower Supervisory Circuits

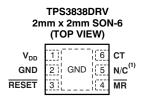
Check for Samples: TPS3836, TPS3837, TPS3838

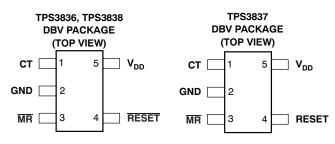
FEATURES

- Supply Current: 220 nA (typical)
- Precision Supply Voltage Supervision Range:
 1.8 V, 2.5 V, 3.0 V, and 3.3 V
- Power-On Reset Generator With Selectable Delay Time: 10 ms or 200 ms
- Push/Pull RESET Output (TPS3836), Push/Pull RESET Output (TPS3837), or Open-Drain RESET Output (TPS3838)
- Manual Reset
- SOT23-5 and 2x2 SON-6 Packages
- Temperature Range: -40°C to +85°C

APPLICATIONS

- Applications Using Low-Power DSPs, Microcontrollers, or Microprocessors
- Portable- and Battery-Powered Equipment
- · Intelligent Instruments
- Wireless Communication Systems
- Notebook Computers
- Automotive Systems
- Applications Using the MSP430™





(1) N/C: Not connected.

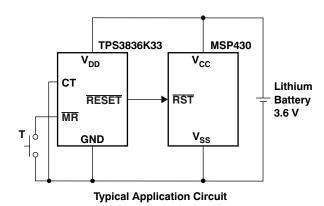
DESCRIPTION

The TPS3836, TPS3837, and TPS3838 families of supervisory circuits provide circuit initialization and timing supervision, primarily for DSP and processor-based systems.

During power-on, RESET is asserted when the supply voltage V_{DD} becomes higher than 1.1 V. Thereafter, the supervisory circuit monitors V_{DD} and keeps the RESET output active as long as V_{DD} remains below the threshold voltage of V_{IT} . An internal timer delays the return of the output to the inactive state (high) to ensure proper system reset. The delay time starts after V_{DD} has risen above the threshold voltage V_{IT} .

When CT is connected to GND, a fixed delay time of typical 10 ms is asserted. When connected to V_{DD} , the delay time is typically 200 ms. When the supply voltage drops below the threshold voltage V_{IT} , the output becomes active (low) again. All the devices of this family have a fixed-sense threshold voltage (V_{IT}) set by an internal voltage divider.

The TPS3836 has an active-low, push-pull RESET output. The TPS3837 has an active-high, push-pull RESET, and the TPS3838 integrates an active-low, open-drain RESET output. The product spectrum is designed for supply voltages of 1.8 V, 2.5 V, 3.0 V, and 3.3 V. The circuits are available in either a SOT23-5 or 2x2 SON-6 package. The TPS3836, TPS3837, and TPS3838 families are characterized for operation over a temperature range of -40°C to +85°C.



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This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

Table 1. ORDERING INFORMATION(1)

PRODUCT	NOMINAL SUPPLY VOLTAGE	THRESHOLD VOLTAGE (V _{IT}) ⁽²⁾
TPS383xE18	1.8 V	1.71 V
TPS383xJ25	2.5 V	2.25 V
TPS383xH30	3.0 V	2.79 V
TPS383xL30	3.0 V	2.64 V
TPS383xK33	3.3 V	2.93 V

⁽¹⁾ For the most current package and ordering information see the Package Option Addendum at the end of this document, or see the TI web site at www.ti.com.

ABSOLUTE MAXIMUM RATINGS(1)

Over operating free-air temperature range, unless otherwise noted.

	TPS383xx	UNIT			
Supply voltage, V _{DD} ⁽²⁾	7	V			
All other pins ^{(2) (3)}	-0.3 to 7	V			
Maximum low output current, I _{OL}	5	mA			
Maximum high output current, I _{OH}	-5	mA			
Input clamp current, I_{IK} ($V_I < 0$ or $V_I > V_{DD}$)	±10	mA			
Output clamp current, I_{OK} ($V_O < 0$ or $V_O > V_{DD}$)	±10	mA			
Continuous total power dissipation	See Dissipation Ratings Table				
Operating temperature range, T _A	-40 to +85	°C			
Storage temperature range, T _{STG}	-65 to +150	°C			
Soldering temperature	+260	°C			

⁽¹⁾ Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

DISSIPATION RATINGS

PACKAGE	T _A < +25°C POWER RATING	DERATING FACTOR ABOVE T _A = +25°C	T _A = +70°C POWER RATING	T _A = +85°C POWER RATING
DBV	437 mW	3.5 mW/°C	280 mW	227 mW
DRV Low-K ⁽¹⁾	715 mW	7.1 mW/°C	395 mW	285 mW
DRV High-K ⁽²⁾	1540 mW	15.4 mW/°C	845 mW	615 mW

⁽¹⁾ The JEDEC low-K (1s) board used to derive this data was a 3in x 3in, two-layer board with 2-ounce copper traces on top of the board.

⁽²⁾ Custom threshold voltages are available. Minimum order quantities apply. Contact factory for details and availability.

⁽²⁾ All voltage values are with respect to GND.

⁽³⁾ If RESET or RESET are pulled above V_{DD}, the internal ESD structure will present an effective 1.5 kΩ resistor between these pins, causing leakage current to flow into the RESET or RESET pin.

⁽²⁾ The JEDEC high-K (2s2p) board used to derive this data was a 3in x 3in, multilayer board with 1-ounce internal power and ground planes and 2-ounce copper traces on the top and bottom of the board.



RECOMMENDED OPERATING CONDITIONS

	MIN	MAX	UNIT
Supply voltage, V _{DD}	1.6	6	V
Voltage range, CT, MR, RESET, and RESET pins	0	V _{DD} + 0.3	V
High-level input voltage, V _{IH}	0.7 × V _{DD}		V
Low-level input voltage, V _{IL}		$0.3 \times V_{DD}$	V
Input transition rise and fall rate at \overline{MR} , $\Delta t/\Delta V$		100	ns/V
Operating temperature range, T _A	-40	+85	°C
Pull-up resistor value, RESET pin (TPS3838 only)	V _{Pull-up} 50 μA		Ω

ELECTRICAL CHARACTERISTICS

Over recommended operating conditions, upless otherwise noted

	PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT			
		RESET (TPS3836)	$V_{DD} = 3.3 \text{ V}, I_{OH} = -2 \text{ mA}$							
V _{OH}	High-level output voltage	,	$V_{DD} = 6 \text{ V}, I_{OH} = -3 \text{ mA}$	0.8 × V _{DD}			V			
ОП		RESET	$V_{DD} = 1.8 \text{ V}, I_{OH} = -1 \text{ mA}$							
		(TPS3837)	$V_{DD} = 3.3 \text{ V}, I_{OL} = -2 \text{ mA}$							
		RESET	$V_{DD} = 1.8 \text{ V}, I_{OL} = 1 \text{ mA}$							
V _{OL}	Low-level output voltage	(TPS3836, TPS3838)	$V_{DD} = 3.3 \text{ V}, I_{OL} = 2 \text{ mA}$			0.4	V			
·OL	zon iovoi output voitago	RESET	$V_{DD} = 3.3 \text{ V}, I_{OL} = 2 \text{ mA}$			0	·			
		(TPS3837)	$V_{DD} = 6 \text{ V}, I_{OL} = 3 \text{ mA}$							
	Power-up reset voltage ⁽¹⁾	TPS3836, TPS3838	V _{DD} ≥ 1.1 V, I _{OL} = 50 μA			0.2	V			
		TPS3837	$V_{DD} \ge 1.1 \text{ V}, I_{OL} = -50 \mu\text{A}$	0.8 × V _{DD}			V			
		TPS383xE18		1.66	1.71	1.74				
		TPS383xJ25		2.18	2.25	2.29				
V _{IT}	Negative-going input threshold voltage (2)	TPS383xH30	$T_A = -40$ °C to +85°C	2.70	2.79	2.85	V			
		TPS383xL30		2.56	2.64	2.69				
		TPS383xK33		2.84	2.93	2.99				
			1.7 V < V _{IT} < 2.5 V		30					
V_{HYS}	Hysteresis at V _{DD} input		2.5 V < V _{IT} < 3.5 V	40		mV				
			3.5 V < V _{IT} < 5 V		50					
	High level input gurrent	MR (3)	$\overline{MR} = 0.7 \times V_{DD}, V_{DD} = 6 V$	-40	-60	-100	μΑ			
I _{IH}	High-level input current	СТ	$CT = V_{DD} = 6 V$	-25		+25	nA			
	Low level input current	MR (3)	$\overline{MR} = 0 \text{ V}, \text{ V}_{DD} = 6 \text{ V}$	-130	-200	-340	μΑ			
I _{IL}	Low-level input current	CT	$CT = 0 V, V_{DD} = 6 V$	-25		+25	nA			
I _{OH}	High-level output current	TPS3838	$V_{DD} = V_{IT} + 0.2 \text{ V}, V_{OH} = V_{DD}$			25	nA			
			$V_{DD} > V_{IT}$, $V_{DD} < 3 V$		220	400	nA			
I_{DD}	Supply current		$V_{DD} > V_{IT}$, $V_{DD} > 3 V$		250	450	nA			
			$V_{DD} < V_{IT}$		10	15	μΑ			
	Internal pull-up resistor at MR				30		kΩ			
CI	Input capacitance at MR and C	Γ	$V_I = 0 V \text{ to } V_{DD}$		5		pF			

 ⁽¹⁾ The lowest voltage at which the RESET output becomes active. t_R, V_{DD} ≥ 15 µs/V.
 (2) To ensure best stability of the threshold voltage, a bypass capacitor (ceramic, 0.1 µF) should be placed near the supply terminal.
 (3) If manual reset is unused, MR should be connected to V_{DD} to minimize current consumption.



SWITCHING CHARACTERISTICS

At $T_A = +25$ °C, $R_L = 1$ M Ω , and $C_L = 50$ pF, unless otherwise noted.

	PARAMETER	₹	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
	$V_{DD} \ge V_{IT} + 0.2 \text{ V}, \overline{MR} = 0.7 \times V_{DD},$ CT = GND, (See Timing Diagram)				10	15		
t _D Delay time			$V_{DD} \ge V_{IT} + 0.2 \text{ V}, \overline{\text{MR}} = 0.7 \times V_{DD},$ CT = V_{DD} , (See Timing Diagram)	100	200	300	ms	
Propagation (delay) time,		V _{DD} to RESET	$V_{IL} = V_{IT} - 0.2 \text{ V}, V_{IH} = V_{IT} + 0.2 \text{ V}$			10		
	nigh-to-low-level output	delay (TPS3836, TPS3838)	V _{IL} = 1.6 V			50	0 μs	
	Propagation (delay) time,	V _{DD} to RESET	$V_{IL} = V_{IT} - 0.2 \text{ V}, V_{IH} = V_{IT} + 0.2 \text{ V}$			10	_	
t _{PLH}	low-to-high-level output	delay (TPS3837)	V _{IL} = 1.6 V		50	μS		
t _{PHL}	Propagation (delay) time, high-to-low-level output	MR to RESET delay (TPS3836, TPS3838)	$V_{DD} \ge V_{IT} + 0.2 \text{ V}, V_{IL} = 0.3 \times V_{DD}, V_{IL}$ = 0.7 × V_{DD}			0.1	μS	
t _{PLH}	Propagation (delay) time, low-to-high-level output	MR to RESET delay (TPS3837)	$V_{DD} \ge V_{IT} + 0.2 \text{ V}, V_{IL} = 0.3 \times V_{DD}, V_{IL} = 0.7 \times V_{DD}$			0.1	μЅ	

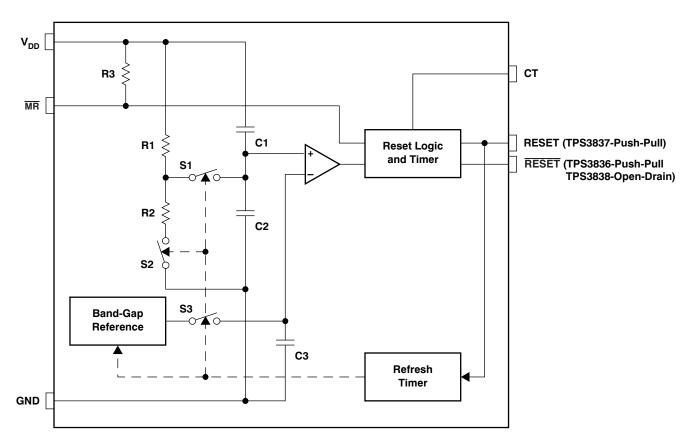
TIMING REQUIREMENTS

At $T_A = +25$ °C, $R_L = 1$ M Ω , and $C_L = 50$ pF, unless otherwise noted.

	PARAMETER	₹	TEST CONDITIONS	MIN	TYP	MAX	UNIT
	Dula a mialth	at V _{DD}	$V_{IH} = V_{IT} + 0.2 \text{ V}, V_{IL} = V_{IT} - 0.2 \text{ V}$	6			
t _W Pulse width	at MR	$V_{DD} \ge V_{IT} + 0.2 \text{ V}, V_{IL} = 0.3 \times V_{DD}, V_{IH} = 0.7 \times V_{DD}$	1			μS	



FUNCTIONAL BLOCK DIAGRAM



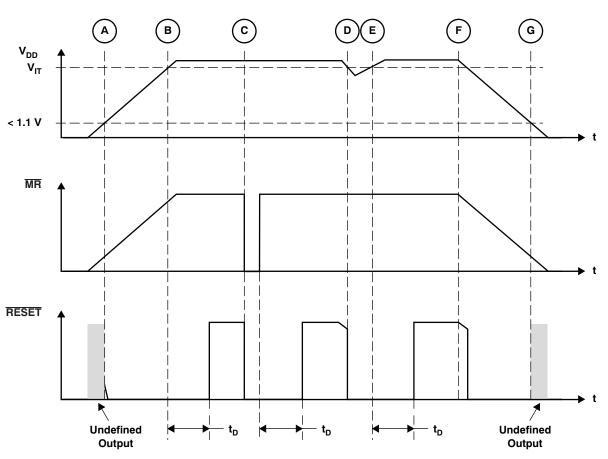
FUNCTION TABLE

MR	$V_{DD} > V_{IT}$	RESET (1)	RESET ⁽²⁾
L	0	L	Н
L	1	L	Н
Н	0	L	Н
Н	1	Н	L

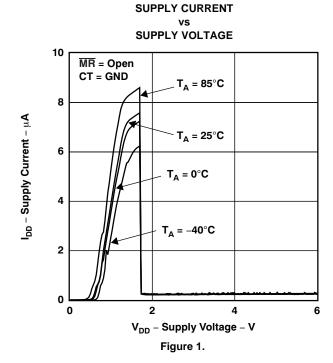
- (1) TPS3836 and TPS3838.
- (2) TPS3837.

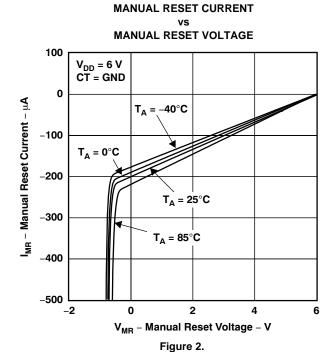


TIMING DIAGRAM



TYPICAL CHARACTERISTICS

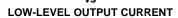


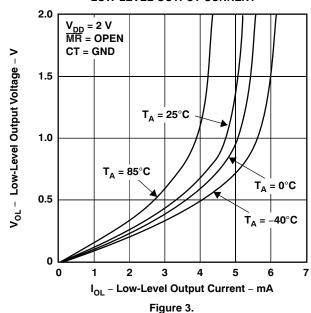




TYPICAL CHARACTERISTICS (continued)

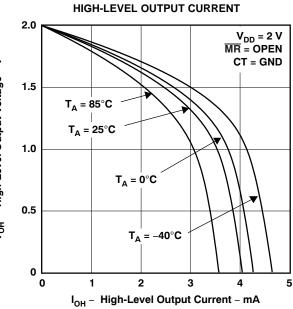
LOW-LEVEL OUTPUT VOLTAGE





V_{OH} - High-Level Output Voltage - V

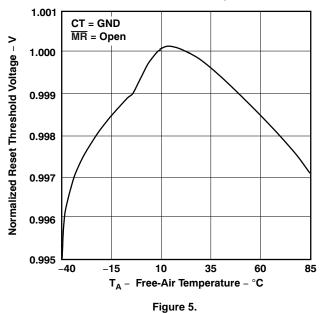
Minimum Pulse Duration at $V_{DD} - \mu s$



HIGH-LEVEL OUTPUT VOLTAGE

NORMALIZED RESET THRESHOLD VOLTAGE VS

FREE-AIR TEMPERATURE



MINIMUM PULSE DURATION AT V_{DD}

Figure 4.

V_{DD} THRESHOLD OVERDRIVE

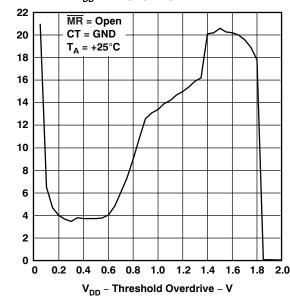


Figure 6.





2-Jun-2016

PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
TPS3836E18DBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDNI	Samples
TPS3836E18DBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDNI	Samples
TPS3836E18DBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDNI	Samples
TPS3836E18DBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDNI	Samples
TPS3836H30DBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PHRI	Samples
TPS3836H30DBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PHRI	Samples
TPS3836H30DBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PHRI	Samples
TPS3836H30DBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PHRI	Samples
TPS3836J25DBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDSI	Samples
TPS3836J25DBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDSI	Samples
TPS3836J25DBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDSI	Samples
TPS3836J25DBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDSI	Samples
TPS3836K33DBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDTI	Samples
TPS3836K33DBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDTI	Samples
TPS3836K33DBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDTI	Samples
TPS3836K33DBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDTI	Samples
TPS3836L30DBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PCAI	Samples





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Orderable Device	Status	Package Type		Pins	-	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
TPS3836L30DBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PCAI	Sample
TPS3836L30DBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PCAI	Sample
TPS3836L30DBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PCAI	Sample
TPS3837E18DBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDOI	Sample
TPS3837E18DBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDOI	Sample
TPS3837J25DBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDRI	Sample
TPS3837J25DBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDRI	Sample
TPS3837J25DBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDRI	Sample
TPS3837K33DBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDUI	Sample
TPS3837K33DBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDUI	Sample
TPS3837K33DBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDUI	Sample
TPS3837K33DBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDUI	Sample
TPS3837L30DBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PCBI	Sample
TPS3837L30DBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PCBI	Sample
TPS3837L30DBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PCBI	Sample
TPS3837L30DBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PCBI	Sample
TPS3838E18DBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDQI	Sample
TPS3838E18DBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDQI	Sample



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Orderable Device	Status	Package Type	Package	Pins	Package	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
TPS3838E18DBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDQI	Samples
TPS3838E18DBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDQI	Samples
TPS3838J25DBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDPI	Samples
TPS3838J25DBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDPI	Samples
TPS3838J25DBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDPI	Samples
TPS3838J25DBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDPI	Samples
TPS3838K33DBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDVI	Samples
TPS3838K33DBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDVI	Samples
TPS3838K33DBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDVI	Samples
TPS3838K33DBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDVI	Samples
TPS3838K33DRVR	ACTIVE	WSON	DRV	6	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	CCS	Samples
TPS3838K33DRVRG4	ACTIVE	WSON	DRV	6	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	CCS	Samples
TPS3838K33DRVT	ACTIVE	WSON	DRV	6	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	CCS	Samples
TPS3838K33DRVTG4	ACTIVE	WSON	DRV	6	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	CCS	Samples
TPS3838L30DBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PCCI	Samples
TPS3838L30DBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PCCI	Samples
TPS3838L30DBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PCCI	Samples
TPS3838L30DBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PCCI	Samples

PACKAGE OPTION ADDENDUM



2-.lun-2016

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead/Ball Finish Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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OTHER QUALIFIED VERSIONS OF TPS3836E18, TPS3836H30, TPS3836J25, TPS3836K33, TPS3836L30, TPS3837E18, TPS3837J25, TPS3837K33, TPS3837L30, TPS3838E18, TPS3838J25, TPS3838K33, TPS3838L30:

• Automotive: TPS3836E18-Q1, TPS3836H30-Q1, TPS3836J25-Q1, TPS3836K33-Q1, TPS3836L30-Q1, TPS3837E18-Q1, TPS3837J25-Q1, TPS3837K33-Q1, TPS3838L30-Q1, TPS3838E18-Q1, TPS3838J25-Q1, TPS3838K33-Q1, TPS3838L30-Q1



PACKAGE OPTION ADDENDUM

2-Jun-2016

• Enhanced Product: TPS3836J25-EP, TPS3836L30-EP, TPS3837K33-EP

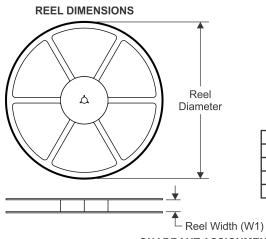
NOTE: Qualified Version Definitions:

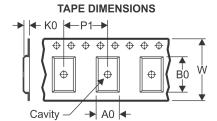
- Automotive Q100 devices qualified for high-reliability automotive applications targeting zero defects
- Enhanced Product Supports Defense, Aerospace and Medical Applications

PACKAGE MATERIALS INFORMATION

www.ti.com 2-Jun-2016

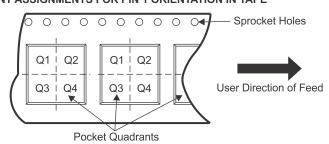
TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



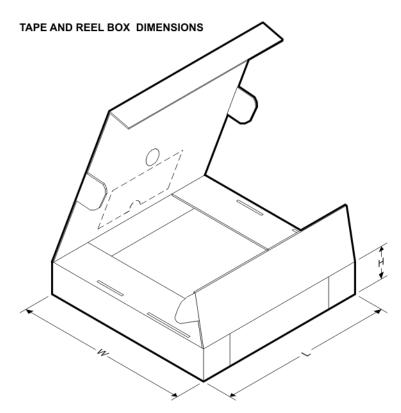
*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TPS3836E18DBVR	SOT-23	DBV	5	3000	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3836E18DBVT	SOT-23	DBV	5	250	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3836H30DBVR	SOT-23	DBV	5	3000	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3836H30DBVT	SOT-23	DBV	5	250	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3836J25DBVR	SOT-23	DBV	5	3000	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3836J25DBVT	SOT-23	DBV	5	250	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3836K33DBVR	SOT-23	DBV	5	3000	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3836K33DBVT	SOT-23	DBV	5	250	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3836L30DBVR	SOT-23	DBV	5	3000	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TPS3836L30DBVT	SOT-23	DBV	5	250	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3837E18DBVT	SOT-23	DBV	5	250	179.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TPS3837J25DBVR	SOT-23	DBV	5	3000	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3837J25DBVT	SOT-23	DBV	5	250	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TPS3837K33DBVR	SOT-23	DBV	5	3000	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3837K33DBVT	SOT-23	DBV	5	250	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3837L30DBVR	SOT-23	DBV	5	3000	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3837L30DBVT	SOT-23	DBV	5	250	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3838E18DBVR	SOT-23	DBV	5	3000	179.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3

PACKAGE MATERIALS INFORMATION

www.ti.com 2-Jun-2016

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TPS3838E18DBVT	SOT-23	DBV	5	250	179.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TPS3838J25DBVR	SOT-23	DBV	5	3000	179.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TPS3838J25DBVR	SOT-23	DBV	5	3000	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3838J25DBVT	SOT-23	DBV	5	250	179.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TPS3838J25DBVT	SOT-23	DBV	5	250	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3838K33DBVR	SOT-23	DBV	5	3000	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3838K33DBVR	SOT-23	DBV	5	3000	179.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TPS3838K33DBVT	SOT-23	DBV	5	250	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3838K33DBVT	SOT-23	DBV	5	250	179.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TPS3838K33DRVR	WSON	DRV	6	3000	179.0	8.4	2.2	2.2	1.2	4.0	8.0	Q2
TPS3838K33DRVT	WSON	DRV	6	250	179.0	8.4	2.2	2.2	1.2	4.0	8.0	Q2
TPS3838L30DBVR	SOT-23	DBV	5	3000	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TPS3838L30DBVR	SOT-23	DBV	5	3000	179.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TPS3838L30DBVT	SOT-23	DBV	5	250	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TPS3838L30DBVT	SOT-23	DBV	5	250	179.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS3836E18DBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TPS3836E18DBVT	SOT-23	DBV	5	250	180.0	180.0	18.0



PACKAGE MATERIALS INFORMATION

www.ti.com 2-Jun-2016

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS3836H30DBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TPS3836H30DBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TPS3836J25DBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TPS3836J25DBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TPS3836K33DBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TPS3836K33DBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TPS3836L30DBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TPS3836L30DBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TPS3837E18DBVT	SOT-23	DBV	5	250	203.0	203.0	35.0
TPS3837J25DBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TPS3837J25DBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TPS3837K33DBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TPS3837K33DBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TPS3837L30DBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TPS3837L30DBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TPS3838E18DBVR	SOT-23	DBV	5	3000	203.0	203.0	35.0
TPS3838E18DBVT	SOT-23	DBV	5	250	203.0	203.0	35.0
TPS3838J25DBVR	SOT-23	DBV	5	3000	203.0	203.0	35.0
TPS3838J25DBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TPS3838J25DBVT	SOT-23	DBV	5	250	203.0	203.0	35.0
TPS3838J25DBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TPS3838K33DBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TPS3838K33DBVR	SOT-23	DBV	5	3000	203.0	203.0	35.0
TPS3838K33DBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TPS3838K33DBVT	SOT-23	DBV	5	250	203.0	203.0	35.0
TPS3838K33DRVR	WSON	DRV	6	3000	203.0	203.0	35.0
TPS3838K33DRVT	WSON	DRV	6	250	203.0	203.0	35.0
TPS3838L30DBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TPS3838L30DBVR	SOT-23	DBV	5	3000	203.0	203.0	35.0
TPS3838L30DBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TPS3838L30DBVT	SOT-23	DBV	5	250	203.0	203.0	35.0

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