

## TN1205H

### High temperature 12 A SCRs

#### Datasheet – production data

### Features

- High junction temperature:  $T_i = 150 \text{ °C}$
- Medium current SCRs
- High noise immunity up to 150 °C
- RoHS (2002/95/EC) compliant
- 600 V V<sub>DRM</sub>, V<sub>RRM</sub>

### Application

- General purpose AC line load switching
- Motor control circuits
- Small home appliances
- Lighting
- Inrush current limiting circuits
- Over-voltage crowbar protection

### Description

Available in standard gate triggering levels, the TN1205H SCR series has very high switching capability up to junction temperature of 150 °C.

These products fit all modes of control found in applications such as overvoltage crowbar protection, motor control circuits in power tools and kitchen aids, inrush current limiting circuits, capacitive discharge ignition and voltage regulation circuits.

These products are particulary adapted for use in areas where the ambient temperature is high or the ventilation low, or where an increase of power density is required.

Through-hole or surface-mount packages provide performance in a limited space area.



#### Table 1.Device summary

Order code	Package	V <sub>DRM</sub> , V <sub>RRM</sub>	I <sub>GT</sub>	
TN1205H-6T	TO-220AB	600 V	2 to 5 mA	
TN1205H-6G	D <sup>2</sup> PAK	000 V	2 10 3 IIIA	

This is information on a product in full production.

## 1 Characteristics

Symbol	Parameter			Value	Unit	
I <sub>T(RMS)</sub>	On-state rms current (180° conduction angle)	nt (180° conduction angle) TO220-AB, T 126 °C				
I <sub>T(AV)</sub>	Average on-state current (180° conduction angle)	D <sup>2</sup> PAK	T <sub>c</sub> = 136 °C	7.6	А	
1	Non repetitive surge pack on state surrent $t_p = 8.3 \text{ ms}$		T - 25 °C	126	Α	
I <sub>TSM</sub> Non repetitive surge peak on-state current		t <sub>p</sub> = 10 ms	– T <sub>j</sub> = 25 °C	120	A	
l <sup>2</sup> t	$I^2$ t Value for fusing $t_p = 10 \text{ ms}$				A <sup>2</sup> S	
V <sub>DSM</sub> , V <sub>RSM</sub>	Non repetitive surge peak off-state voltage	t <sub>p</sub> = 10 ms	V <sub>DRM</sub> , V <sub>RRM</sub> +100	V		
dl/dt	$ \begin{array}{ c c c } \hline Critical \mbox{ rate of rise of on-state current } I_G = 2 \mbox{ x } I_{GT}, \\ t_r \leq 100 \mbox{ ns} \end{array} \  \  F = 60 \mbox{ Hz} \  \  T_j \label{eq:rate} $			100	A/µs	
I <sub>GM</sub>	Peak gate current	t <sub>p</sub> = 20 μs	T <sub>j</sub> = 150 °C	4	Α	
P <sub>G(AV)</sub>	Average gate power dissipation	1	W			
V <sub>RGM</sub>	Maximum peak reverse gate voltage	5	V			
T <sub>stg</sub> T <sub>j</sub>	Storage junction temperature range Operating junction temperature range	- 40 to + 150	°C			
TL	Maximum lead temperature for soldering during 10	260	°C			

#### Table 2. Absolute ratings (limiting values)

#### Table 3.Electrical characteristics (T<sub>i</sub> = 25 °C, unless otherwise specified)

Symbol	Test conditions	Test conditions			Unit
	V 10 V D 22 O		MIN.	2	
<sup>I</sup> GT	$I_{GT}$ $V_D = 12 V, R_L = 33 \Omega$		MAX.	5	mA
V <sub>GT</sub>	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega$		MAX.	1.3	V
V <sub>GD</sub>	$V_{D} = V_{DRM}, R_{L} = 3.3 \text{ k}\Omega $ MI			0.2	V
I <sub>Н</sub>	I <sub>T</sub> = 500 mA gate open		MAX.	20	mA
١L	$I_{G} = 1.2 I_{GT}$		MAX.	40	mA
dV/dt	V <sub>D</sub> = 67% V <sub>DBM</sub> gate open	T <sub>j</sub> = 125 °C	MIN.	200	V/µs
uv/ui	VD = 07 % VDRM gate open	T <sub>j</sub> = 150 °C	IVIIIN.	100	v/µs
t <sub>gt</sub>	$I_{TM}$ = 40 A, $V_D$ = 500 V, $I_G$ = 100 mA, $dI_G/dt$ = 5 A/µs		typ.	1.9	μs
t <sub>q</sub>	$ \begin{array}{l} V_{DM} = 335 \text{ V}, \text{ Tj} = 125 \ ^{\circ}\text{C}, \text{ I}_{TM} = 20 \text{ A}, \text{ V}_{R} = 25 \text{ V}, \ (\text{dI}_{T}/\text{dt})_{Max} = 30 \text{ A}/\mu\text{s}, \\ \text{dV}_{D}/\text{dt} = 50 \text{ V}/\mu\text{s}, \text{ R}_{GK} = 100 \ \Omega \end{array}  \right. \qquad \text{typ.} $			65	μs



Symbol	Test condi	Value	Unit		
V <sub>T</sub>	I <sub>TM</sub> = 24 A, t <sub>p</sub> = 380 μs	T <sub>j</sub> = 25 °C		1.6	V
V <sub>TD</sub>	Threshold voltage	T <sub>j</sub> = 150 °C		0.8	V
R <sub>d</sub>	Dynamic resistance	T <sub>j</sub> = 150 °C	MAX.	30	mΩ
		T <sub>j</sub> = 25 °C	WIAA.	5	μA
I <sub>DRM</sub> I <sub>RRM</sub>	$V_{DRM} = V_{RRM}$	T <sub>j</sub> = 125 °C		1	
		T <sub>j</sub> = 150 °C		3	mA

#### Table 4.Static characteristics

#### Table 5.Thermal resistance

Symbol	Parameter	Value Max.	Unit		
R <sub>th(j-c)</sub>	Junction to case (DC)			1.3	°C/W
P	lunction to ambient (DC)	$S^{(1)} = 1 \text{ cm}^2$	D <sup>2</sup> PAK	45	°C/W
R <sub>th(j-a)</sub> Junction to ambient (DC)			TO-220AB	60	0/11

1. S = Copper surface under tab

#### Figure 1. Maximum average power dissipation vs. average on-state current





<sup>\_Z</sup>th(j-a)

T<sub>p</sub>(s)

1.0E+03

1.0E+02

## Figure 3. Average and DC on-state current vs. ambient temperature



#### Figure 5. Relative variation of I<sub>GT</sub>,V<sub>GT</sub>, I<sub>H</sub>, I<sub>L</sub> vs. junction temperature (typical values)



1.0E+00

**Relative variation of thermal** 

impedance vs. pulse duration

TO-220AB

1.0E+01











Figure 4.

1.00

0.10

0.01

1.0E-03

 $K = [Z_{th} / R_{th}]$ 

1.0E-02

z<sub>th(j-c)</sub>

D<sup>2</sup>PAK

S<sub>cu</sub> = 1 cm<sup>2</sup>

(Epoxy Fr4)

1.0E-01

≠f¶|



# Figure 9. On-state characteristics (maximum values)





Figure 11. Thermal resistance junction to ambient vs. copper surface under tab (D<sup>2</sup>PAK, printed circuit board FR4, copper thickness: 35 μm)





## 2 Ordering information scheme

Figure 12.	Orderina	information	scheme
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Voltage $6 = 600 V$ PackageT = TO-220ABG = $D^2 PAK$	Standard SCRCurrent $12 = 12 \text{ A}$ Sensitivity $05 = 2 \text{ to 5 mA}$ Junction temperatureH = 150 °C	TN 12 05 H - 6 G - TR
Package T = TO-220AB	Voltage	
	Package T = TO-220AB	



### 3 Package information

- Epoxy meets UL94, V0
- Lead-free package

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <u>www.st.com</u>. ECOPACK<sup>®</sup> is an ST trademark.

Table 6. TO-220AB dimensions

					Dimer	nsions		
		Ref.	Ref. Millimeters		rs	inches		
			Min.	Тур.	Max.	Min.	Тур.	Max.
		А	15.20		15.90	0.598		0.625
		a1		3.75			0.147	
Ø I	b2	a2	13.00		14.00	0.511		0.551
		В	10.00		10.40	0.393		0.409
	F	b1	0.61		0.88	0.024		0.034
A		b2	1.23		1.32	0.048		0.051
14 I3		С	4.40		4.60	0.173		0.181
		c1	0.49		0.70	0.019		0.027
		c2	2.40		2.72	0.094		0.107
a2		e	2.40		2.70	0.094		0.106
	M =	F	6.20		6.60	0.244		0.259
→□+ →□+ →□+ →□+ →□+ →□+ →□+ →□+ →□+ →□+	tinit tinit	ØI	3.75		3.85	0.147		0.151
		14	15.80	16.40	16.80	0.622	0.646	0.661
		L	2.65		2.95	0.104		0.116
		12	1.14		1.70	0.044		0.066
		13	1.14		1.70	0.044		0.066
		М		2.60			0.102	



					Dimer	nsions		
		Ref.	Mi	illimete	ers		Inches	
			Min.	Тур.	Max.	Min.	Тур.	Max.
		Α	4.30		4.60	0.169		0.181
	<b>▲</b> →	A1	2.49		2.69	0.098		0.106
	C2→→	A2	0.03		0.23	0.001		0.009
	В	0.70		0.93	0.027		0.037	
	C	B2	1.25	1.40		0.048	0.055	
		С	0.45		0.60	0.017		0.024
		C2	1.21		1.36	0.047		0.054
		D	8.95		9.35	0.352		0.368
G		Е	10.00		10.28	0.393		0.405
		G	4.88		5.28	0.192		0.208
	2mm min. FLAT ZONE	L	15.00		15.85	0.590		0.624
	V2	L2	1.27		1.40	0.050		0.055
	- · ·	L3	1.40		1.75	0.055		0.069
		R		0.40			0.016	
		V2	0°		8°	0°		8°

Table 7.D<sup>2</sup>PAK Dimensions







## 4 Ordering information

### Table 8. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
TN1205H-6T	TN1205H6T	TO-220AB	2.0 g	50	Tube
TN1205H-6G	TN1205H6G	D <sup>2</sup> PAK	1.5 g	50	Tube
TN1205H-6G-TR	TN1205H6G	D <sup>2</sup> PAK	1.5 g	1000	Tape and reel

## 5 Revision history

#### Table 9. Document revision history

Date	Revision	Changes
17-Feb-2011	1	First issue.
26-Sep-2011	2	Corrected typographical error in Features and Description.
17-Jan-2012	3	Updated units for t <sub>gt</sub> in <i>Table 3</i> .
26-Apr-2012	4	Moved junction temperature to top of features list. Description reworded for readability. No technical changes.



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