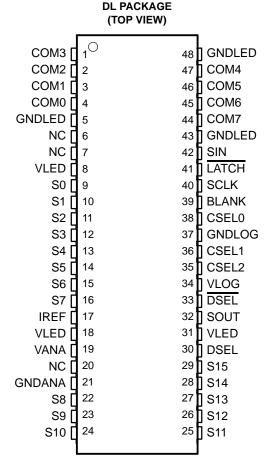
SLAS264A - MARCH 2000 REVISED SEPTEMBER 2002

- Drive Capability:
 - Segment . . . 30 mA \times 16 Bits
 - Common . . . 640 mA
- Constant Current Output . . . 3 mA to 30 mA (Current Value Setting for All Channels Using External Resistor)
- Constant Current Accuracy ±6% (Maximum Error Between Bits)
- Data Input: Clock Synchronized Serial Input
- LED Type Applied Cathode Common
- Logic Power Supply Voltage 4.5 V to 5.5 V
- LED Power Supply Voltage 4.5 V to 5.5 V
- Operating Frequency . . . 10 MHz
- Operating Free-Air Temperature Range –20°C to 85°C
- 48-Pin SSOL Package

description

The TLC5920 is an LED driver incorporating a 16-channel shift register, data latch, and constant current circuitry with current value control and 8-channel common driver into a single chip. The constant output current is capable of 30 mA for 16 bits simultaneously, and the current value can be set by one external register. This device also includes a 16-bit segment driver and 8-bit common driver; therefore, the monocolor LED array with 16 \times 8 dots can be driven by only one TLC5920, and a two-color LED array with 16 \times 16 dots can be driven by two TLC5920s.

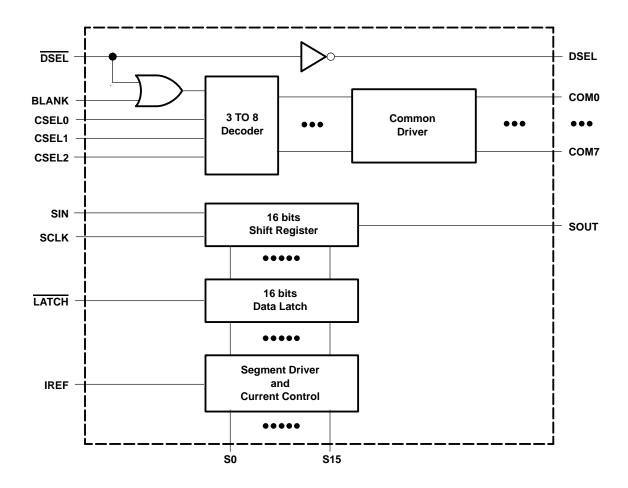




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functional block diagram





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Terminal Functions

TERMINAL									
NAME	NO.	1/0	DESCRIPTION						
BLANK	39	I	Blank(light off). By turning all the output for the common driver off, the LED is turned off. When BLANK is high, the LED is turned off.						
COM0 – COM7	4, 3, 2, 1, 47, 46, 45, 44	0	LED common driver output						
CSEL0 – 2	38, 36, 35	-	Common driver select. One terminal out of COM0 through COM7 is selected. 2 1 0 Common Driver L L L 0 L L H 1 L H L 2 L H H 3 H L L 4 H L H 5 H H H 6 H H H 7						
DSEL	33	I	Display select. When DSEL is high, the LED is turned off. Note that, when BLANK is high, the LED is turned off with no regard to the DSEL input.						
DSEL	30	0	Display select output. The inverted data of DSEL is clocked out.						
GNDANA	21		Analog ground						
GNDLED	5, 43, 48		LED driver ground						
GNDLOG	37		Logic ground						
IREF	17	I	Constant current control setting. The LED current is set to the desired value by connecting an external resistor between IREF and GND.						
LATCH	41	I	Latch. When LATCH is high, data on the shift register goes through latch. When LATCH is low, data is latched.						
SIN	42	I	Serial input for display						
SOUT	32	0	Serial output for display						
SCLK	40	I	Synchronous clock input for serial data transfer. The input data of SIN is synchronized to the rising edge of SCLK, and transferred to SOUT.						
S0 – S15	9, 10, 11, 12, 13, 14, 15, 16, 22, 23, 24, 25, 26, 27, 28, 29	0	LED segment driver output						
VANA	19		Analog power supply voltage						
VLOG	34		Logic power supply voltage						
VLED	8, 18, 31		LED driver power supply voltage						



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absolute maximum ratings† (see Note 1)

Logic supply voltage, V _(LOG)	– 0.3 V to 7 V
LED supply voltage, V _(LED)	– 0.3 V to 7 V
Analog supply voltage, V _(ANA)	
Output current, I _{OH(S)}	
Output current, I _{OL(C)}	
Input voltage range, V _I	0.3 V to $V_{(I,OG)}$ + 0.3 V
Output voltage range, V _O	– 0.3 V to $V_{(LOG)}$ + 0.3 V
Continuous total power dissipation	
Thermal resistance	83°C/W
Operating free-air temperature range (see Note 2), T _A	– 20 to 85°C
Storage temperature range, T _{stq}	–40°C to 125°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

dc characteristics (see Note 3)

PARAMETER	TEST CONDITIONS	MIN	NOM	MAX	UNIT
Logic supply voltage, V _(LOG)		4.5	5	5.5	V
LED supply voltage, V _(LED)		4.5	5	5.5	V
Analog power supply, V _(ANA)		4.5	5	5.5	V
Voltage between GND and V _(DEF) , G _(DEF)	$G_{(DEF)} = GND_{(LOG)} - GND_{(LED)}$	-0.3	0	0.3	V
High-level input voltage, VIH		2.0		V(LOG)	V
Low-level input voltage, V _{IL}		GND(LOG)		0.8	V
High-level output current, IOH	V _(LOG) = 4.5V, SOUT, DSEL			-1	mA
High-level output current, IOH(S)	S0 to S15			-30	IIIA
Low-level output current, IOL	$V_{(LOG)} = 4.5V$, SOUT, DSEL			1.6	mA
Low-level output current, IOL(C)	DUTY = 1/16, COM0 to COM7			640	ША
Operating free-air temperature range, T _A (see Note 2)		-20		85	°C

NOTES: 2 $T_J \le 150$ °C (refer to appendix thermal condition).

ac characteristics ($T_A = -20^{\circ}C$ to $85^{\circ}C$)

	PARAMETER	TEST CONDITIONS	MIN	NOM	MAX	UNIT
f(SCLK)	Shift clock frequency				10	MHz
tw(H)/tw(I)	SCLK pulse duration (high- or low-level)		40			ns
t _r /t _f	Rise/fall time				100	ns
	Satura timo	SIN - SCLK	10			no
t _{su}	Setup time	SCLK – LATCH	10			ns
tu.	Hold time	LATCH - SCLK	10			ns
^t h	Tiola time	SIN - SCLK	10			115



NOTES: 1. All voltage values are with respect to GND terminal.

^{2.} $T_J \le 150^{\circ}C$ (refer to appendix thermal condition).

^{3.} VANA must be same as VLED.

electrical characteristics (unless otherwise noted),

MIN/MAX: $V_{(LOG)} = V_{(ANA)} = V_{(LED)} = 4.5 \text{ V to } 5.5 \text{ V}, T_A = -20^{\circ}\text{C to } 85^{\circ}\text{C}$ TYP: $V_{(LOG)} = V_{(ANA)} = V_{(LED)} = 5 \text{ V}, T_A = 25^{\circ}\text{C}$

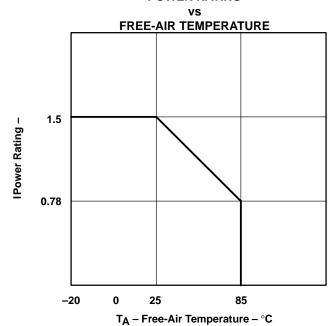
	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Vон	High-level output voltage	I _{OH} = -1 mA, SOUT, DSEL	3.6			V
\/o.	Low lovel output voltage	I _{OL} = 1.6 mA, SOUT, DSEL			0.6	V
VOL	Low-level output voltage	I _{OL} = 640 mA, COM0 to COM7		0.6	0.9	٧
lį	Input current	$V_I = V_{(LOG)}$ or $GND_{(LOG)}$			±1	μΑ
I(LOG)		Data transfer, SCLK = 10 MHz			0.1	
I(LED)	Supply current	LED is turned off		0.8	1.6	mA
I(ANA)		LED is turned off		0.8	1.6	
IOH(S03)		$V(Sn) = 2.5 \text{ V}, R(IREF) = 4200 \Omega$	-2.1	-3	-3.9	
IOH(S10)	Segment current	$V(Sn) = 2.5 \text{ V}, R(IREF) = 1260 \Omega$	-8.5	-10	-11.5	mA
I _{OH(S20)}	Segment current	$V_{(Sn)} = 2.5 \text{ V}, R_{(IREF)} = 630 \Omega$	-17	-20	-23	IIIA
IOH(S30)		$V(Sn) = 2.5 \text{ V}, R(IREF) = 420 \Omega$	-25.5	-30	-34.5	
ΔlOH(S)	Segment current error between bits	$V(LED) = 5 \text{ V}, R(IREF) = 630 \ \Omega, \ V(Sn) = 2.5 \text{ V}$		±3%	±6%	
V _{REF}	Voltage reference		1.2	1.26	1.3	V

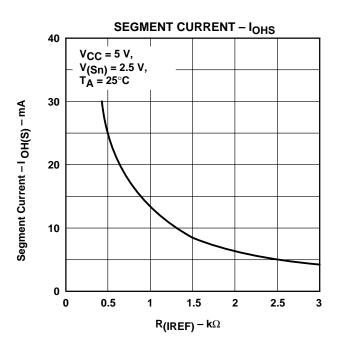
switching characteristics, C_L = 15 pF

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT		
		SOUT			40			
١.	Rise time	DSEL			40			
t _r	Rise time	COMn			80	ns		
		Sn			80			
		SOUT			40			
	Fall time	DSEL			40	20		
t _f	raii time	COMn			40	ns		
		Sn			40			
		LATCH - Sn			40			
. .	Drangation delay time		40					
^t d	Propagation delay time	SCLK - SOUT			40	ns		
		DSEL - DSEL			40			
		CSELn - COMn			120			
t(DLH)	Propagation delay time	Propagation delay time DSEL - COMn						
		BLANK - COMn			120			
		CSELn - COMn			40			
t(DHL)	Propagation delay time	DSEL - COMn			40	ns		
		BLANK - COMn			40			

PARAMETER MEASUREMENT INFORMATION

POWER RATING

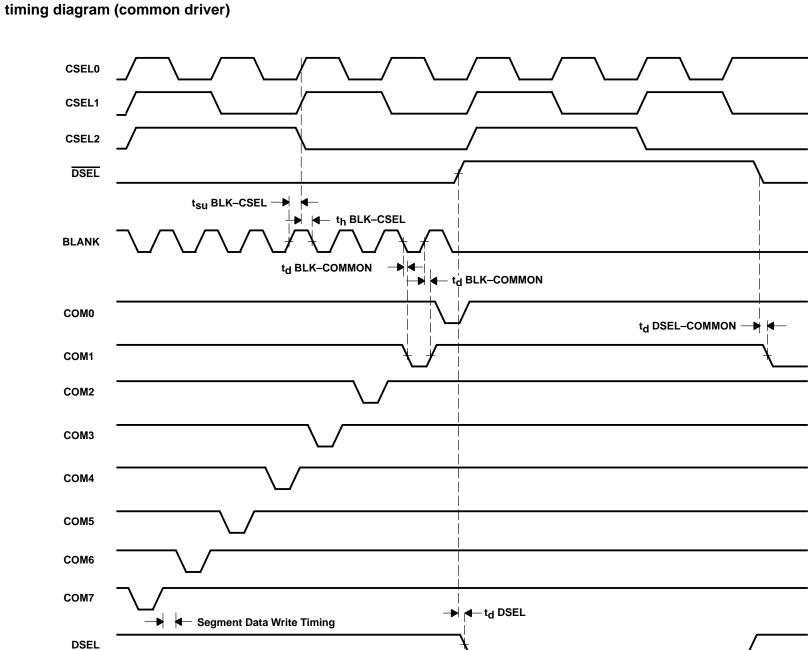




$$I_{OH(S)} = \frac{V_{REF}}{R_{(IREF)}} \times 10$$



TLC5920 16x8 BIT LED DRIVER/CONTROLLER



Release Date: 7-11-94

16x8 BIT LED DRIVER/CONTROLLER

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D0B

(D15A)

D15A

D1A

D0A

D14A

D1B

D0B

D0B

D2B

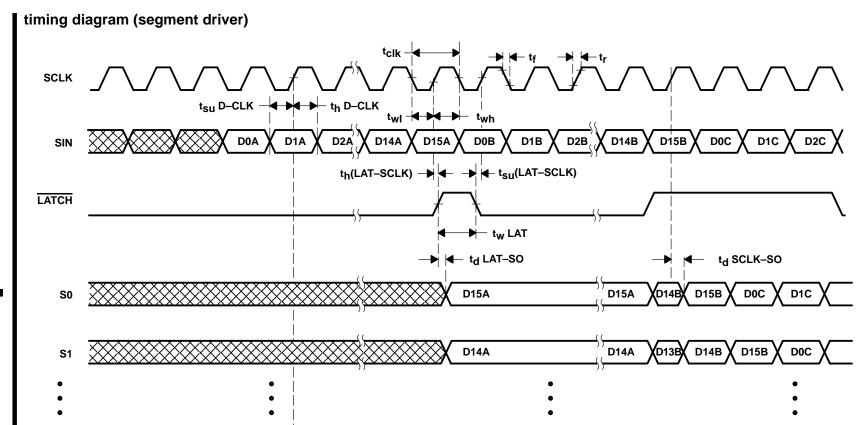
D1B

D01B

D3B

D2B

D2B



D1A

D0A

D0A

D1A

D2A

← t_d SCLK-SOUT



SOUT

APPLICATION INFORMATION

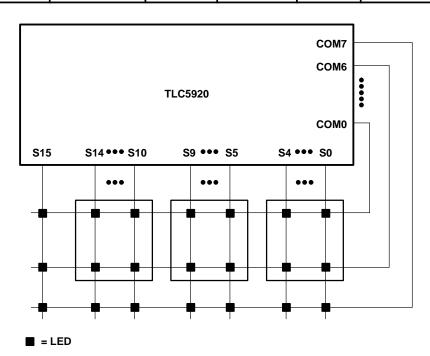
example 1

The other remaining terminals used for dot matrix LED drive can be utilized for LED lamp drive and other displays.

LEDs driven by TLC5920

cathode common type

	LED		TLC5920	DUTY	DRIVE CURRENT
TYPE	NO. OF COLOR	QUANTITY	QUANTITY	ווטע	(mA)
LAMP	Mono	16	1	Static	30
LAIVIP	Two	8 1	Static	30	
7 SEGMENT	Mono	16	1	1/8	30
7 SEGIVIEINT	Two	8	1	1/8	30
5 x 7	Mono	3	1	1/8	30
5 X 7	Two	1	1	1/8	30
8 x 8	Mono	2	1	1/8	30
0 X 0	Two	1	1	1/8	30
	Mono	2	2	1/16	20
16 x 16	Two	1	2	1/16	20
	Three	1	3	1/16	13
24 x 24	Mono	2	3	1/24	13
24 X 24	Two	1	3	1/24	13

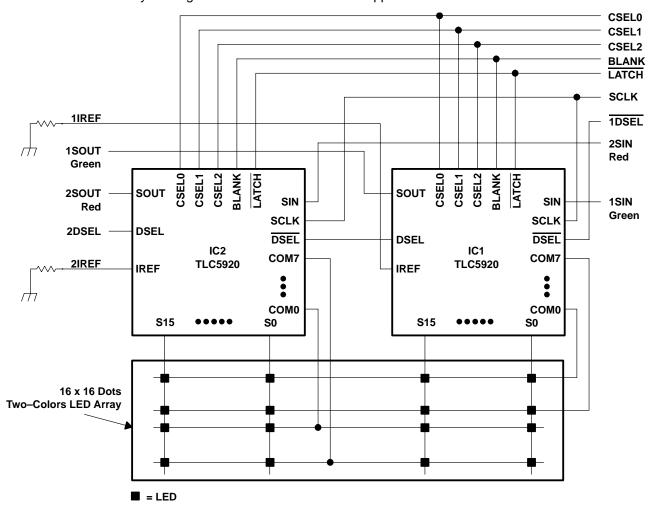




APPLICATION INFORMATION

example 2

Using two TLC5920s, an LED with two colors and 16 x 16 dots can be driven. The number of LED arrays can also be increased by making a cascade connection in the application circuit.



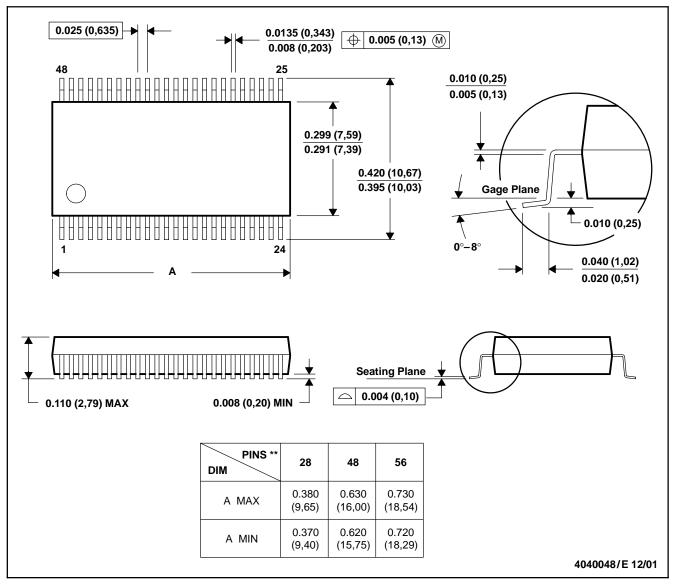


MECHANICAL DATA

DL (R-PDSO-G**)

48 PINS SHOWN

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
- D. Falls within JEDEC MO-118





.com 6-Dec-2006

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
TLC5920DL	ACTIVE	SSOP	DL	48	25	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
TLC5920DLG4	ACTIVE	SSOP	DL	48	25	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
TLC5920DLR	ACTIVE	SSOP	DL	48	1000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
TLC5920DLRG4	ACTIVE	SSOP	DL	48	1000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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PACKAGE MATERIALS INFORMATION

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TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

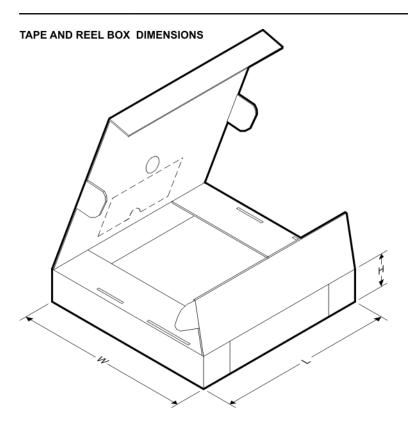
QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device		Package Drawing			Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLC5920DLR	SSOP	DL	48	1000	330.0	32.4	11.35	16.2	3.1	16.0	32.0	Q1

www.ti.com 9-May-2009



*All dimensions are nominal

Device	Package Type	Package Type Package Drawing		SPQ	Length (mm)	Width (mm)	Height (mm)
TLC5920DLR	SSOP	DL	48	1000	346.0	346.0	49.0

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