TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSVII)

TK6A60D

Switching Regulator Applications

Low drain-source ON-resistance: RDS (ON) = 1.0Ω (typ.)

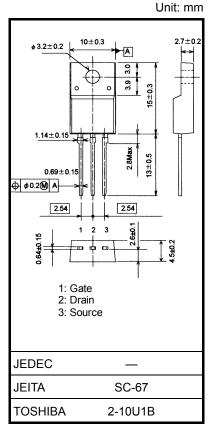
• High forward transfer admittance: $|Y_{fs}| = 3.0 \text{ S (typ.)}$

• Low leakage current: $I_{DSS} = 10 \mu A (V_{DS} = 600 V)$

• Enhancement mode: $V_{th} = 2.0 \text{ to } 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	600	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	6		
	Pulse (t = 1 ms) (Note 1)	I _{DP}	24	Α	
Drain power dissipation (Tc = 25°C)		P _D	40	W	
Single pulse avalanche energy (Note 2)		E _{AS}	173	mJ	
Avalanche current		I _{AR}	6	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	4.0	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

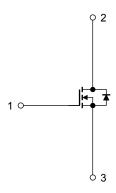
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	3.125	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 8.4 mH, $R_G = 25 \Omega$, $I_{AR} = 6 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.





Electrical Characteristics (Ta = 25°C)

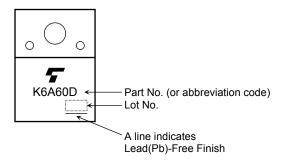
Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off current		I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	_	_	10	μА
Drain-source brea	akdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600	_	_	V
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source ON-	-resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 3 A	_	1.0	1.25	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 3 A	0.8	3.0	_	S
Input capacitance		C _{iss}		_	800	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	4	_	
Output capacitan	Output capacitance				100	_	
Tui	Rise time	t _r	$\begin{array}{c c} 10 \text{ V} & \text{ID} = 3 \text{ A} & \text{Vout} \\ \hline VGS & \\ 0 \text{ V} & \\ \hline SO \Omega & \\ \end{array}$ $\begin{array}{c} RL = \\ 67 \Omega \\ \end{array}$ $\begin{array}{c} VDD \approx 200 \text{ V} \end{array}$	_	20	_	
	Turn-on time	t _{on}		_	40	_	
	Fall time	t _f		_	12	_	ns
	Turn-off time	t _{off}	Duty \leq 1%, $t_W = 10 \mu s$		60	_	
Total gate charge		Qg		_	16	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 6\text{A}$	_	10	_	nC
Gate-drain charge		Q _{gd}		_	6		

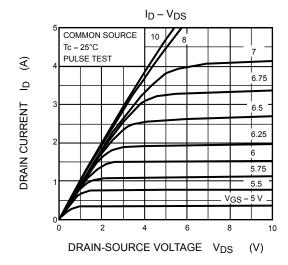
Source-Drain Ratings and Characteristics (Ta = 25°C)

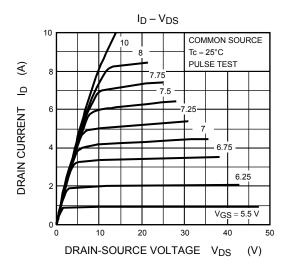
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	6	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	24	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 6 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 6 A, V _{GS} = 0 V,	_	1200	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs	_	10	_	μС

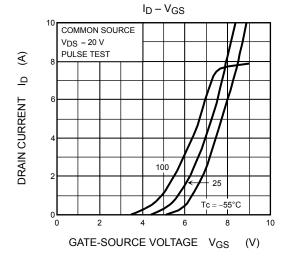
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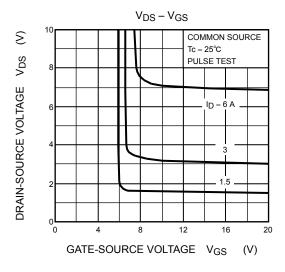
Marking

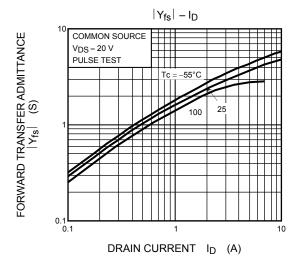


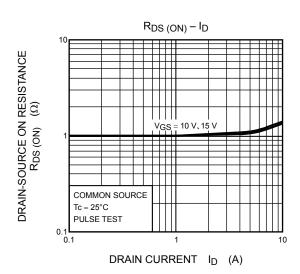




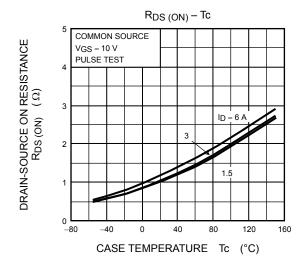


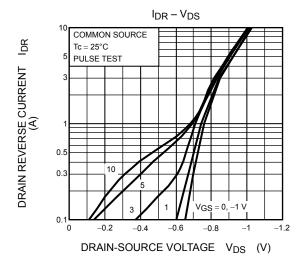


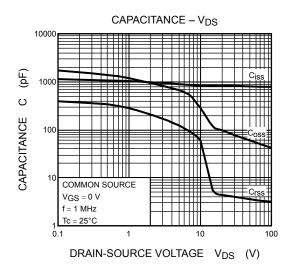


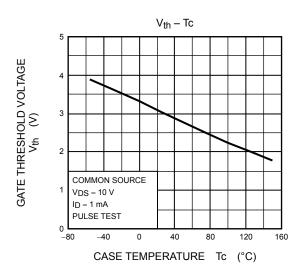


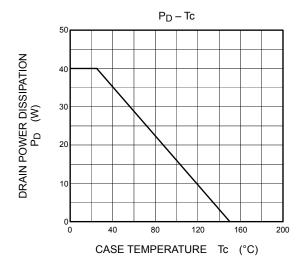
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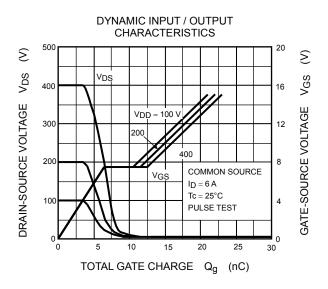




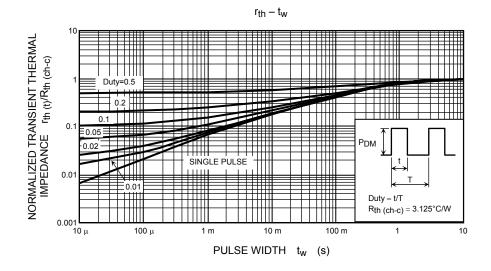


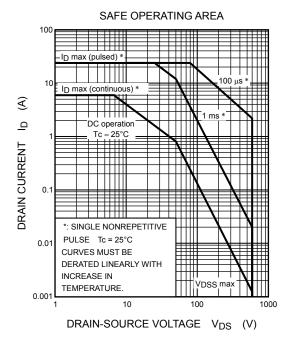


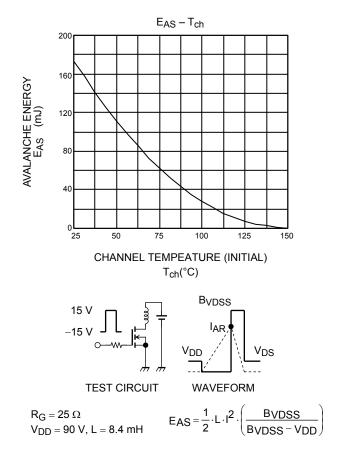




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20070701-EN GENERAL

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