TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TD62064P,TD62064AP,TD62064F,TD62064AF TD62074P,TD62074AP,TD62074F,TD62074AF

#### 4CH HIGH-CURRENT DARLINGTON SINK DRIVER

The TD62064P / AP / F / AF and TD62074P / AP / F / AF are high–voltage, high–current darlington drivers comprised of four NPN darlington pairs.

All units feature integral clamp diodes for switching inductive loads and all units of TD62074P / AP / F / AF feature uncommitted collectors and emitters for isolated darlington applications.

For proper operation, the substrate (SUB) must be connected to the most negative voltage.

Applications include relay, hammer, lamp and stepping moter drivers.

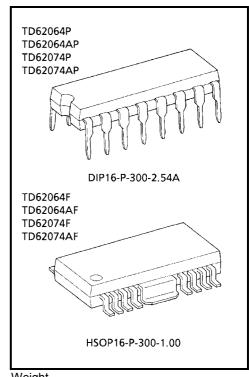
### **FEATURES**

- Output current (single output) 1.5 A (Max)
- High sustaining voltage output
  V (Min) (TD62064P / F, 074P / F)
  V (Min) (TD62064AP / AF, 074AP / AF)

• Output clamp diodes : TD62064P / AP / F / AF

• Isolated darlington array: TD62074P/AP/F/AF

- Input compatible with TTL and 5 V CMOS
- GND and SUB terminal = heat sink
- Package type-P, AP: DIP-16 pin
- Package type-F, AF: HSOP-16 pin

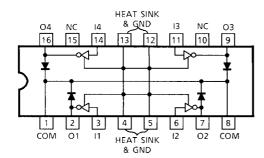


Weight

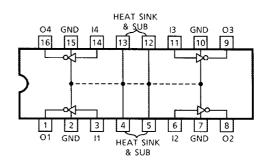
DIP16-P-300-2.54A : 1.11 g (Typ.) HSOP16-P-300-1.00 : 0.50 g (Typ.)

# **PIN CONNECTION (TOP VIEW)**

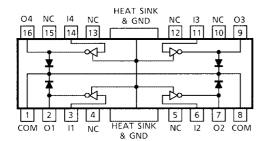
### TD62064P / AP



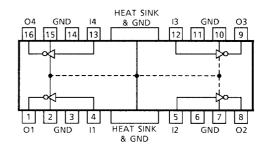
### TD62074P / AP



### TD62064F / AF

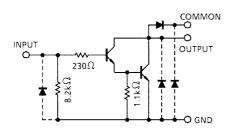


#### TD62074F / AF

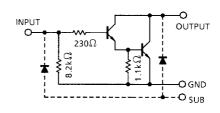


# **SCHEMATICS (EACH DRIVER)**

### TD62064P / AP / F / AF



### TD62074P / AP / F / AF



Note: The input and output parasitic diodes cannot be used as clamp diodes.

# MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT		
Output Sustaining	P, F	Vor. (0110)	-0.5~35	V		
Voltage	AP, AF	V <sub>CE</sub> (SUS)	-0.5~50			
Output Current	out Current		1.5	A / ch		
Input Current		I <sub>IN</sub>	50	mA		
Input Voltage		V <sub>IN</sub>	-0.5~17	V		
Clamp Diode Reverse Voltage	P, F	\/_ (Note 1)	35	V		
	AP, AF	V <sub>R</sub> (Note 1)	50	V		
Clamp Diode Forward C	urrent	I <sub>F</sub> (Note 1)	1.5	A / ch		
	P, F	V <sub>SUB</sub>	35	V		
Isolated Voltage	AP, AF	(Note 2)	50	V		
5 5	P, AP	Б	1.47 / 2.7 (Note 3)	W		
Power Dissipation	F, AF	P <sub>D</sub>	0.9 / 1.4 (Note 4)			
Operating Temperature		T <sub>opr</sub>	-40~85	°C		
Storage Temperature		T <sub>stg</sub>	-55~150	°C		

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Note 1: TD62064P / AP / F / AF Note 2: TD62074P / AP / F / AF

Note 3: On Glass Epoxy PCB (50 × 50 × 1.6 mm Cu 50%) Note 4: On Glass Epoxy PCB (60 × 30 × 1.6 mm Cu 30%)



# RECOMMENDED OPERATING CONDITIONS (Ta = -40~85°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION		MIN	TYP.	MAX	UNIT	
Output Sustaining Voltage	P, F	VCE (SUS)		0	_	35	V		
	AP, AF			0	_	50			
			DC1 Circuit, Ta = 2	0	_	1250			
Output Current	P, AP (Note 1)	Гоит	T <sub>pw</sub> = 25 ms 4 Circuits T <sub>j</sub> = 120°C Ta = 85°C	Duty = 10 %	0	_	1250	mA / ch	
				Duty = 50 %	0	_	390		
	5 A5 (N + 0)			Duty = 10 %	0	_	907		
	F, AF (Note 2)			Duty = 50 %	0	_	172		
,		V <sub>IN</sub>		0	_	8	V		
Input Voltage	(Output On)	V <sub>IN (ON)</sub>	I <sub>OUT</sub> = 1.25 A	2.5	_	8	V		
	(Output Off)	V <sub>IN (OFF)</sub>		0	_	0.4	V		
Input Current		I <sub>IN</sub>			0	_	20	mA	
Clamp Diode Reverse Voltage	P, F	V-	TD62064P / AP / F / AF		0	_	35	V	
	AP, AF	$V_{R}$	1002004F / AF / F	0	_	50			
Clamp Diode Forward Current		l <sub>F</sub>			_	_	1.25	Α	
Isolation Voltage	P, F	\/	TD62074P / AP / F / AF		_	_	35	V	
	AP, AF	$V_{SUB}$	1002074F / AF / F	_	_	50			
Power Dissipation	P, AP	D-	Ta = 85°C	(Note 1)	_	_	1.4	W	
	F, AF	$P_{D}$	Ta = 85°C	(Note 2)	_	_	0.7	VV	

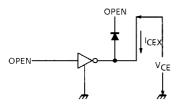
Note 1: On Glass Epoxy PCB ( $50 \times 50 \times 1.6$  mm Cu 50%) Note 2: On Glass Epoxy PCB ( $60 \times 30 \times 1.6$  mm Cu 30%)

# **ELECTRICAL CHARACTERISTICS (Ta = 25°C)**

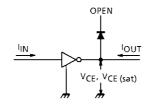
CHARACTERIS	STIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION		MIN	TYP.	MAX	UNIT		
Output Leakage Current	AP, AF	I <sub>CEX</sub>	1	V <sub>CE</sub> = 50 V, Ta = 25°C		_	_	50			
				V <sub>CE</sub> = 50 V, Ta = 85°C		_	_	500	μA		
	P, F			V <sub>CE</sub> = 35 V, Ta = 25°C		_	_	50	μΛ		
				V <sub>CE</sub> = 35 V, Ta = 85°C		_	_	500			
Collector-Emitter Saturation Voltage		V	2	I <sub>OUT</sub> = 1.25 A, I <sub>IN</sub> = 2 mA		1	_	1.6	V		
		V <sub>CE</sub> (sat)		I <sub>OUT</sub> = 0.75 A, I <sub>IN</sub> = 935 μA		I		1.25			
DC Current Transfer Ratio		h <sub>FE</sub>	2	V <sub>CE</sub> = 2	V	I <sub>OUT</sub> = 1.0 A	١	800	ı		
Do Current Transfer Nati	DC Current Hansler Ratio			VCE - 2	V	I <sub>OUT</sub> = 0.25 A	١	1500	ı		
Input Voltage (Output On	Input Voltage (Output On)		3	I <sub>OUT</sub> = 1.25 A, I <sub>IN</sub> = 2 mA		I		2.4	V		
	AP, AF	- I <sub>R</sub>	4	V <sub>R</sub> = 50 V, Ta = 25°C		I		50			
Clamp Diode Leakage				V <sub>R</sub> = 50 V, Ta = 85°C		١	_	100	μA		
Current	F			V <sub>R</sub> = 35 V, Ta = 25°C		_	_	50	μΑ		
				V <sub>R</sub> = 35 V, Ta = 85°C		_	_	100			
Clamp Diode Forward Vo	ltage	V <sub>F</sub>	5	I <sub>F</sub> = 1.25 A		-	_	2	V		
Input Capacitance		C <sub>IN</sub>	6	V <sub>IN</sub> = 0 V, f = 1MHz		_	15	_	pF		
Turn-On Delay	P, F	<b>+</b>			V <sub>OUT</sub> =	= 35 V, R <sub>L</sub> = 29 Ω		0.1			
	AP, AF	t <sub>ON</sub>	7	C <sub>L</sub> = 15 pF	$V_{OUT}$ = 50 V, $R_L$ = 42 $\Omega$			0.1	_		
Turn-Off Delay	P, F	+	,	15 pF	V <sub>OUT</sub> =	= 35 V, R <sub>L</sub> = 29 Ω		1.0	_	- µs	
	AP, AF	toff			V <sub>OUT</sub> =	= 50 V, R <sub>L</sub> = 42 Ω					

# **TEST CIRCUIT**

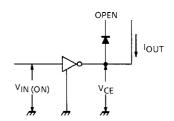
# 1. ICEX



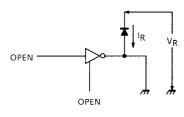
# 2. V<sub>CE (sat)</sub>, h<sub>FE</sub>



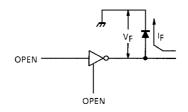
# 3. V<sub>IN (ON)</sub>



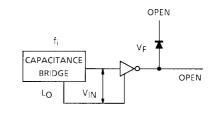
# 4. I<sub>R</sub>



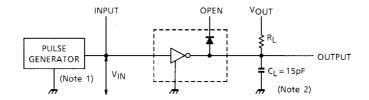
# 5. V<sub>F</sub>

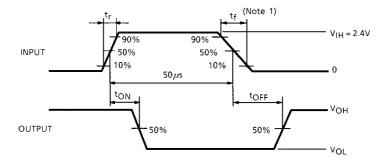


6. C<sub>IN</sub>



# 7. ton, toff





Note 1: Pulse Width 50 µs, Duty Cycle 10%

Output Impedance 50  $\Omega$ ,  $t_f \le 5$ ns,  $t_f \le 10$ ns

Note 2: C<sub>1</sub> includes probe and jig capacitance

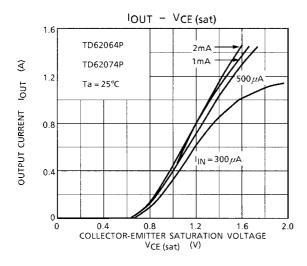
### **PRECAUTIONS for USING**

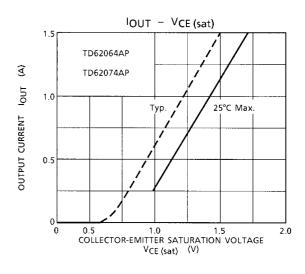
(1) This IC does not include built-in protection circuits for excess current or overvoltage. If this IC is subjected to excess current or overvoltage, it may be destroyed. Hence, the utmost care must be taken when systems which incorporate this IC are designed. Utmost care is necessary in the design of the output line, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

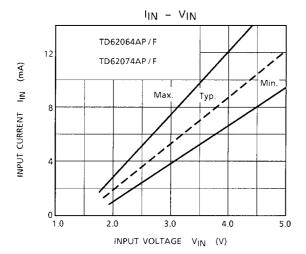
(2) When using TD62064P/AP/F/AF to drive an inductive load (such as a motor, solenoid, or relay), Toshiba recommend you use diodes (pins 1 and 8) to absorb the counter electromotive force generated when driving an inductive load.

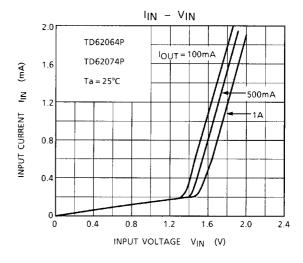
When using TD62074P/AP/F/AF to drive an inductive load (such as a motor, solenoid, or relay), Toshiba recommend you connect diodes externally to absorb the counter electromotive force generated when driving an inductive load.

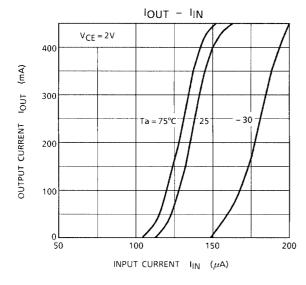
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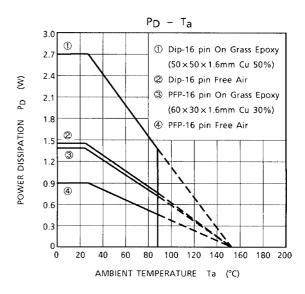


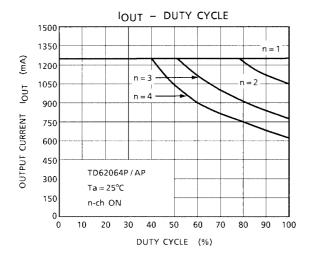


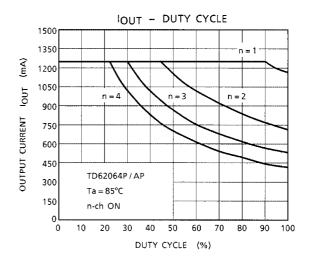


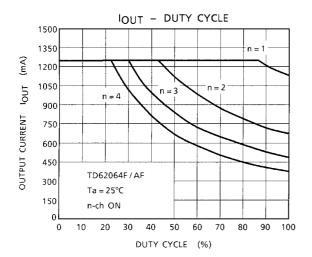


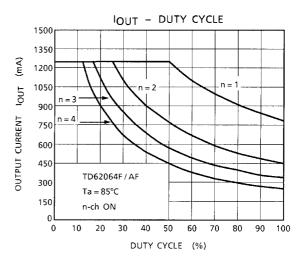








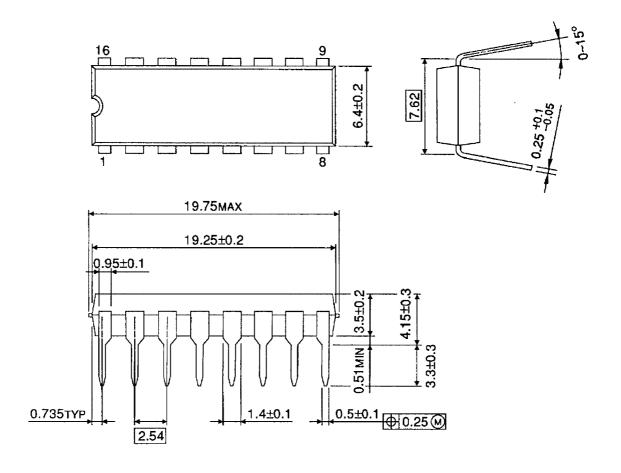




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# **PACKAGE DIMENSIONS**

DIP16-P-300-2.54A Unit: mm

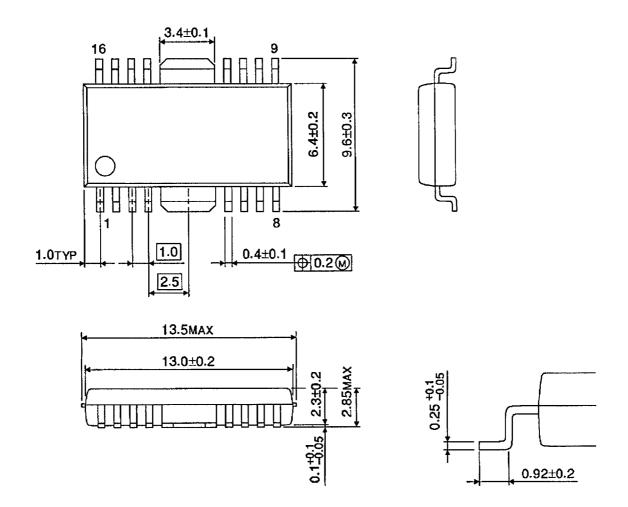


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Weight: 1.11 g (Typ.)

# **PACKAGE DIMENSIONS**

HSOP16-P-300-1.00 Unit: mm



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Weight: 0.50 g (Typ.)

#### **RESTRICTIONS ON PRODUCT USE**

000707EBA

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