TOSHIBA TC7SU04F/FU

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC7SU04F, TC7SU04FU

INVERTER

The TC7SU04 is a high speed C2MOS INVERTER fabricated with silicon gate C2MOS technology.

It achieves high speed operation similar to equivalent LSTTL while maintaining the C²MOS low power dissipation.

The internal circuit is composed of single stages inverter, it can be applied for crystal oscillation.

The input is equipped with protection circuits against static discharge or transient excess voltage.

Output currents are 1/2 compared to TC74HC series models.

FEATURES

•	High Speed	 $t_{pd} = 7ns$ (Typ.) at
		$V_{CC} = 5V$

Low Power Dissipation $I_{CC} = 1\mu A$ (Max.) at

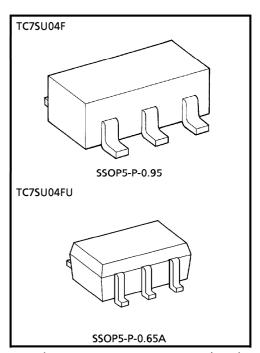
 $Ta = 25^{\circ}C$ High Noise Immunity $V_{NIH} = V_{NIL}$ = 28% V_{CC} (Min.)

Output Drive Capability 5 LSTTL Loads

• Symmetrical Output Impedance ... $|I_{OH}| = I_{OL}$ = 2mA (Min.)

• Balanced Propagation Delays $t_{pLH} = t_{pHL}$

Wide Operating Voltage Range ... V_{CC} (opr) = 2∼6V

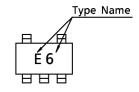


Weight SSOP5-P-0.95 : 0.016g (Typ.) SSOP5-P-0.65A: 0.006g (Typ.)

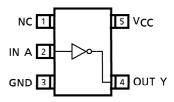
MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	Vcc	-0.5~7	V
DC Input Voltage	VIN	-0.5~V _{CC} + 0.5	V
DC Output Voltage	Vout	-0.5~V _{CC} +0.5	V
Input Diode Current	ΙΚ	± 20	mA
Output Diode Current	lok	± 20	mA
DC Output Current	IOUT	± 12.5	mΑ
DC V _{CC} /Ground Current	Icc	± 25	mA
Power Dissipation	PD	200	mW
Storage Temperature	T _{stg}	- 65~150	°C
Lead Temperature (10s)	TL	260	°C

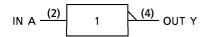
MARKING



PIN ASSIGNMENT (TOP VIEW)



LOGIC DIAGRAM



RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	Vcc	2~6	V
Input Voltage	VIN	0~V _{CC}	V
Output Voltage	Vout	0~V _{CC}	V
Operating Temperature	T _{opr}	- 40~85	°C

DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	TEST CONDITION			Ta = 25°C			Ta = -4	UNIT	
CHARACTERISTIC	3 TIVIDOL			Vcc	MIN.	TYP.	MAX.	MIN.	MAX.	OINIT
High Lovel	V _{IH}			2.0	1.7	_	_	1.7	_	
High-Level Input Voltage			_	4.5	3.6	_	—	3.6	_	V
Imput voitage				6.0	4.8	_	_	4.8	_	
Low-Level				2.0	—	—	0.3	_	0.3	
Input Voltage	V _{IL}		_	4.5	—	—	0.9	_	0.9	V
Imput voitage				6.0	_	_	1.2	_	1.2	
	Voн	V _{IN} = V _{IL}		2.0	1.8	2.0	—	1.8	_	
Himb Laval			$I_{OH} = -20 \mu A$	4.5	4.0	4.5	—	4.0	_	
High-Level				6.0	5.5	5.9	_	5.5	_	V
Output Voltage			$I_{OH} = -2mA$	4.5	4.18	4.31	_	4.13	_	
			$I_{OH} = -2.6mA$	6.0	5.68	5.80	_	5.63		
	VOL			2.0	_	0.0	0.2	_	0.2	
			$I_{OL} = 20 \mu A$	4.5	 	0.0	0.2	_	0.5	
Low-Level		$V_{IN} = V_{IH}$		6.0	—	0.0	0.5	_	0.5	V
Output Voltage			$I_{OL} = 2mA$	4.5	_	0.17	0.26	_	0.33	
			$I_{OL} = 2.6 mA$	6.0	_	0.18	0.26	_	0.33	
Input Leakage	nput Leakage		or GND	6.0			± 0.1		± 1.0	
Current		$V_{IN} = V_{CC}$ or GND		0.0			<u> </u>		1.0	
Quiescent	lee	VINI - V.C.C. (or GND	6.0			1.0		10.0	μ A
Supply Current ICC		V _{IN} = V _{CC} or GND		0.0			1.0		10.0	

Output currents are 1/2 compared to TC74HC series models.

AC ELECTRICAL CHARACTERISTICS ($C_1 = 15pF$, Input $t_r = t_f = 6ns$, $V_{CC} = 5V$)

CHARACTERISTIC	SYMBOL	TEST CONDITION		Ta = 25°C			
CHARACTERISTIC	3TIVIBOL TEST CONDITION		MIN.	TYP.	MAX.	UNIT	
Output Transition	tTLH			_	10	nc	
Time	tTHL	_	_)	10	ns	
Propagation Delay	t _{pLH}			7	15	ne	
Time	tpHL	_	_	′	13	ns	

AC ELECTRICAL CHARACTERISTICS ($C_L = 50pF$, Input $t_r = t_f = 6ns$)

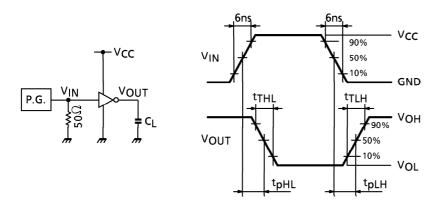
CHARACTERISTIC	SYMBOL	TEST CONDITION		Ta = 25°C			Ta = -4	UNIT	
CHARACTERISTIC		TEST CONDITION	Vcc	MIN.	TYP.	MAX.	MIN.	MAX.	
Output Transition	t		2.0	_	50	125	_	155	
Time	t _{TLH}	_	4.5	l —	14	25	_	31	ns
Time	t _{THL}		6.0	—	12	21	_	26	
Propagation Dolay	4		2.0	_	48	100	_	125	
Propagation Delay	t _{pLH}	-	4.5	l —	12	20	_	25	ns
Time	t _{pHL}		6.0	—	9	17	_	21	
Input Capacitance	CIN	_		_	5	10	_	10	
Power Dissipation Capacitance	C _{PD}	(Note 1)		_	10	_	ı	ı	pF

Note 1: C_{PD} defined as the value of internal equivalent capacitance of IC which is calculated from the operating current consumption without load (refer to Test Circuit).

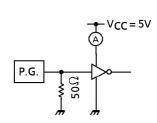
Average operating current can be obtained by the equation hereunder.

 $ICC (opr) = CPD \cdot VCC \cdot fIN + ICC$

SWITCHING CHARACTERISTICS TEST CIRCUIT



ICC (opr) TEST CIRCUIT

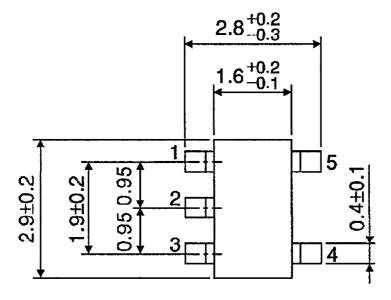


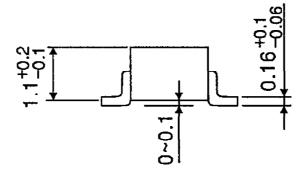
Input waveform is the same as that in case of switching characteristics test.

PACKAGE DIMENSIONS

SSOP5-P-0.95

Unit: mm



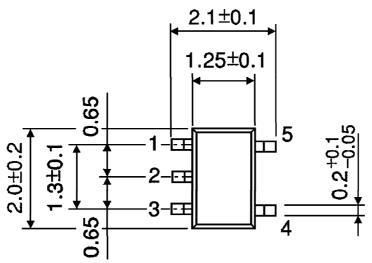


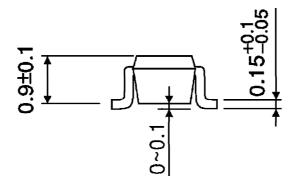
Weight: 0.016g (Typ.)

Unit: mm

PACKAGE DIMENSIONS

SSOP5-P-0.65A





Weight: 0.006g (Typ.)

RESTRICTIONS ON PRODUCT USE

000707EBA

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