TOSHIBA TC7S14F/FU

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC7S14F, TC7S14FU

SCHMITT INVERTER

The TC7S14 is a high speed C2MOS SCHMITT INVERTER fabricated with silicon gate C2MOS technology. It achieves a high speed operation similar to equivalent LSTTL while maintaining the C2MOS low power dissipation.

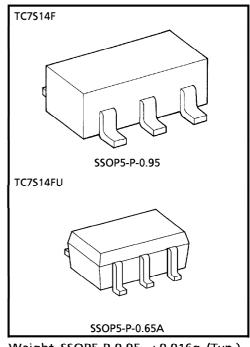
Pin Configuration and function are the same as the TC7SU04F but input have 25% V_{CC} hysteresis and with its schmitt trigger function, the TC7S14F can be used as line receivers which will receive slow input signal. Input is equipped with protection circuits against static discharge or transinent excess voltage.

Output currents are 1/2 compared to TC74HC series models.

FEATURES

•	High Speed	t_{pd} = 11ns (Typ.) at V_{CC} = 5V
•	Low Power Dissipation	$I_{CC} = 1\mu A$ (Max.) at $Ta = 25^{\circ}C$
•	High Noise Immunity	$V_H = 1.1V$ at $V_{CC} = 5V$
•	Output Drive Capability	5 LSTTL Loads
•	Symmetrical Output Impedance	$ I_{OH} = I_{OL} = 2mA$



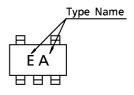


Weight SSOP5-P-0.95 : 0.016g (Typ.) SSOP5-P-0.65A: 0.006g (Typ.)

MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	V _{CC}	-0.5~7	V
DC Input Voltage	V _{IN}	$-0.5 \sim V_{CC} + 0.5$	V
DC Output Voltage	Vout	$-0.5 \sim V_{CC} + 0.5$	٧
Input Diode Current	ΙΚ	± 20	mA
Output Diode Current	loк	± 20	mΑ
DC Output Current	lout	± 12.5	mΑ
DC V _{CC} /Ground Current	lcc	± 50	mΑ
Power Dissipation	PD	200	mW
Storage Temperature	T _{stg}	- 65∼150	°C
Lead Temperature (10s)	TL	260	°C

MARKING

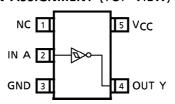


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LOGIC DIAGRAM



PIN ASSIGNMENT (TOP VIEW)



TRUTH TABLE

А	Υ
L	Н
Η	L

RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	Vcc	2~6	V
Input Voltage	VIN	0~V _{CC}	V
Output Voltage	VOUT	0~V _{CC}	V
Operating Temperature	T _{opr}	- 40~85	°C

DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	TEST CONDITION			Ta = 25°C			$Ta = -40 \sim 85^{\circ}C$		UNIT
CHARACTERISTIC	3 TIVIDOL			Vcc	MIN.	TYP.	MAX.	MIN.	MAX.	CIVIT
Positive				2.0	1.0	1.25	1.5	1.0	1.5	
Threshold Voltage	V _P		_	4.5	2.3	2.7	3.15	2.3	3.15	V
Tillesiloid Voltage				6.0	3.0	3.5	4.2	3.0	4.2	
Negative				2.0	0.3	0.65	0.9	0.3	0.9	
Threshold Voltage	V_N		_	4.5	1.13	1.6	2.0	1.13	2.0	V
Tillesiloid Voltage				6.0	1.5	2.3	2.6	1.5	2.6	
				2.0	0.3	0.6	1.0	0.3	1.0	
Hysteresis Voltage	VH		_	4.5	0.6	1.1	1.4	0.6	1.4	V
				6.0	0.8	1.2	1.7	0.8	1.7	
	VOH	V _{IN} = V _{IL}	I _{OH} = -20μA	2.0	1.9	2.0	—	1.9	_	
lliah Laval				4.5	4.4	4.5	—	4.4	_	
High-Level Output Voltage				6.0	5.9	6.0	_	5.9	_	V
Output voitage			$I_{OH} = -2mA$	4.5	4.18	4.31	—	4.13	_	
			$I_{OH} = -2.6mA$	6.0	5.68	5.80	—	5.63	_	
	V _{OL}	V _{IN} = V _{IH}		2.0	_	0.0	0.1	_	0.1	
l and land			$I_{OL} = 20 \mu A$	4.5	 —	0.0	0.1	_	0.1	
Low-Level				6.0	_	0.0	0.1	_	0.1	V
Output Voltage	-	'''	$I_{OL} = 2mA$	4.5	_	0.17	0.26	_	0.33	
			$I_{OL} = 2.6 mA$	6.0	_	0.18	0.26	_	0.33	
Input Leakage Current	IN	V _{IN} = V _{CC} or GND		6.0	_	_	± 0.1	_	± 1.0	μΑ
Quiescent Supply Current	lcc	V _{IN} = V _{CC} or GND		6.0	_	_	1.0	_	10.0	μΑ

Output currents are 1/2 compared to TC74HC series models.

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AC ELECTRICAL CHARACTERISTICS ($C_L = 15pF$, $V_{CC} = 5V$, Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	Т	UNIT		
CHARACTERISTIC	3 TIVIBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Transition	^t TLH			4	8	
Time	^t THL	_		4	8	nc
Propagation Delay	t _{pLH}			11	21	ns
Time	t _{pHL}	_		''	21	

AC ELECTRICAL CHARACTERISTICS ($C_L = 50pF$, Input $t_f = t_f = 6ns$)

CHARACTERISTIC	SYMBOL	TEST CONDITION		Ta = 25°C			Ta = -4	UNIT	
CHARACTERISTIC	STIVIBOL			MIN.	TYP.	MAX.	MIN.	MAX.	CIVII
Output Transition	4		2.0	_	50	125	_	145	
Time	^t TLH	_	4.5	 —	14	25	_	30	
Time	^t THL		6.0	 	12	21	_	24	20
Propagation Delay	+	_	2.0	_	48	100	_	235	ns
	t _{pLH}		4.5	l —	12	20	_	48	
Time	t _{pHL}		6.0	 	9	17	_	40	
Input Capacitance	CIN	_		_	5	10	_	10	
Power Dissipation Capacitance	C _{PD}	Note (1)		_	28	_	ı	_	pF

Note (1): C_{PD} is defined as the value of internal equivalent capacitance which is calculated from the operating current consumption without load.

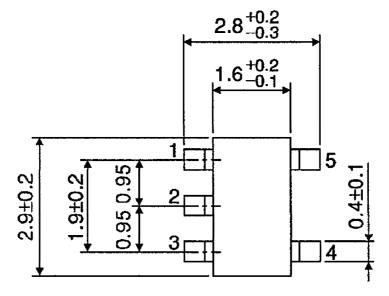
Average operating current can be obtained by the equation :

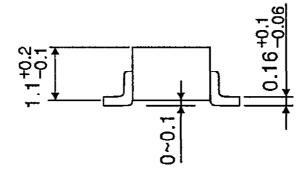
ICC (opr) = CPD·VCC·fIN + ICC

PACKAGE DIMENSIONS

SSOP5-P-0.95

Unit: mm





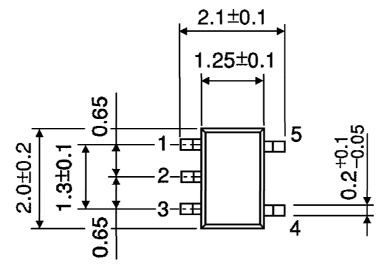
Weight: 0.016g (Typ.)

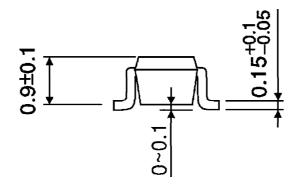
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PACKAGE DIMENSIONS

SSOP5-P-0.65A

Unit: mm





Weight: 0.006g (Typ.)

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