Vishay High Power Products

Schottky Rectifier, 1.0 A



 V_{R}

 I_{RM}

Cathode

Anode

30 V

15 mA at 125 °C

- Small foot print, surface mountable
- Very low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

The STPS1L30UPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	1.0	A		
V _{RRM}		30	V		
I _{FSM}	t _p = 5 ms sine	360	A		
V _F	1.0 Apk, T _J = 125 °C	0.30	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	STPS1L30UPbF	UNITS		
Maximum DC reverse voltage	V _R	30	N/		
Maximum working peak reverse voltage	V _{RWM}				

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 106 °C, rectangular waveform		1.0	
Maximum peak one cycle	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	360	А
non-repetitive surge current		10 ms sine or 6 ms rect. pulse		75	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 6 mH		3.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical		1.0	А

For technical questions, contact: diodes-tech@vishay.com





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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	. TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.420	V
		2 A		0.470	
		1 A	T _J = 125 °C	0.300	
		2 A		0.375	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.2	mA
		$T_J = 100 \ ^{\circ}C$		5.0	
		T _J = 125 °C		15	
Maximum junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		200	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾		25	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	
Approximate weight			0.10	g
Approximate weight			0.003	oz.
Marking device		Case style SMB (similar to DO-214AA) V13L		3L

Notes

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB



DC

1.2

16

1.6

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130

120

110

100

90

0

= 0.20 D

Square wave (D = 0.50)80 % rated V_B applied See note (1)

0.8

I_{F(AV)} - Average Forward Current (A)

Fig. 4 - Maximum Average Forward Current vs.

0.4

D = 0.25D = 0.25D = 0.33 D = 0.50 D = 0.75

Allowable Case Temperature (°C)

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Fig. 1 - Maximum Forward Voltage Drop Characteristics





Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 80 % rated V_R

10 000

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ORDERING INFORMATION TABLE



Tape and reel only (3000 pieces)

LINKS TO RELATED DOCUMENTS			
Dimensions	http://www.vishay.com/doc?95017		
Part marking information	http://www.vishay.com/doc?95029		
Packaging information	http://www.vishay.com/doc?95034		



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