TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

SSM3K15FV

High Speed Switching Applications Analog Switch Applications

Optimum for high-density mounting in small packages

- Low on-resistance
 - : $R_{on} = 4.0 \Omega \text{ (max) } (@V_{GS} = 4 \text{ V})$
 - : $R_{on} = 7.0 \Omega (max) (@V_{GS} = 2.5 V)$

Absolute Maximum Ratings (Ta = 25°C)

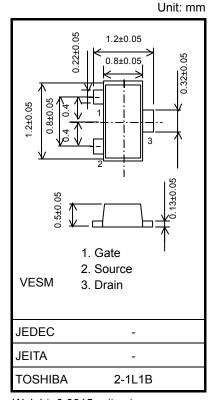
Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DS}	30	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC	I _D	100	mA	
	Pulse	I _{DP}	200		
Drain power dissipation (Ta = 25°C)		P _D (Note 1)	150	mW	
Channel temperature		T _{ch}	150	°C	
Storage temperature		T _{stg}	−55 ~ 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

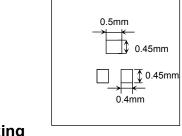
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling

Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating, mounted on FR4 board (25.4 mm × 25.4 mm × 1.6 t)

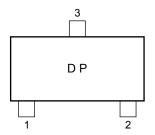


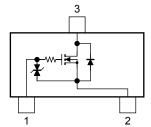
Weight: 0.0015 g (typ.)



Marking

Equivalent Circuit





Handling Precaution

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

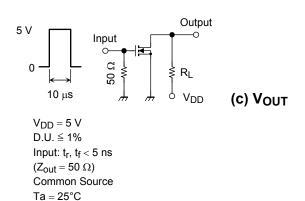
Electrical Characteristics (Ta = 25°C)

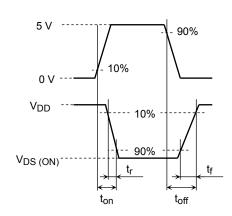
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$	_	_	±1	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 0.1 \text{ mA}, V_{GS} = 0$	30	_	_	V
Drain cut-off curre	ent	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0$	_	_	1	μА
Gate threshold vo	Itage	V _{th}	V _{DS} = 3 V, I _D = 0.1 mA	0.8	_	1.5	V
Forward transfer a	admittance	Y _{fs}	V _{DS} = 3 V, I _D = 10 mA	25	_	_	mS
Drain-Source on-resistance		R _{DS (ON)}	I _D = 10 mA, V _{GS} = 4 V	_	2.2	4.0	Ω
			I _D = 10 mA, V _{GS} = 2.5 V	_	4.0	7.0	
Input capacitance		C _{iss}	V _{DS} = 3 V, V _{GS} = 0, f = 1 MHz	_	7.8	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 3 V, V _{GS} = 0, f = 1 MHz	_	3.6	_	pF
Output capacitance		C _{oss}	V _{DS} = 3 V, V _{GS} = 0, f = 1 MHz	_	8.8	_	pF
Switching time	Turn-on time	t _{on}	V _{DD} = 5 V, I _D = 10 mA, V _{GS} = 0~5 V	_	50	_	ns
	Turn-off time	t _{off}		_	180	_	

Switching Time Test Circuit







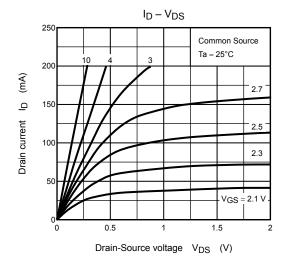


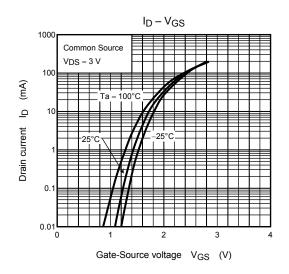
Precaution

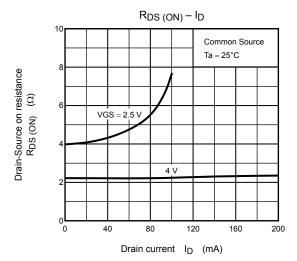
 V_{th} can be expressed as the voltage between gate and source when the low operating current value is I_D = 100 μ A for this product. For normal switching operation, $V_{GS~(on)}$ requires a higher voltage than V_{th} and $V_{GS~(off)}$ requires a lower voltage than V_{th} .

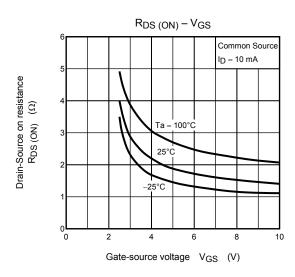
(The relationship can be established as follows: $V_{GS\ (off)} < V_{th} < V_{GS\ (on)}$)

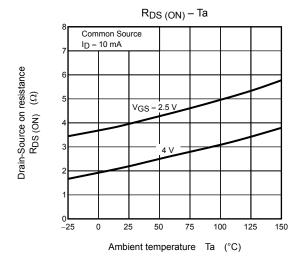
Please take this into consideration when using the device.

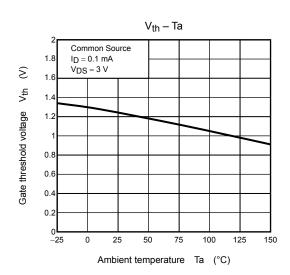


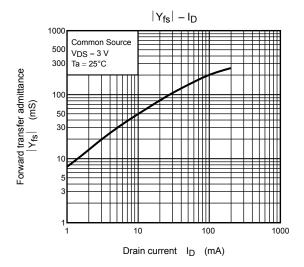


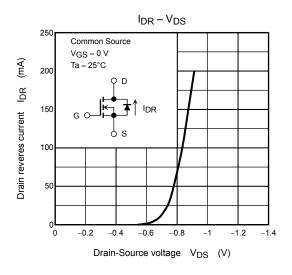


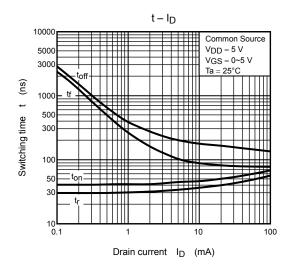


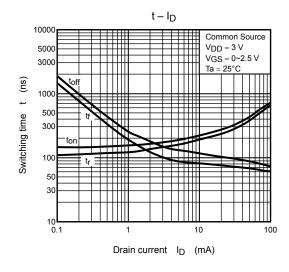


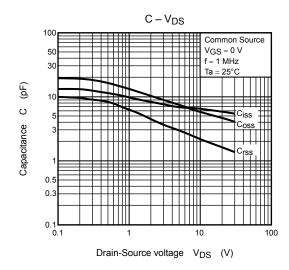


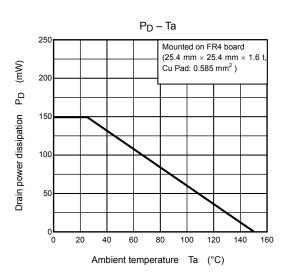












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20070701-EN GENERAL

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