

## isc N-Channel MOSFET Transistor

## SPP21N50C3, ISPP21N50C3

### • FEATURES

- Static drain-source on-resistance:  $R_{DS(on)} \leq 190\text{m}\Omega$
- Enhancement mode
- Fast Switching Speed
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### • DESCRIPTION

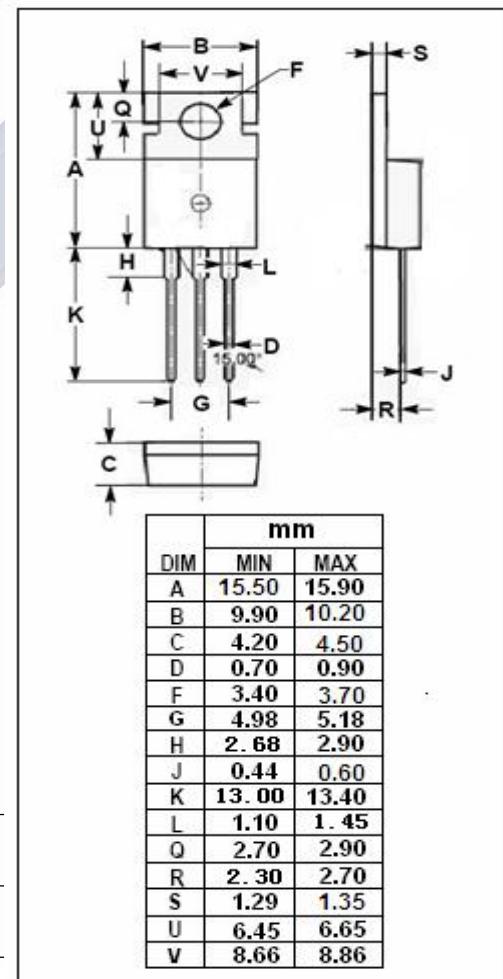
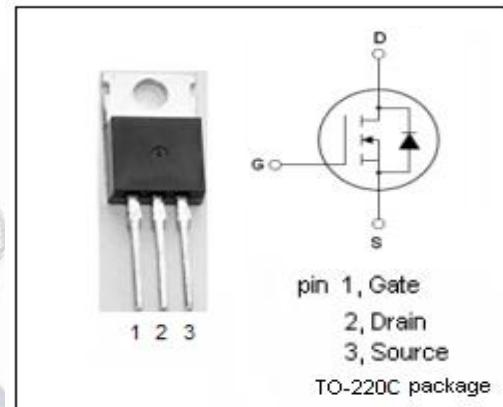
- New revolutionary high voltage technology
- Ultra low effective capacitance

### • ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )

| SYMBOL    | PARAMETER                                  | VALUE    | UNIT             |
|-----------|--|----------|------------------|
| $V_{DSS}$ | Drain-Source Voltage                       | 500      | V                |
| $V_{GS}$  | Gate-Source Voltage                        | $\pm 20$ | V                |
| $I_D$     | Drain Current-Continuous                   | 21       | A                |
| $I_{DM}$  | Drain Current-Single Pulsed                | 63       | A                |
| $P_D$     | Total Dissipation @ $T_c=25^\circ\text{C}$ | 208      | W                |
| $T_j$     | Max. Operating Junction Temperature        | 150      | $^\circ\text{C}$ |
| $T_{stg}$ | Storage Temperature                        | -55~150  | $^\circ\text{C}$ |

### • THERMAL CHARACTERISTICS

| SYMBOL         | PARAMETER                             | MAX | UNIT                      |
|----------------|---------------------------------------|-----|---------------------------|
| $R_{th(ch-c)}$ | Channel-to-case thermal resistance    | 0.6 | $^\circ\text{C}/\text{W}$ |
| $R_{th(ch-a)}$ | Channel-to-ambient thermal resistance | 62  | $^\circ\text{C}/\text{W}$ |



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## ELECTRICAL CHARACTERISTICS

 $T_c=25^\circ\text{C}$  unless otherwise specified

| SYMBOL                     | PARAMETER                      | CONDITIONS   | MIN | TYP | MAX | UNIT             |
|----------------------------|--------------------------------|--|-----|-----|-----|------------------|
| $\text{BV}_{\text{DSS}}$   | Drain-Source Breakdown Voltage | $\text{V}_{\text{GS}}=0\text{V}; \text{I}_D = 250 \mu\text{A}$       | 500 |     |     | V                |
| $\text{V}_{\text{GS(th)}}$ | Gate Threshold Voltage         | $\text{V}_{\text{DS}}=\text{V}_{\text{GS}}; \text{I}_D = 1\text{mA}$ | 2.1 |     | 3.9 | V                |
| $\text{R}_{\text{DS(on)}}$ | Drain-Source On-Resistance     | $\text{V}_{\text{GS}}=10\text{V}; \text{I}_D=13.1\text{A}$           |     |     | 190 | $\text{m}\Omega$ |
| $\text{I}_{\text{GSS}}$    | Gate-Source Leakage Current    | $\text{V}_{\text{GS}}=20\text{V}; \text{V}_{\text{DS}}=0\text{V}$    |     |     | 0.1 | $\mu\text{A}$    |
| $\text{I}_{\text{DSS}}$    | Drain-Source Leakage Current   | $\text{V}_{\text{DS}}=500\text{V}; \text{V}_{\text{GS}}= 0\text{V}$  |     |     | 1   | $\mu\text{A}$    |
| $\text{V}_{\text{SD}}$     | Diode forward voltage          | $\text{I}_F=\text{I}_S; \text{V}_{\text{GS}} = 0\text{V}$            |     |     | 1.2 | V                |