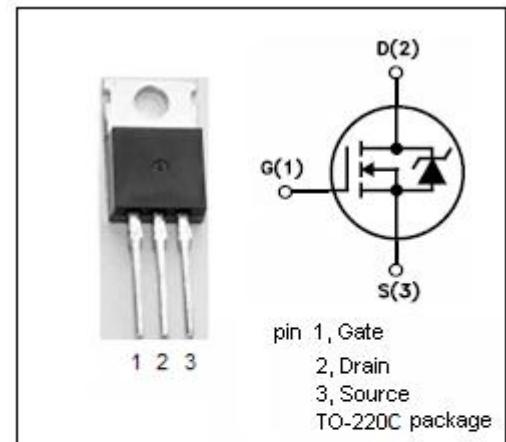




INCHANGE Semiconductor

**Isc N-Channel MOSFET Transistor****SPP11N80C3****• FEATURES**

- Ultra low effective capacitances
- Low gate charge
- Improved transconductance
- Low gate drive power loss
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**• APPLICATIONS**

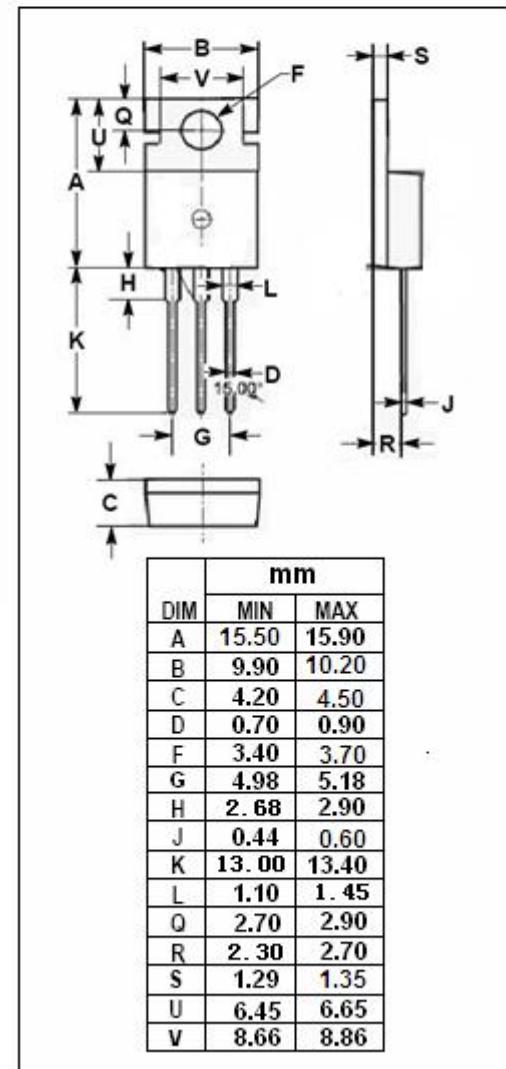
- Switching applications

**• ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	800	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-Continuous@ $T_c=25^\circ\text{C}$ $T_c=100^\circ\text{C}$	11 7.1	A
$I_{DM}$	Drain Current-Single Pulsed	33	A
$P_D$	Total Dissipation	156	W
$T_j$	Operating Junction Temperature	-55~150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature	-55~150	$^\circ\text{C}$

**• THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	0.8	$^\circ\text{C}/\text{W}$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	62	$^\circ\text{C}/\text{W}$





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**Isc N-Channel MOSFET Transistor****SPP11N80C3****ELECTRICAL CHARACTERISTICS****T<sub>c</sub>=25°C unless otherwise specified**

<b>SYMBOL</b>	<b>PARAMETER</b>	<b>CONDITIONS</b>	<b>MIN</b>	<b>TYP</b>	<b>MAX</b>	<b>UNIT</b>
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = 0.25mA	800			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =±20V; I <sub>D</sub> =0.68mA	2.1		3.9	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> = 10V; I <sub>D</sub> =7.1A		390	450	mΩ
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±20V; V <sub>DS</sub> = 0V			±0.1	μA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = 800V; V <sub>GS</sub> = 0V; T <sub>J</sub> =25°C T <sub>J</sub> =150°C			20 200	μA
V <sub>SDF</sub>	Diode forward voltage	I <sub>SD</sub> =11A, V <sub>GS</sub> = 0 V			1.2	V