

## Isc N-Channel MOSFET Transistor

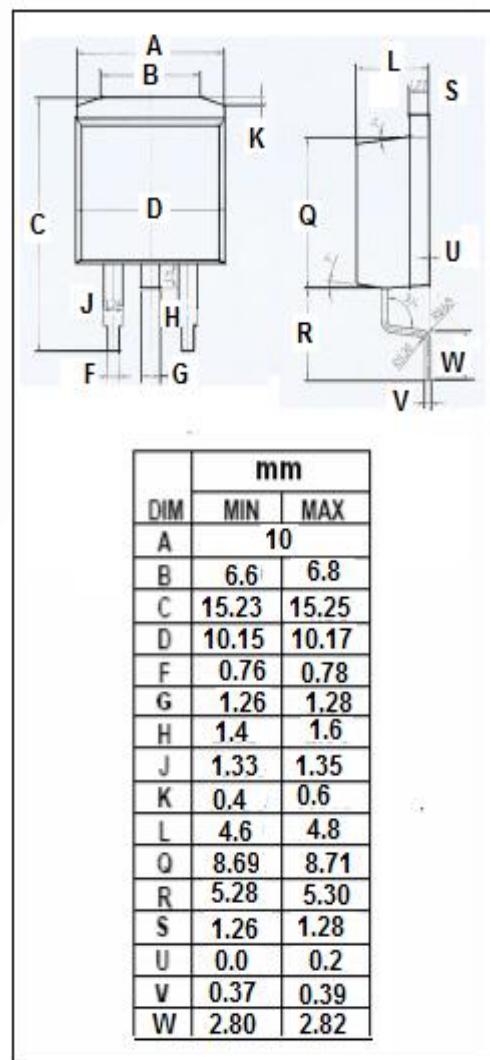
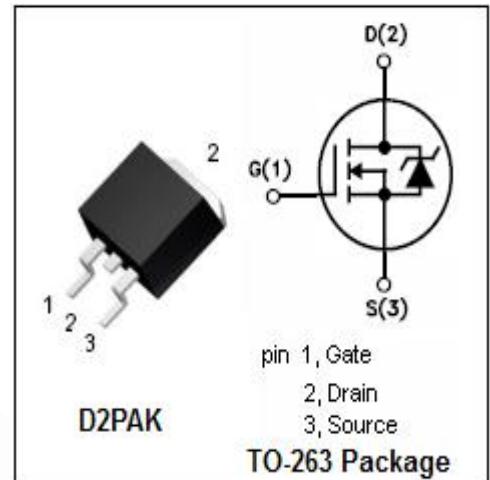
## SPB17N80C3

### • FEATURES

- With To-263(D2PAK) package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### • APPLICATIONS

- Switching applications



### • ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	800	V
$V_{GSS}$	Gate-Source Voltage	$\pm 30$	V
$I_D$	Drain Current-Continuous $T_c=25^\circ\text{C}$ $T_c=100^\circ\text{C}$	17 11	A
$I_{DM}$	Drain Current-Single Pulsed	51	A
$P_D$	Total Dissipation @ $T_c=25^\circ\text{C}$	208	W
$T_{ch}$	Max. Operating Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature	-55~150	$^\circ\text{C}$

### • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	0.6	$^\circ\text{C}/\text{W}$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	62	$^\circ\text{C}/\text{W}$

**Isc N-Channel MOSFET Transistor****SPB17N80C3****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ C$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V; I_D=0.25mA$	800			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}; I_D=1.0mA$	2.1		3.9	V
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}= 10V; I_D=11A$		250	290	$m\Omega$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}= \pm 20V; V_{DS}=0V$			$\pm 0.1$	$\mu A$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=800V; V_{GS}= 0V; T_j=25^\circ C$ $V_{DS}=800V; V_{GS}= 0V; T_j=150^\circ C$			25 250	$\mu A$
$V_{SDF}$	Diode forward voltage	$I_{SD}=17A, V_{GS} = 0V$		1.0	1.2	V