



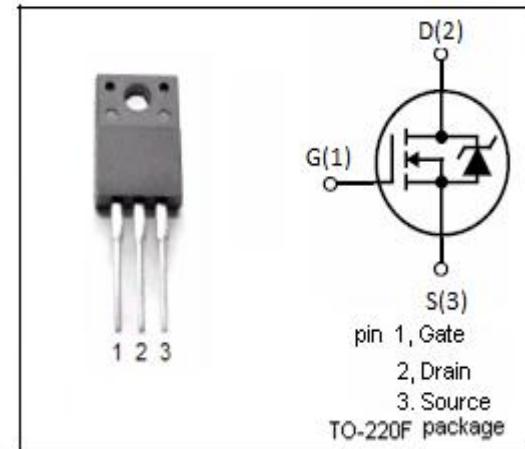
INCHANGE Semiconductor

## isc N-Channel MOSFET Transistor

SPA20N60C3

## • FEATURES

- New revolutionary high voltage technology
- Ultra low gate charge
- High peak current capability
- Improved transconductance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



## • APPLICATIONS

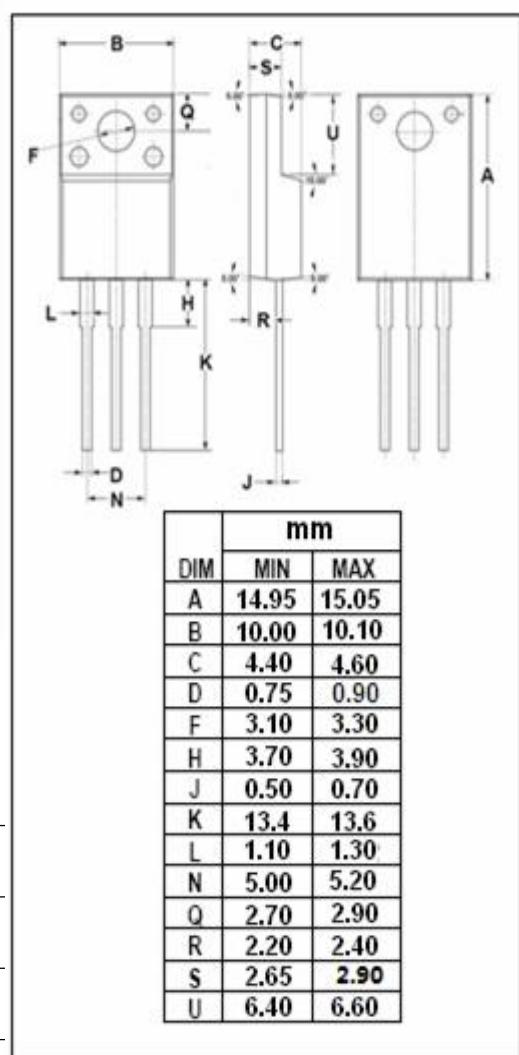
- Switching applications

• ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	600	V
$V_{GSS}$	Gate-Source Voltage	$\pm 30$	V
$I_D$	Drain Current-Continuous@ $T_c=25^\circ\text{C}$ $T_c=100^\circ\text{C}$	20.7 13.1	A
$I_{DM}$	Drain Current-Single Pulsed	62.1	A
$P_D$	Total Dissipation	34.5	W
$T_j$	Operating Junction Temperature	-55~150	°C
$T_{stg}$	Storage Temperature	-55~150	°C

## • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	3.6	°C/W
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	62	°C/W





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**isc N-Channel MOSFET Transistor****SPA20N60C3****ELECTRICAL CHARACTERISTICS****T<sub>c</sub>=25°C unless otherwise specified**

<b>SYMBOL</b>	<b>PARAMETER</b>	<b>CONDITIONS</b>	<b>MIN</b>	<b>TYP</b>	<b>MAX</b>	<b>UNIT</b>
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = 0.25mA	600			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =±30V; I <sub>D</sub> =1mA		3	3.9	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> = 10V; I <sub>D</sub> =13.1A		160	190	mΩ
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±30V; V <sub>DS</sub> = 0V			±0.1	μA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = 600V; V <sub>GS</sub> = 0V; T <sub>J</sub> =25°C T <sub>J</sub> =125°C			1 100	μA
V <sub>SDF</sub>	Diode forward voltage	I <sub>SD</sub> =20.7A, V <sub>GS</sub> = 0 V			1.2	V