

Sensitive high immunity SCRs up to 0.8 A

Features

- I_{T(RMS)} up to 0.8 A
- ightharpoonup V_{DRM}/V_{RRM} 400 and 600 V
- I_{GT} from 0.5 to 25 μ A

Description

Thanks to highly sensitive triggering levels, the P011xx SCR series is suitable for all applications where available gate current is limited, such as ground fault circuit interruptors, pilot circuits in solid state relays, standby mode power supplies, smoke and alarm detectors.

Available in through-hole or surface-mount packages, the voltage capability of this series has been upgraded since its introduction and is now available up to 600 V.

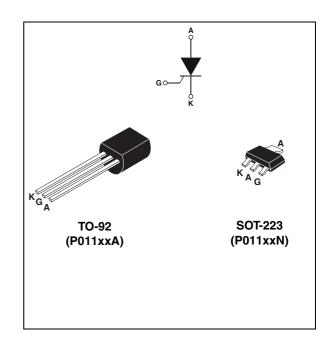


Table 1. Device summary

Order code	Vol	tage	Sensitivity		Package	
Order code	400 V	600 V	Min.	Max.	Package	
P0111DA 1AA3	Х		4 μΑ	25 μΑ	TO-92	
P0111DA 5AL3	Х		4 μΑ	25 μΑ	TO-92	
P0111DN 5AA4	Х		4 μΑ	25 μΑ	SOT-223	
P0111MA 1AA3		Х	4 μΑ	25 μΑ	TO-92	
P0111MA2AL3 ⁽¹⁾		Х	4 μΑ	25 μΑ	TO-92	
P0111MN 5AA4		Х	4 μΑ	25 μΑ	SOT-223	
P0115DA 1AA3	X		15 μΑ	50 μΑ	TO-92	
P0115DA 5AL3	X		15 μΑ	50 μΑ	TO-92	
P0118DA 1AA3	Х		0.5 μΑ	5 μΑ	TO-92	
P0118DA 5AL3	X		0.5 μΑ	5 μΑ	TO-92	
P0118DN 5AA4	Х		0.5 μΑ	5 μΑ	SOT-223	
P0118MA 2AL3		Х	0.5 μΑ	5 μΑ	TO-92	
P0118MA 5AL3		Х	0.5 μΑ	5 μΑ	TO-92	

^{1.} This order code has no space.

Characteristics P011xx

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Table 2. Absolute ratings (limiting values)

Symbol	Parameter	Value	Unit			
	RMS on-state current (180° conduction angle)	TO-92	T _I = 55 °C	0.8	Α	
I _T (RMS)	nivis on-state current (160 conduction angle)	SOT-223	T _{amb} = 70 °C	0.6	^	
IT	Average on-state current (180° conduction angle)	TO-92	T _I = 55 °C	0.5	Α	
IT _(AV)	Average on-state current (160 Conduction angle)	SOT-223	T _{amb} = 70 °C		"	
l	Non repetitive surge peak on-state current	$t_p = 8.3 \text{ ms}$	T _i = 25 °C	8	Α	
I _{TSM}		$t_p = 10 \text{ ms}$	1 - 25 0	7	Λ	
l ² t	I ² t Value for fusing	$t_p = 10 \text{ ms}$	T _j = 25 °C	0.24	A ² s	
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$, $t_r \le 100 \text{ ns}$	F = 60 Hz	T _j = 125 °C	50	A/µs	
I_{GM}	Peak gate current $t_p = 20 \mu s$		T _j = 125 °C	1	Α	
$P_{G(AV)}$	Average gate power dissipation	T _j = 125 °C	0.1	W		
T _{stg} T _j	Storage junction temperature range Operating junction temperature range		- 40 to + 150 - 40 to + 125	°C		

Table 3. Electrical characteristics ($T_i = 25$ °C, unless otherwise specified)

Symbol	Test conditions			P0111	P0115	P0118	Unit
1			Min.	4	15	0.5	пΛ
I _{GT}	$V_D = 12 \text{ V}$ $R_L = 140 \Omega$		Max.	25	50	5	μA
V _{GT}			Max.	0.8			V
V _{GD}	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $R_{GK} = 1 \text{ k}\Omega$ $T_j = 125 \text{ °C}$			0.1			V
V _{RG}	I _{RG} = 10 μA			8			V
I _H	$I_T = 50 \text{ mA}$ $R_{GK} = 1 \text{ k}\Omega$			5			mA
IL	$I_G = 1 \text{ mA}$ $R_{GK} = 1 \text{ k}\Omega$		Max.	6		mA	
dV/dt	$V_D = 67 \% V_{DRM} R_{GK} = 1 k\Omega$	T _j = 125 °C	Min.	80	75	75	V/µs
V _{TM}	$I_{TM} = 1.6 \text{ A}$ tp = 380 µs $T_j = 25 ^{\circ}\text{C}$		Max.	1.95			V
V _{t0}	Threshold voltage $T_j = 12$		Max.	0.95			V
R _d	Dynamic resistance $T_j = 125 ^{\circ}\text{C}$		Max.	600			mΩ
	$V_{DRM} = V_{RRM} = 400 \text{ V}$ $R_{GK} = 1 \text{ k}\Omega$			1			
I _{DRM}	$V_{DRM} = V_{RRM} = 600 \text{ V}$ $R_{GK} = 1 \text{ k}\Omega$	T _j = 25 °C	Max.	10			μA
I _{RRM}	$V_{DRM} = V_{RRM}$ $R_{GK} = 1 \text{ k}\Omega$	T _j = 125 °C			100		

P011xx Characteristics

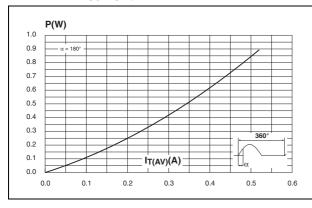
Table 4. Thermal resistance

Symbol	Parameter	Maximum	Unit		
R _{th(j-a)}	Junction to case (DC)		TO-92	80	°C/W
R _{th(j-t)}	Junction to tab (DC)		SOT-223	30	°C/W
В	lunction to embient (DC)		TO-92	150	°C/W
R _{th(j-a)}	Junction to ambient (DC)	$S^{(1)} = 5 \text{ cm}^2$	SOT-223	60	C/VV

^{1.} S = Copper surface under tab.

Figure 1. Maximum average power dissipation versus average on-state current

Figure 2. Average and DC on-state current versus lead temperature



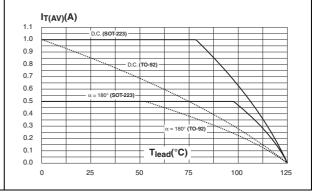
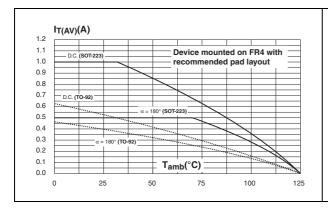
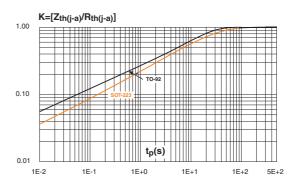


Figure 3. Average and DC on-state current versus ambient temperature

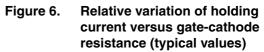
Figure 4. Relative variation of thermal impedance junction to ambient versus pulse duration

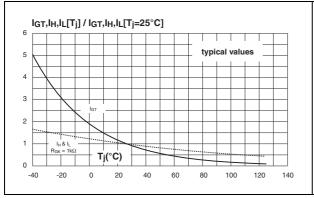




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Figure 5. Relative variation of gate trigger, holding and latching current versus junction temperature





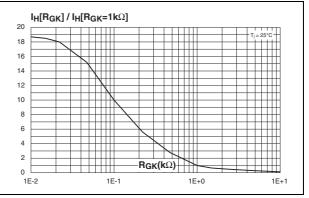
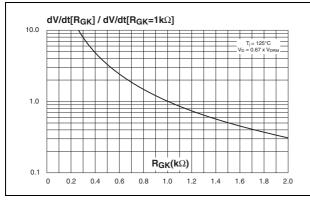


Figure 7. Relative variation of dV/dt immunity Figure 8. versus gate-cathode resistance (typical values).

Relative variation of dV/dt immunity versus gate-cathode capacitance (typical values)



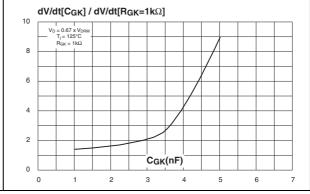
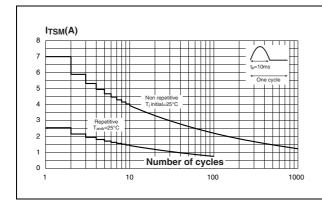
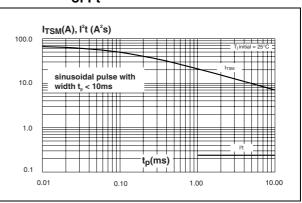


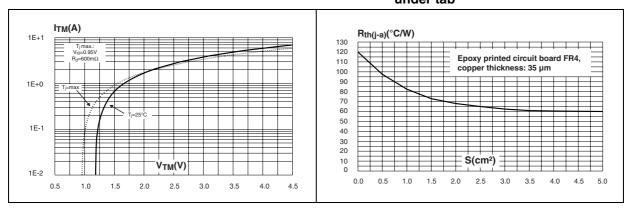
Figure 9. Surge peak on-state current versus Figure 10. Non-repetitive surge peak on-state current and corresponding value of I2t





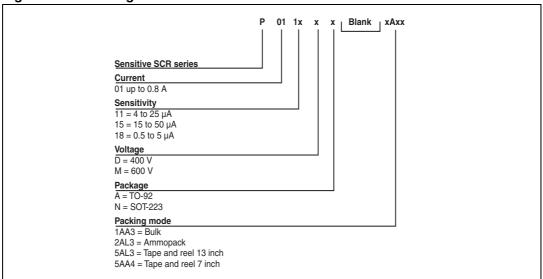
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Figure 11. On-state characteristics (maximum Figure 12. Thermal resistance junction to ambient versus copper surface under tab



2 Ordering information scheme

Figure 13. Ordering information scheme



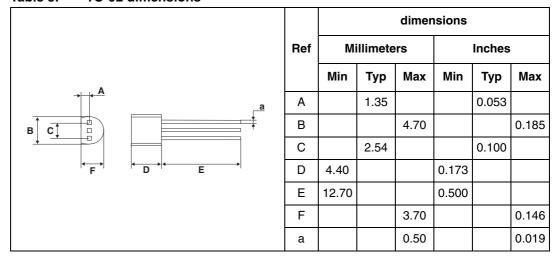
Package information P011xx

3 Package information

Epoxy meets UL94, V0

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Table 5. TO-92 dimensions



P011xx Package information

Table 6. SOT-223 dimensions

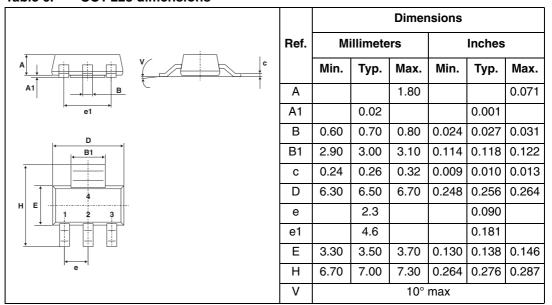
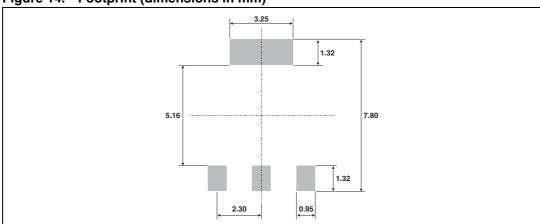


Figure 14. Footprint (dimensions in mm)



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4 Ordering information

Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty	Packing mode
P0111DA 1AA3	P0111 DA	TO-92	0.2 g	2500	BAG
P0111DA 5AL3	P0111 DA	TO-92	0.2 g	2000	Tape and reel 13 inch
P0111DN 5AA4	P1D	SOT-223	0.11 g	1000	Tape and reel 7 inch
P0111MA 1AA3	P0111 MA	TO-92	0.2 g	2500	Bag
P0111MA2AL3 ⁽¹⁾	P0111 MA	TO-92	0.2 g	2000	Ammopack
P0111MN 5AA4	P1M	SOT-223	0.11 g	1000	Tape and reel 7 inch
P0115DA 1AA3	P0115 DA	TO-92	0.2 g	2500	Bag
P0115DA 5AL3	P0115 DA	TO-92	0.2 g	2000	Tape and reel 13 inch
P0118DA 1AA3	P0118 DA	TO-92	0.2 g	2500	Bag
P0118DA 5AL3	P0118 DA	TO-92	0.2 g	2000	Tape and reel 13 inch
P0118DN 5AA4	P8D	SOT-223	0.11 g	1000	Tape and reel 7 inch
P0118MA 2AL3	P0118 MA	TO-92	0.2 g	2000	Ammopack
P0118MA 5AL3	P0118 MA	TO-92	0.2 g	2000	Tape and reel 13 inch

^{1.} This order code has no space.

5 Revision history

Table 8. Document revision history

Date	Revision	Description of changes
26-Jan-2009	1	First issue.

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