



NX3008NBKMB

30 V, single N-channel Trench MOSFET

5 November 2022

Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN1006B-3 (SOT883B) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Very fast switching
- Low threshold voltage
- Trench MOSFET technology
- ESD protection up to 2 kV
- Ultra thin package profile with 0.37 mm height

3. Applications

- Relay driver
- High-speed line driver
- Low-side load switch
- Switching circuits

4. Quick reference data

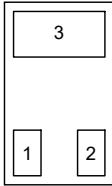
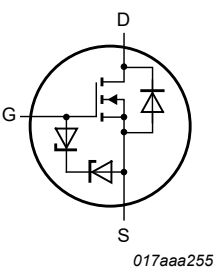
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{DS}	drain-source voltage	$T_j = 25\text{ °C}$		-	-	30	V
V_{GS}	gate-source voltage			-8	-	8	V
I_D	drain current	$V_{GS} = 4.5\text{ V}; T_{amb} = 25\text{ °C}$	[1]	-	-	530	mA
Static characteristics							
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 4.5\text{ V}; I_D = 350\text{ mA}; T_j = 25\text{ °C}$		-	1	1.4	Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm^2 .

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 Transparent top view DFN1006B-3 (SOT883B)	 017aaa255
2	S	source		
3	D	drain		

6. Ordering information

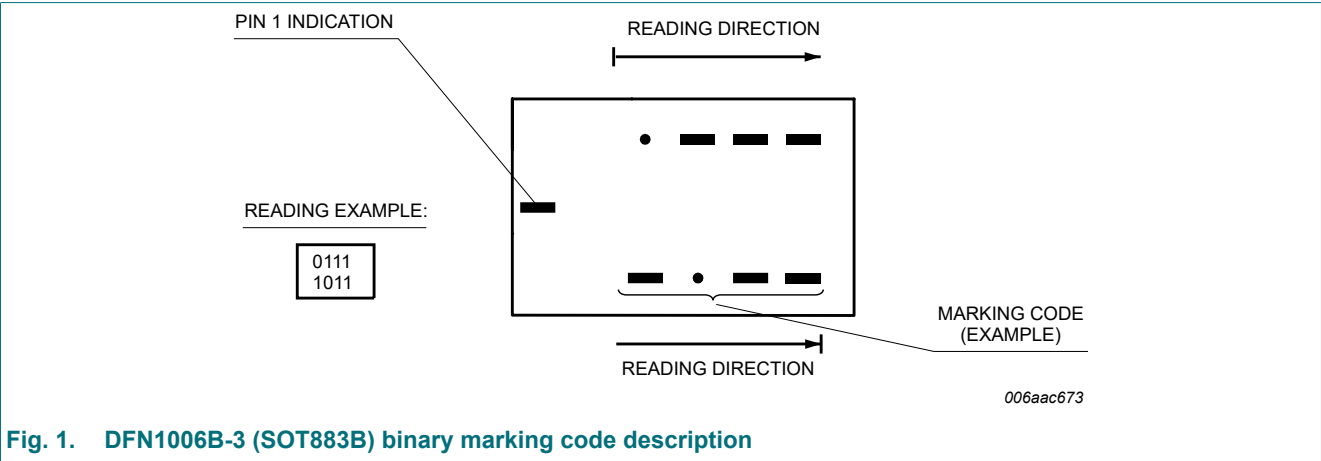
Table 3. Ordering information

Type number	Package		
	Name	Description	Version
NX3008NBKMB	DFN1006B-3	plastic, leadless ultra small plastic package; 3 solder lands; 0.35 mm pitch; 1.0 mm x 0.6 mm x 0.37 mm body	SOT883B

7. Marking

Table 4. Marking codes

Type number	Marking code
NX3008NBKMB	0000
	0011



8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	30	V
V _{GS}	gate-source voltage			-8	8	V
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	530	mA
		V _{GS} = 4.5 V; T _{amb} = 100 °C	[1]	-	330	mA
I _{DM}	peak drain current	T _{amb} = 25 °C; single pulse; t _p ≤ 10 μs		-	2.1	A
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	360	mW
			[1]	-	715	mW
		T _{sp} = 25 °C		-	2700	mW
T _j	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drain diode						
I _S	source current	T _{amb} = 25 °C	[1]	-	530	mA
ESD maximum rating						
V _{ESD}	electrostatic discharge voltage	HBM	[3]	-	2000	V

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Measured between all pins.

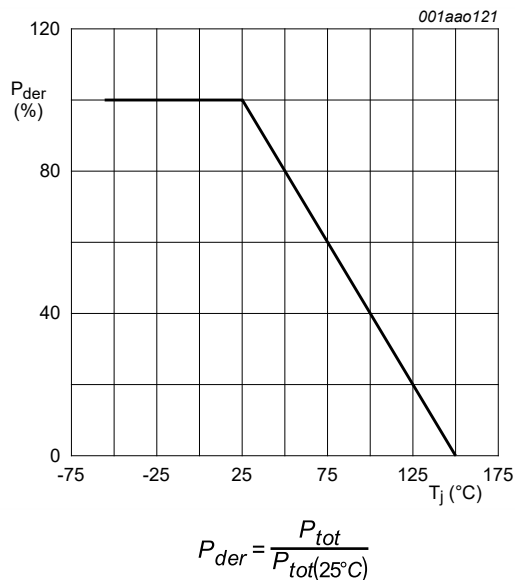


Fig. 2. Normalized total power dissipation as a function of junction temperature

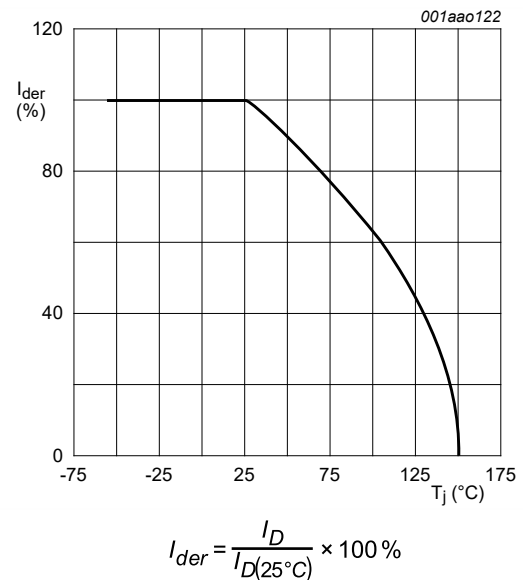
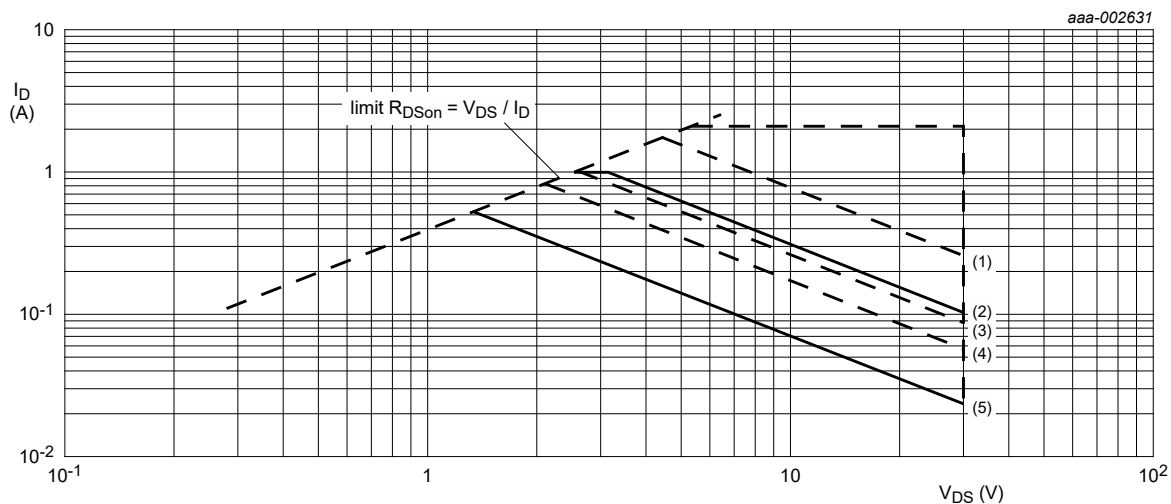


Fig. 3. Normalized continuous drain current as a function of junction temperature



I_{DM} is single pulse

(1) $t_p = 1$ ms

(2) DC; $T_{sp} = 25$ °C

(3) $t_p = 10$ ms

(4) $t_p = 100$ ms

(5) DC; $T_{amb} = 25$ °C; drain mounting pad 1 cm^2

Fig. 4. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	305	360	K/W
			[2]	-	150	175	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	40	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².

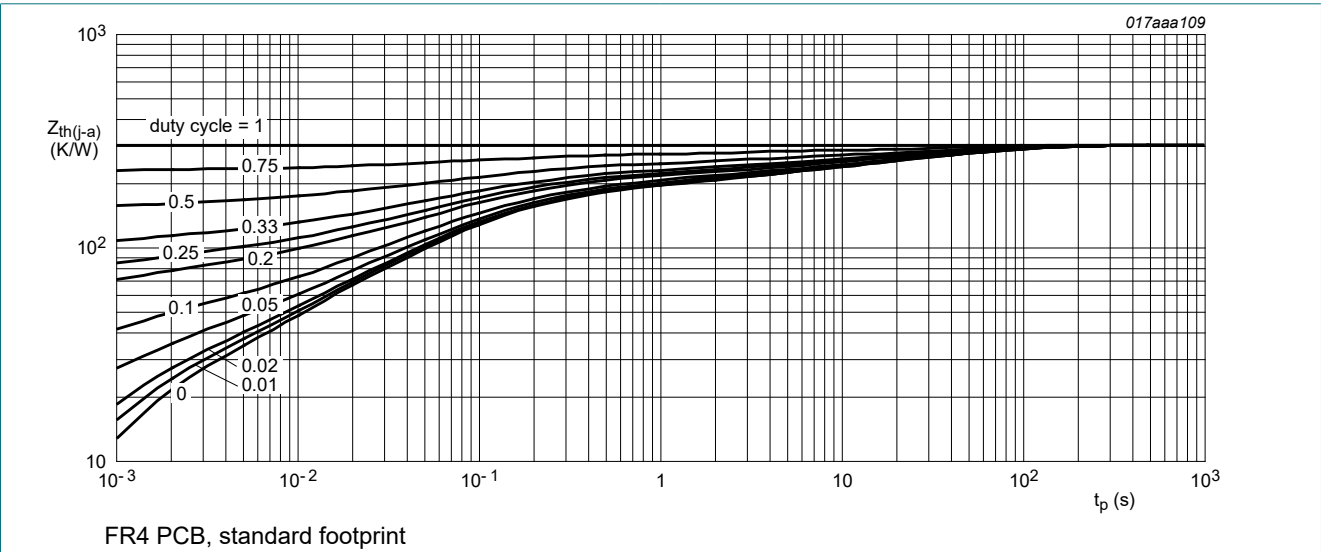


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

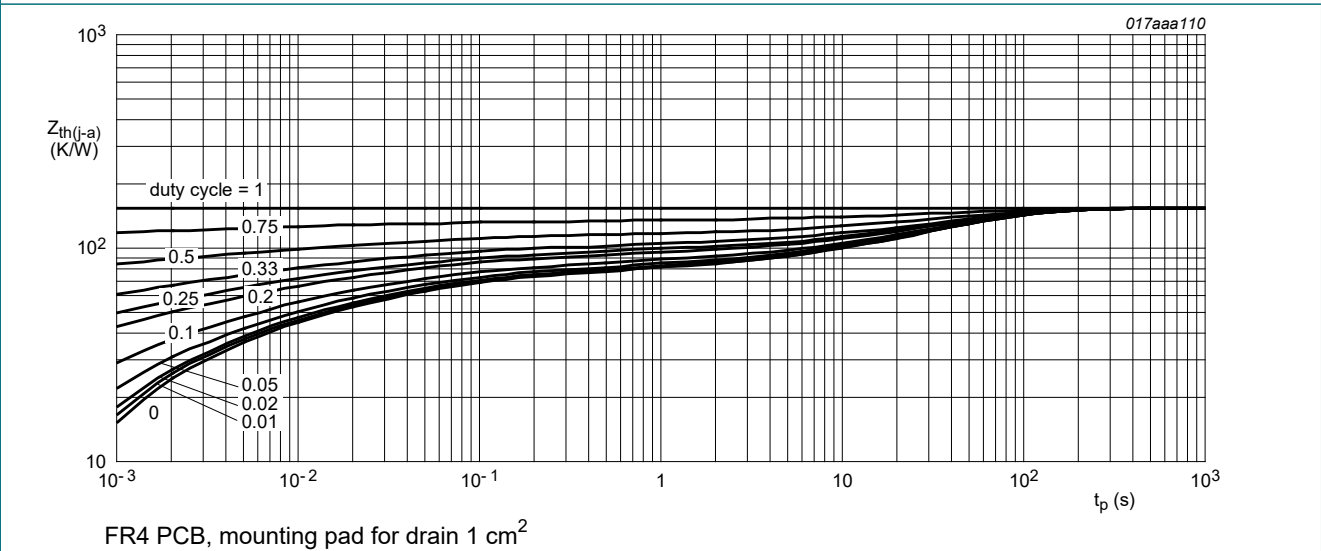


Fig. 6. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _j = 25 °C		30	-	-	V
V _{GSt}	gate-source threshold voltage	I _D = 250 μA; V _{DS} = V _{GS} ; T _j = 25 °C		0.6	0.9	1.1	V
I _{DSS}	drain leakage current	V _{DS} = 30 V; V _{GS} = 0 V; T _j = 25 °C		-	-	1	μA
		V _{DS} = 30 V; V _{GS} = 0 V; T _j = 150 °C		-	-	10	μA
I _{GSS}	gate leakage current	V _{GS} = 8 V; V _{DS} = 0 V; T _j = 25 °C		-	0.2	1	μA
		V _{GS} = -8 V; V _{DS} = 0 V; T _j = 25 °C		-	0.2	1	μA
R _{DSon}	drain-source on-state resistance	V _{GS} = 4.5 V; I _D = 350 mA; T _j = 25 °C		-	1	1.4	Ω
		V _{GS} = 4.5 V; I _D = 350 mA; T _j = 150 °C		-	1.8	2.5	Ω
		V _{GS} = 2.5 V; I _D = 200 mA; T _j = 25 °C		-	1.4	2.1	Ω
		V _{GS} = 1.8 V; I _D = 10 mA; T _j = 25 °C		-	2	2.8	Ω
g _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 350 mA; T _j = 25 °C		-	310	-	mS
Dynamic characteristics							
Q _{G(tot)}	total gate charge	V _{DS} = 15 V; I _D = 400 mA; V _{GS} = 4.5 V; T _j = 25 °C		-	0.52	0.68	nC
Q _{GS}	gate-source charge			-	0.17	-	nC
Q _{GD}	gate-drain charge			-	0.08	-	nC
C _{iss}	input capacitance	V _{DS} = 15 V; f = 1 MHz; V _{GS} = 0 V; T _j = 25 °C		-	34	50	pF
C _{oss}	output capacitance			-	6.5	-	pF
C _{rss}	reverse transfer capacitance			-	2.2	-	pF
t _{d(on)}	turn-on delay time	V _{DS} = 20 V; R _L = 250 Ω; V _{GS} = 4.5 V; R _{G(ext)} = 6 Ω; T _j = 25 °C		-	15	30	ns
t _r	rise time			-	11	-	ns
t _{d(off)}	turn-off delay time			-	69	138	ns
t _f	fall time			-	19	-	ns
Source-drain diode							
V _{SD}	source-drain voltage	I _S = 350 mA; V _{GS} = 0 V; T _j = 25 °C		0.47	0.85	1.2	V

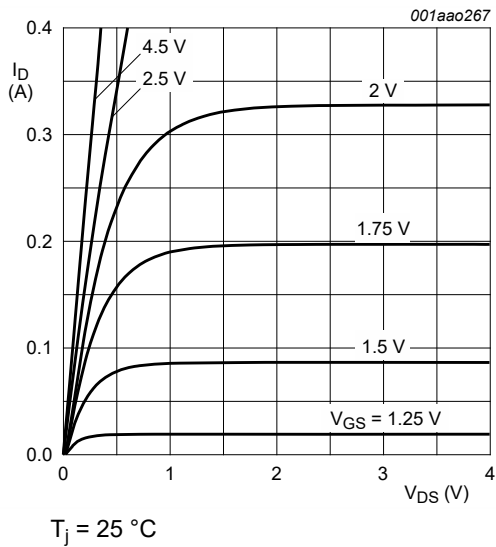


Fig. 7. Output characteristics: drain current as a function of drain-source voltage; typical values

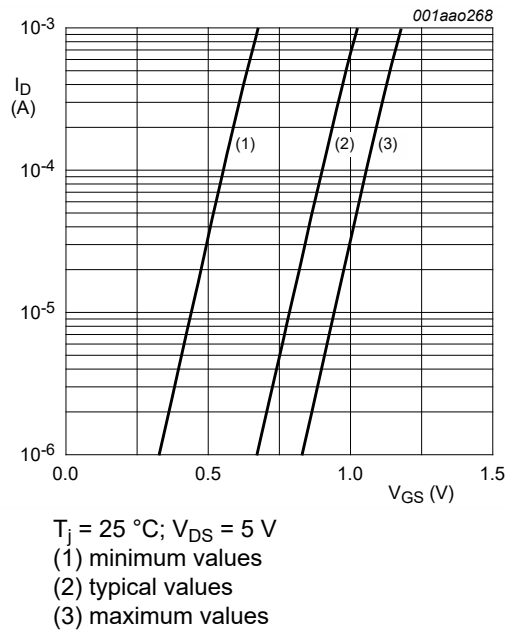


Fig. 8. Sub-threshold drain current as a function of gate-source voltage

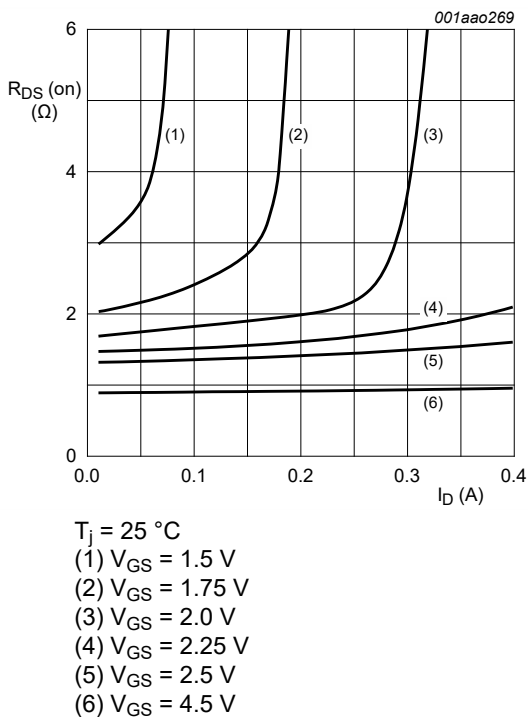


Fig. 9. Drain-source on-state resistance as a function of drain current; typical values

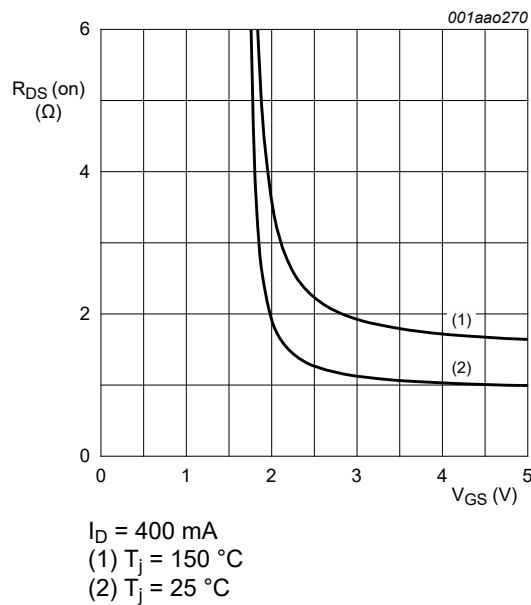


Fig. 10. Drain-source on-state resistance as a function of gate-source voltage; typical values

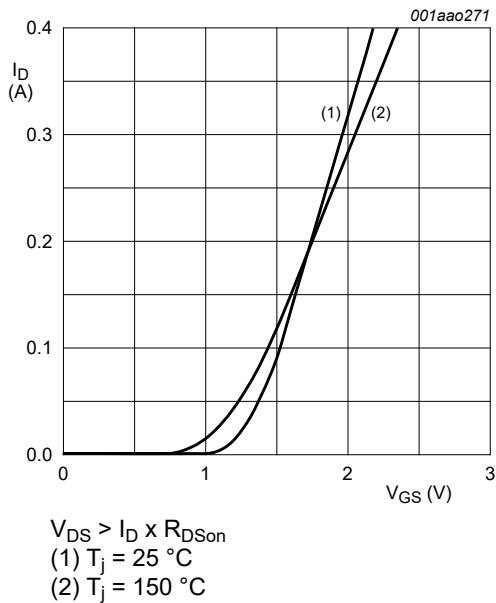


Fig. 11. Transfer characteristics: drain current as a function of gate-source voltage; typical values

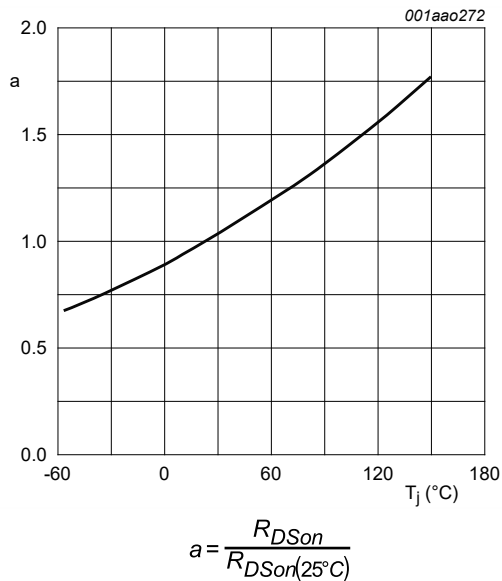


Fig. 12. Normalized drain-source on-state resistance as a function of junction temperature; typical values

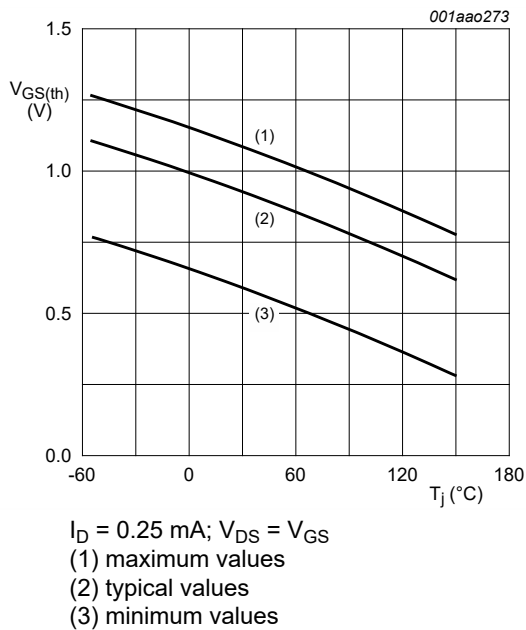


Fig. 13. Gate-source threshold voltage as a function of junction temperature

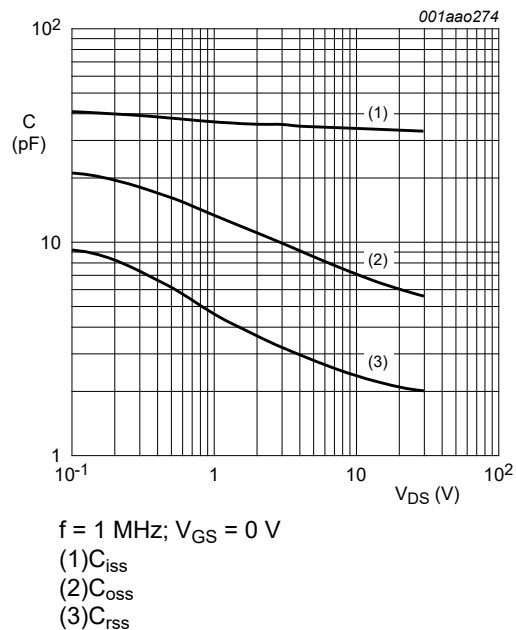


Fig. 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

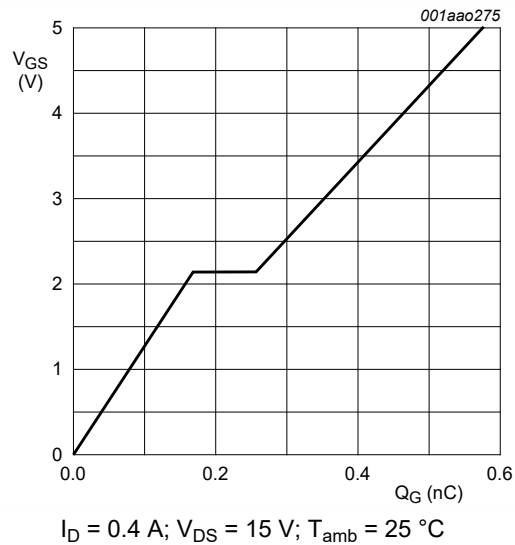


Fig. 15. Gate-source voltage as a function of gate charge; typical values

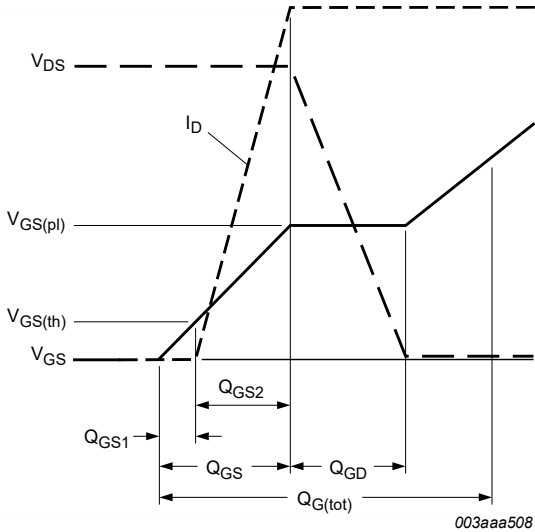


Fig. 16. Gate charge waveform definitions

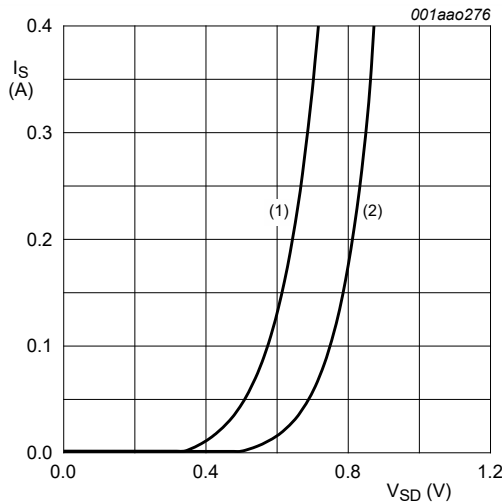


Fig. 17. Source current as a function of source-drain voltage; typical values

11. Test information

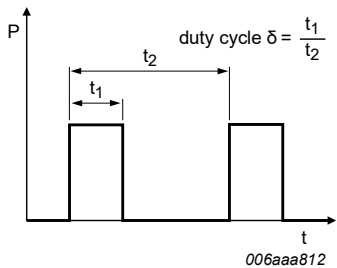


Fig. 18. Duty cycle definition

12. Package outline

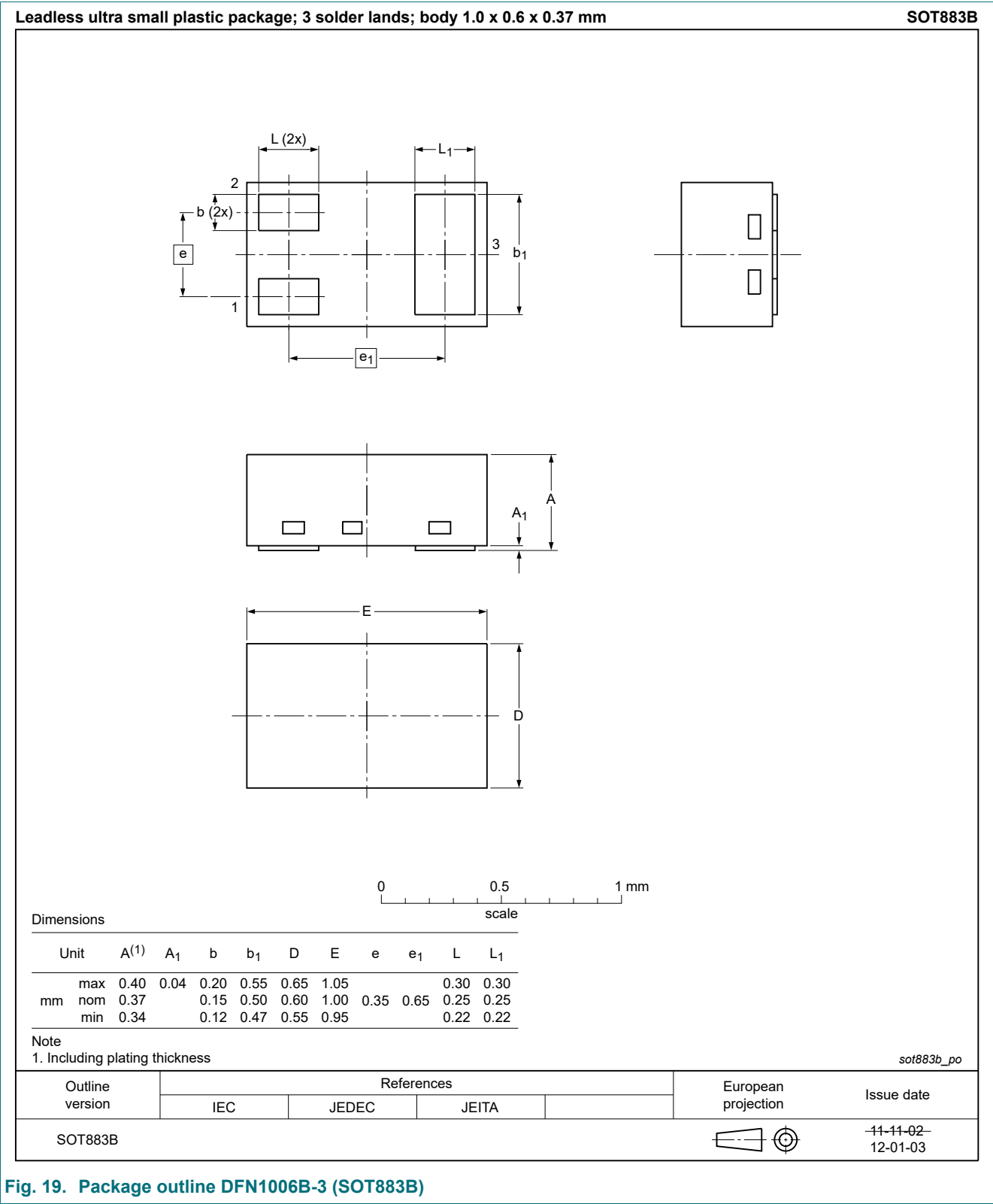
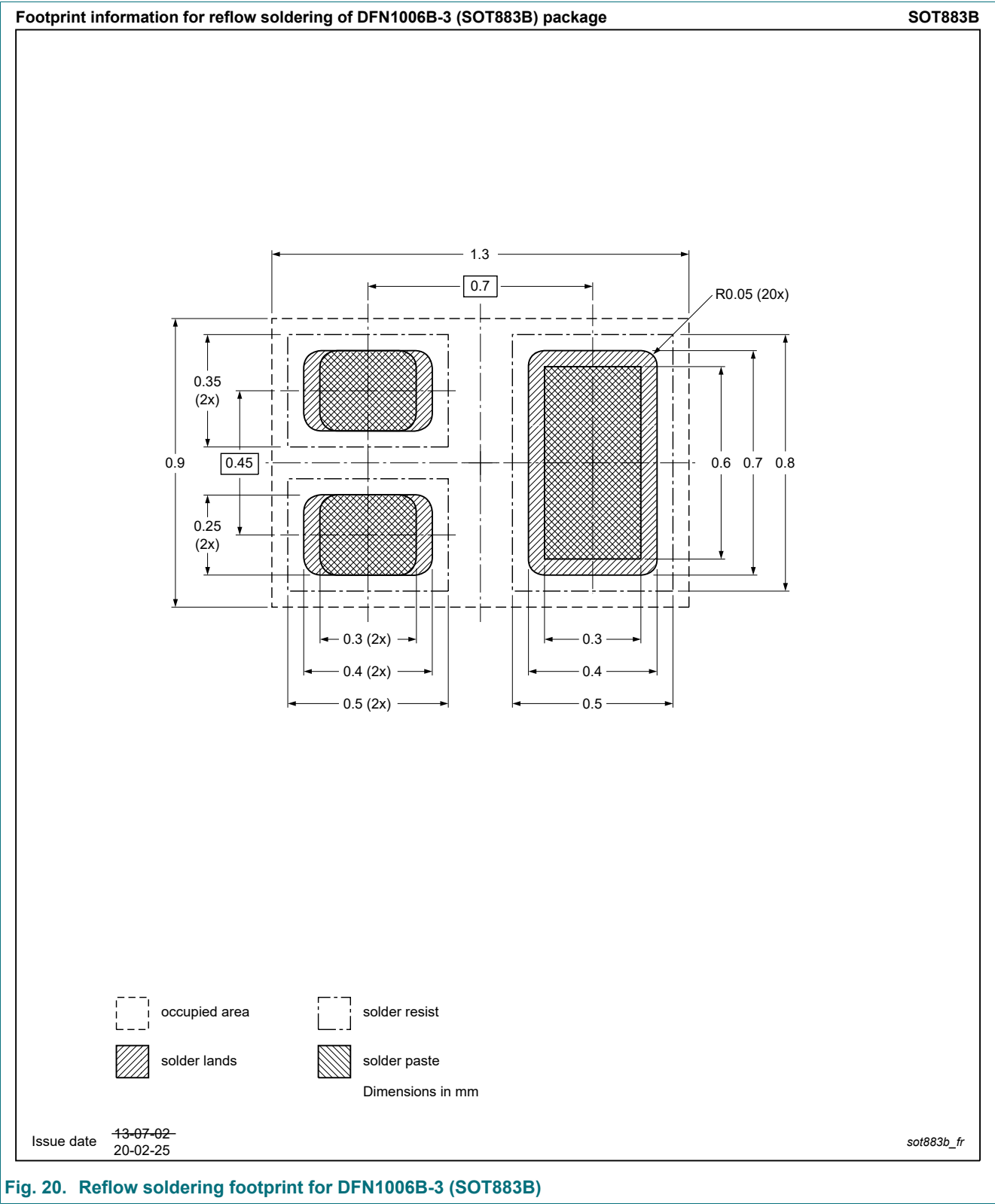


Fig. 19. Package outline DFN1006B-3 (SOT883B)

13. Soldering



14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
NX3008NBKMB v.2	20221105	Product data sheet	-	NX3008NBKMB v.1
Modifications:	• Chapter "Characteristics": typo correction, V_{SD} axis scaling for Fig. 17 revised			
NX3008NBKMB v.1	20120511	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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