Power MOSFET

-60 V, -15.5 A, Single P-Channel, DPAK

Features

- Withstands High Energy in Avalanche and Commutation Modes
- Low Gate Charge for Fast Switching
- AEC Q101 Qualified NTDV20P06L
- These Devices are Pb-Free and are RoHS Compliant

Applications

- Bridge Circuits
- Power Supplies, Power Motor Controls
- DC-DC Conversion

MAXIMUM RATINGS ($T_J = 25^{\circ}C$ unless otherwise noted)

	Symbol	Value	Unit		
Drain-to-Source Voltage			V_{DSS}	-60	V
Gate-to-Source	Continuous		V_{GS}	±20	V
Voltage	Non-Repetitive	$t_p \le 10 \text{ ms}$	V_{GSM}	±30	
Continuous Drain Current (Note 1)	Steady State T _A = 25°C		Ι _D	-15.5	Α
Power Dissipation (Note 1)	Steady State	T _A = 25°C	P _D	65	W
Pulsed Drain Current	t _p = 10	I _{DM}	±50	Α	
Operating Junction and Storage Temperature			T _J , T _{STG}	–55 to 175	°C
Single Pulse Drain–to–Source Avalanche Energy (V_{DD} = 25 V, V_{GS} = 5 V, I_{PK} = 15 A, L = 2.7 mH, R_G = 25 Ω)			E _{AS}	304	mJ
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Case (Drain)	$R_{\theta JC}$	2.3	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	80	
Junction-to-Ambient - Steady State (Note 2)	$R_{\theta JA}$	110	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. Surface-mounted on FR4 board using 1 in sq. pad size (Cu area = 1.127 in sq. [1 oz] including traces)
- Surface-mounted on FR4 board using the minimum recommended pad size (Cu area = 0.412 in sq.)

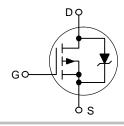


ON Semiconductor®

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V _{(BR)DSS}	R _{DS(on)} TYP	I _D MAX (Note 1)		
-60 V	130 mΩ @ -5.0 V	–15.5 A		

P-Channel

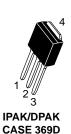


MARKING DIAGRAMS

Drain

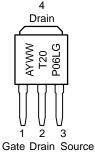


DPAK CASE 369C STYLE 2



STYLE 2

OZL OJOO OJ



20P06L Device Code A = Assembly Location

Y = Year

WW = Work Week

G = Pb-Free Package

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

ELECTRICAL CHARACTERISTICS ($T_J = 25$ °C unless otherwise noted)

Parameter	Symbol	Test Condition		Min	Тур	Max	Units
OFF CHARACTERISTICS							•
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$		-60	-74		V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J				-64		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V,	T _J = 25°C			-1.0	μΑ
		$V_{GS} = 0 \text{ V}, V_{DS} = -60 \text{ V}$	T _J = 150°C			-10	1
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS}$	= ±20 V			±100	nA
ON CHARACTERISTICS (Note 3)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D = 0$	–250 μΑ	-1.0	-1.5	-2.0	V
Gate Threshold Temperature Coefficient	V _{GS(TH)} /T _J				3.1		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	$V_{GS} = -5.0 \text{ V}, I_{D}$	= -7.5 A		0.130	0.150	Ω
		$V_{GS} = -5.0 \text{ V}, I_{D}$	= -15 A		0.143		1
Forward Transconductance	9FS	$V_{DS} = -10 \text{ V, } I_{D} = -10 \text{ V}$	= -7.5 A		11		S
Drain-to-Source On-Voltage	V _{DS(on)}	$V_{GS} = -5.0 \text{ V},$	T _J = 25°C			-1.2	V
		$I_D = -7.5 \text{ A}$ $T_J = 150^{\circ}\text{C}$				-1.9	1
CHARGES AND CAPACITANCES	•				•		
Input Capacitance	C _{ISS}	$V_{GS} = 0 \text{ V, f} = 1 \text{ MHz, } V_{DS} = -25 \text{ V}$			740	1190	pF
Output Capacitance	C _{OSS}				207	300	1
Reverse Transfer Capacitance	C _{RSS}				66	120	1
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -5.0 \text{ V}, V_{DS} = -48 \text{ V},$ $I_D = -18 \text{ A}$			15	26	nC
Gate-to-Source Charge	Q _{GS}				4.0		1
Gate-to-Drain Charge	Q_{GD}				7.0		1
SWITCHING CHARACTERISTICS (Note 4))						
Turn-On Delay Time	t _{d(ON)}				11	20	ns
Rise Time	t _r	Vas = -5.0 V. Vpr	o = −30 V.		90	180	1
Turn-Off Delay Time	t _{d(OFF)}	$V_{GS} = -5.0 \text{ V}, V_{DE}$ $I_D = -15 \text{ A}, R_G = -15 \text{ A}$	= 9.1 Ω		28	50	1
Fall Time	t _f				70	135	1
DRAIN-SOURCE DIODE CHARACTERIST	rics						•
Forward Diode Voltage	V_{SD}	T _J = 2	T _J = 25°C		1.5	2.5	V
		$V_{GS} = 0 \text{ V, } I_{S} = -15 \text{ A}$ $T_{J} = 150^{\circ}\text{C}$			1.3		1
Reverse Recovery Time	t _{RR}	$V_{GS} = 0 \text{ V, } d_{1S}/d_t = 100 \text{ A/}\mu\text{s,}$ $I_S = -12 \text{ A}$			60		ns
Charge Time	t _a				39		1
Discharge Time	t _b				21		1
Reverse Recovery Charge	Q _{RR}				0.13		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

^{3.} Pulse Test: pulse width $\leq 300~\mu s$, duty cycle $\leq 2\%$ 4. Switching characteristics are independent of operating junction temperatures

TYPICAL PERFORMANCE CURVES

(T_J = 25°C unless otherwise noted)

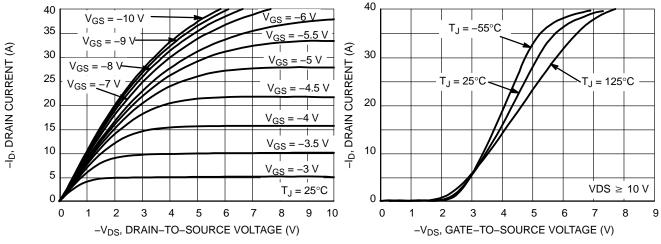


Figure 1. On-Region Characteristics

Figure 2. Transfer Characteristics

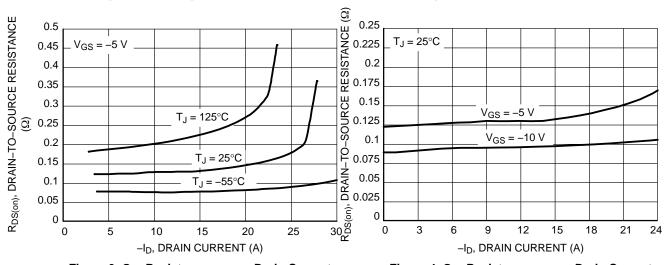


Figure 3. On–Resistance versus Drain Current and Temperature

Figure 4. On–Resistance versus Drain Current and Gate Voltage

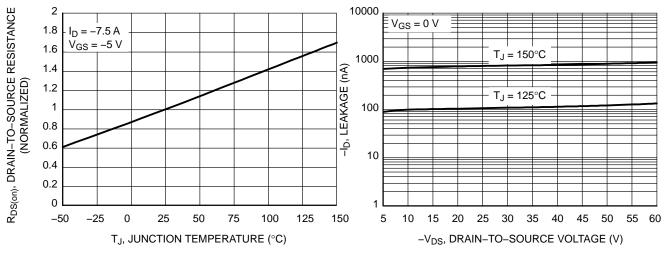
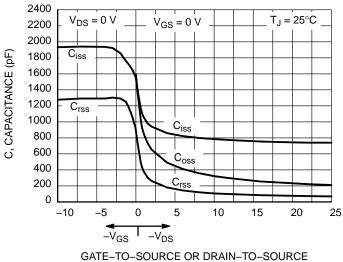


Figure 5. On–Resistance Variation with Temperature

Figure 6. Drain-to-Source Leakage Current versus Voltage



VOLTAGE (V)

Figure 7. Capacitance Variation

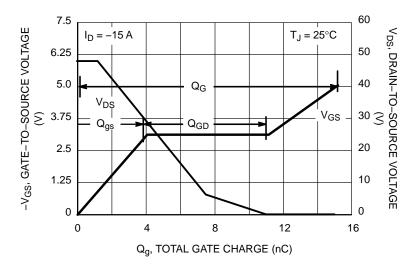
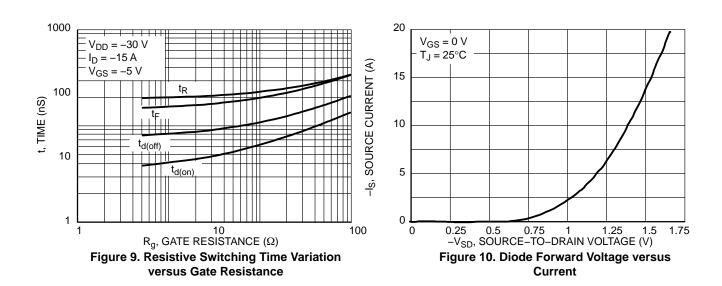


Figure 8. Gate-to-Source and Drain-to-Source Voltage versus Total Charge



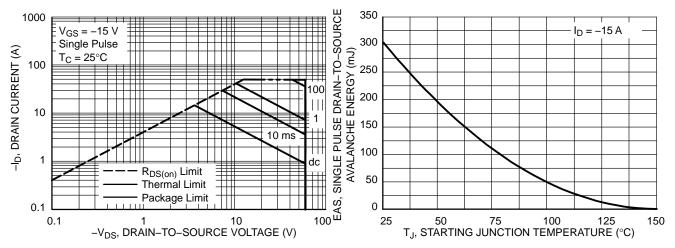
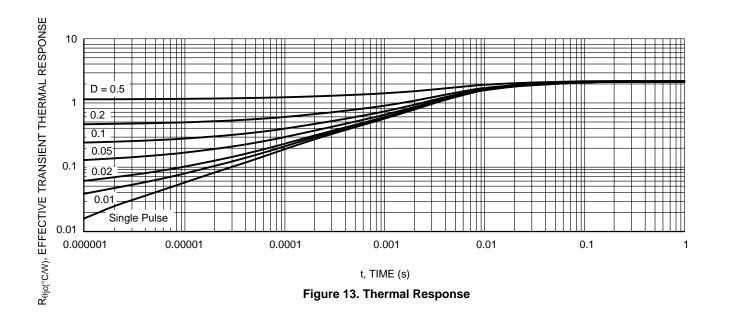


Figure 11. Maximum Rated Forward Biased Safe Operating Area

Figure 12. Maximum Avalanche Energy versus Starting Junction Temperature



ORDERING INFORMATION

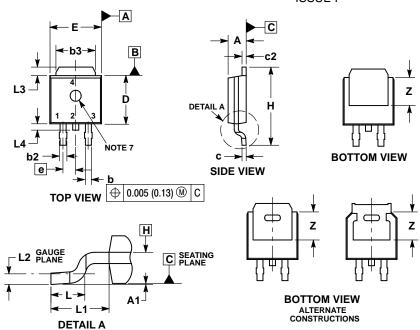
Device	Package	Shipping [†]	
NTD20P06LG		75 Units / Rail	
NTD20P06LT4G	DPAK (Pb-Free)	2500 / Tape & Reel	
NTDV20P06LT4G		2500 / Tape & Reel	
NTDV20P06LT4G-VF01		2500 / Tape & Reel	

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

DPAK (SINGLE GAUGE)

CASE 369C-01 ISSUE F



ROTATED 90° CW

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: INCHES.
 3. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS b3, L3 and Z.
- MENSIONS 03, L3 and Z.

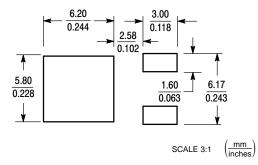
 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.006 INCHES PER SIDE.
- 5. DIMENSIONS D AND E ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
- 6. DATUMS A AND B ARE DETERMINED AT DATUM
- PLANE H.
 7. OPTIONAL MOLD FEATURE.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.086	0.094	2.18	2.38	
A1	0.000	0.005	0.00	0.13	
b	0.025	0.035	0.63	0.89	
b2	0.028	0.045	0.72	1.14	
b3	0.180	0.215	4.57	5.46	
С	0.018	0.024	0.46	0.61	
c2	0.018	0.024	0.46	0.61	
D	0.235	0.245	5.97	6.22	
E	0.250	0.265	6.35	6.73	
е	0.090 BSC		2.29 BSC		
Н	0.370	0.410	9.40	10.41	
L	0.055	0.070	1.40	1.78	
L1	0.114 REF		2.90 REF		
L2	0.020 BSC		0.51 BSC		
L3	0.035	0.050	0.89	1.27	
L4		0.040		1.01	
Z	0.155		3.93		

STYLE 2:

- PIN 1. GATE
 2. DRAIN
 3. SOURCE
 4. DRAIN

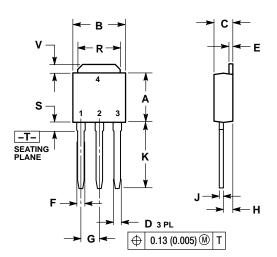
SOLDERING FOOTPRINT*

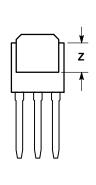


*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

IPAK CASE 369D-01 ISSUE C





NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- . CONTROLLING DIMENSION: INCH.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.235	0.245	5.97	6.35	
В	0.250	0.265	6.35	6.73	
С	0.086	0.094	2.19	2.38	
D	0.027	0.035	0.69	0.88	
Е	0.018	0.023	0.46	0.58	
F	0.037	0.045	0.94	1.14	
G	0.090	BSC	2.29	BSC	
Н	0.034	0.040	0.87	1.01	
ſ	0.018	0.023	0.46	0.58	
Κ	0.350	0.380	8.89	9.65	
R	0.180	0.215	4.45	5.45	
S	0.025	0.040	0.63	1.01	
٧	0.035	0.050	0.89	1.27	
Z	0.155		3.93		

STYLE 2:

PIN 1. GATE 2. DRAIN

- 3. SOURCE
- . DRAIN

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