

# TinyLogic ULP-A Buffer with Three-State Output

## NC7SP126

The NC7SP126 is a single non-inverting 3-state buffer in tiny footprint packages. The device is designed to operate for  $V_{CC} = 0.9\text{ V}$  to  $3.6\text{ V}$ .

### Features

- Designed for  $0.9\text{ V}$  to  $3.6\text{ V}$   $V_{CC}$  Operation
- $2.6\text{ ns}$   $t_{PD}$  at  $3.3\text{ V}$  (Typ)
- Inputs/Outputs Over-Voltage Tolerant up to  $3.6\text{ V}$
- $I_{OFF}$  Supports Partial Power Down Protection
- Source/Sink  $2.6\text{ mA}$  at  $3.3\text{ V}$
- Available in SC-88A and MicroPak™ Packages
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

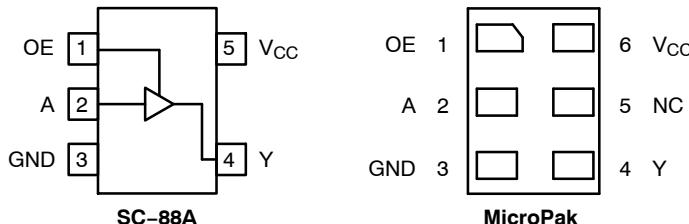


Figure 1. Pinout Diagrams (Top Views)

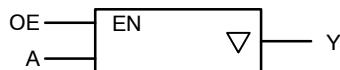


Figure 2. Logic Symbol

### PIN ASSIGNMENT

Pin	SC88A	MicroPak
1	OE	OE
2	A	A
3	GND	GND
4	Y	Y
5	$V_{CC}$	N.C.
6	-	$V_{CC}$

N.C. = No Connect

### FUNCTION TABLE

Input		Output
OE	A	Y
L	X	Z
H	L	L
H	H	H

X = Don't Care

Z = High Impedance State

## MAXIMUM RATINGS

Symbol	Characteristics	Value	Unit
$V_{CC}$	DC Supply Voltage	-0.5 to +4.3	V
$V_{IN}$	DC Input Voltage	-0.5 to +4.3	V
$V_{OUT}$	DC Output Voltage Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode ( $V_{CC} = 0$ V)	-0.5 to $V_{CC} + 0.5$ -0.5 to +4.3 -0.5 to +4.3	V
$I_{IK}$	DC Input Diode Current $V_{IN} < GND$	-50	mA
$I_{OK}$	DC Output Diode Current $V_{OUT} < GND$	-50	mA
$I_{OUT}$	DC Output Source/Sink Current	$\pm 50$	mA
$I_{CC}$ or $I_{GND}$	DC Supply Current per Supply Pin or Ground Pin	$\pm 50$	mA
$T_{STG}$	Storage Temperature Range	-65 to +150	°C
$T_L$	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C
$T_J$	Junction Temperature Under Bias	+150	°C
$\theta_{JA}$	Thermal Resistance (Note 2) SC-88A MicroPak	377 154	°C/W
$P_D$	Power Dissipation in Still Air SC-88A MicroPak	332 812	mW
MSL	Moisture Sensitivity	Level 1	-
$F_R$	Flammability Rating Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	-
$V_{ESD}$	ESD Withstand Voltage (Note 3) Human Body Model Charged Device Model	4000 2000	V
$I_{Latchup}$	Latchup Performance (Note 4)	$\pm 100$	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Applicable to devices with outputs that may be tri-stated.
2. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow per JESD51-7.
3. HBM tested to EIA / JESD22-A114-A. CDM tested to JESD22-C101-A. JEDEC recommends that ESD qualification to EIA/JESD22-A115A (Machine Model) be discontinued.
4. Tested to EIA/JESD78 Class II.

# NC7SP126

## RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
$V_{CC}$	Positive DC Supply Voltage	0.9	3.6	V
$V_{IN}$	DC Input Voltage	0	3.6	V
$V_{OUT}$	DC Output Voltage	0 0 0	$V_{CC}$ 3.6 3.6	
$T_A$	Operating Temperature Range	-40	+85	°C
$t_r, t_f$	Input Transition Rise and Fall Time	$V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$		ns/V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

## DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Condition	$V_{CC}$ (V)	$T_A = 25^\circ\text{C}$			$T_A = -40^\circ\text{C} \text{ to } +85^\circ\text{C}$		Unit
				Min	Typ	Max	Min	Max	
$V_{IH}$	High-Level Input Voltage		0.9	-	0.5	-	-	-	V
			1.1 to 1.3	$0.65 \times V_{CC}$	-	-	$0.65 \times V_{CC}$	-	
			1.4 to 1.6	$0.65 \times V_{CC}$	-	-	$0.65 \times V_{CC}$	-	
			1.65 to 1.95	$0.65 \times V_{CC}$	-	-	$0.65 \times V_{CC}$	-	
			2.3 to 2.7	1.6	-	-	1.6	-	
			3.0 to 3.6	2.1	-	-	2.1	-	
$V_{IL}$	Low-Level Input Voltage		0.9	-	0.5	-	-	-	V
			1.1 to 1.3	-	-	$0.35 \times V_{CC}$	-	$0.35 \times V_{CC}$	
			1.4 to 1.6	-	-	$0.35 \times V_{CC}$	-	$0.35 \times V_{CC}$	
			1.65 to 1.95	-	-	$0.35 \times V_{CC}$	-	$0.35 \times V_{CC}$	
			2.3 to 2.7	-	-	0.7	-	0.7	
			3.0 to 3.6	-	-	0.9	-	0.9	
$V_{OH}$	High-Level Output Voltage	$V_{IN} = V_{IH}$ or $V_{IL}$							V
		$I_{OH} = -20 \mu\text{A}$	0.9	-	$V_{CC} - 0.1$	-	-	-	
			1.1 to 1.3	$V_{CC} - 0.1$	-	-	$V_{CC} - 0.1$	-	
			1.4 to 1.6	$V_{CC} - 0.1$	-	-	$V_{CC} - 0.1$	-	
			1.65 to 1.95	$V_{CC} - 0.1$	-	-	$V_{CC} - 0.1$	-	
			2.3 to 2.7	$V_{CC} - 0.1$	-	-	$V_{CC} - 0.1$	-	
			3.0 to 3.6	$V_{CC} - 0.1$	-	-	$V_{CC} - 0.1$	-	
		$I_{OH} = -0.5 \text{ mA}$	1.1 to 1.3	$0.75 \times V_{CC}$	-	-	$0.70 \times V_{CC}$	-	
			1.4 to 1.6	1.07	-	-	0.99	-	
			1.65 to 1.95	1.24	-	-	1.22	-	
			2.3 to 2.7	1.95	-	-	1.87	-	
		$I_{OH} = -2.6 \text{ mA}$	3.0 to 3.6	2.61	-	-	2.55	-	

## DC ELECTRICAL CHARACTERISTICS (continued)

Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C			T <sub>A</sub> = -40°C to +85°C		Unit
				Min	Typ	Max	Min	Max	
V <sub>OL</sub>	Low-Level Output Voltage	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>							V
		I <sub>OL</sub> = 20 µA	0.9	—	0.1	—	—	—	
			1.1 to 1.3	—	—	0.1	—	0.1	
			1.4 to 1.6	—	—	0.1	—	0.1	
			1.65 to 1.95	—	—	0.1	—	0.1	
			2.3 to 2.7	—	—	0.1	—	0.1	
			3.0 to 3.6	—	—	0.1	—	0.1	
		I <sub>OL</sub> = 0.5 mA	1.1 to 1.3	—	—	0.3 x V <sub>CC</sub>	—	0.3 x V <sub>CC</sub>	
		I <sub>OL</sub> = 1 mA	1.4 to 1.6	—	—	0.31	—	0.37	
		I <sub>OL</sub> = 1.5 mA	1.65 to 1.95	—	—	0.31	—	0.35	
		I <sub>OL</sub> = 2.1 mA	2.3 to 2.7	—	—	0.31	—	0.33	
		I <sub>OL</sub> = 2.6 mA	3.0 to 3.6	—	—	0.31	—	0.33	
I <sub>IN</sub>	Input Leakage Current	V <sub>IN</sub> = 0 V to 3.6 V	0.9 to 3.6	—	—	±0.1	—	±0.5	µA
I <sub>OZ</sub>	3-State Output Leakage Current	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> V <sub>OUT</sub> = 0 V to 3.6 V	0.9 to 3.6	—	—	±0.5	—	±0.5	µA
I <sub>OFF</sub>	Power Off Leakage Current	V <sub>IN</sub> = 0 V to 3.6 V or V <sub>OUT</sub> = 0 V to 3.6 V	0	—	—	0.5	—	0.5	µA
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> = V <sub>CC</sub> or GND	0.9 to 3.6	—	—	0.9	—	0.9	µA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## AC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C			T <sub>A</sub> = -40°C to +85°C		Unit
				Min	Typ	Max	Min	Max	
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay, A to Y (Figures 3 and 4)	R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 10 pF	0.9	—	50.7	—	—	—	ns
			1.10 to 1.30	—	13.4	27.6	—	39.6	
			1.40 to 1.60	—	6.8	11.2	—	14.5	
			1.65 to 1.95	—	4.8	8.6	—	11.6	
			2.3 to 2.7	—	3.1	6.3	—	8.2	
			3.0 to 3.6	—	2.6	5.3	—	7.2	
t <sub>PZH</sub> , t <sub>PZL</sub>	Output Enable Time, OE to Y (Figures 3 and 4)	R <sub>1</sub> = R <sub>L</sub> = 5 kΩ C <sub>L</sub> = 10 pF	0.9	—	50.6	—	—	—	ns
			1.10 to 1.30	—	13.2	26.7	—	40.4	
			1.40 to 1.60	—	6.7	11.9	—	14.8	
			1.65 to 1.95	—	4.7	9.7	—	12.3	
			2.3 to 2.7	—	3.0	7.7	—	10.5	
			3.0 to 3.6	—	2.5	6.9	—	8.6	
t <sub>PHZ</sub> , t <sub>PLZ</sub>	Output Disable Time, OE to Y (Figures 3 and 4)	R <sub>1</sub> = R <sub>L</sub> = 5 kΩ C <sub>L</sub> = 10 pF	0.9	—	14.6	—	—	—	ns
			1.10 to 1.30	—	8.2	20.5	—	42.0	
			1.40 to 1.60	—	6.3	15.3	—	18.0	
			1.65 to 1.95	—	6.1	14.7	—	17.8	
			2.3 to 2.7	—	5.8	13.7	—	15.0	
			3.0 to 3.6	—	5.7	13.5	—	14.8	

## AC ELECTRICAL CHARACTERISTICS (continued)

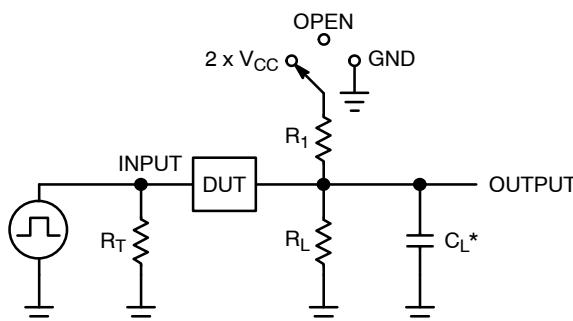
Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C			T <sub>A</sub> = -40°C to +85°C		Unit
				Min	Typ	Max	Min	Max	
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay, A to Y (Figures 3 and 4)	R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 15 pF	0.9	—	52.3	—	—	—	ns
			1.10 to 1.30	—	14.0	28.6	—	42.5	
			1.40 to 1.60	—	7.2	11.8	—	15.4	
			1.65 to 1.95	—	5.2	9.1	—	12.2	
			2.3 to 2.7	—	3.4	6.6	—	8.6	
			3.0 to 3.6	—	2.8	5.6	—	7.5	
t <sub>PZH</sub> , t <sub>PZL</sub>	Output Enable Time, OE to Y (Figures 3 and 4)	R <sub>1</sub> = R <sub>L</sub> = 5 kΩ C <sub>L</sub> = 15 pF	0.9	—	52.2	—	—	—	ns
			1.10 to 1.30	—	13.7	27.9	—	43.3	
			1.40 to 1.60	—	7.1	12.5	—	15.5	
			1.65 to 1.95	—	5.1	10.2	—	12.9	
			2.3 to 2.7	—	3.2	8.0	—	9.9	
			3.0 to 3.6	—	2.7	7.2	—	8.9	
t <sub>PHZ</sub> , t <sub>PLZ</sub>	Output Disable Time, OE to Y (Figures 3 and 4)	R <sub>1</sub> = R <sub>L</sub> = 5 kΩ C <sub>L</sub> = 15 pF	0.9	—	16.1	—	—	—	ns
			1.10 to 1.30	—	9.3	21.6	—	44.9	
			1.40 to 1.60	—	7.1	15.9	—	18.8	
			1.65 to 1.95	—	7.1	15.2	—	18.2	
			2.3 to 2.7	—	6.8	14.1	—	15.4	
			3.0 to 3.6	—	6.5	13.9	—	15.1	
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay, A to Y (Figures 3 and 4)	R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 30 pF	0.9	—	56.9	—	—	—	ns
			1.10 to 1.30	—	15.6	32.0	—	51.1	
			1.40 to 1.60	—	8.6	13.8	—	17.7	
			1.65 to 1.95	—	6.3	10.6	—	14.0	
			2.3 to 2.7	—	4.1	7.6	—	9.9	
			3.0 to 3.6	—	3.4	6.4	—	8.9	
t <sub>PZH</sub> , t <sub>PZL</sub>	Output Enable Time, OE to Y (Figures 3 and 4)	R <sub>1</sub> = R <sub>L</sub> = 5 kΩ C <sub>L</sub> = 30 pF	0.9	—	56.8	—	—	—	ns
			1.10 to 1.30	—	15.4	31.6	—	51.9	
			1.40 to 1.60	—	8.5	14.5	—	17.9	
			1.65 to 1.95	—	6.2	11.7	—	14.7	
			2.3 to 2.7	—	3.9	9.1	—	11.1	
			3.0 to 3.6	—	3.3	8.1	—	10.1	
t <sub>PHZ</sub> , t <sub>PLZ</sub>	Output Disable Time, OE to Y (Figures 3 and 4)	R <sub>1</sub> = R <sub>L</sub> = 5 kΩ C <sub>L</sub> = 30 pF	0.9	—	20.3	—	—	—	ns
			1.10 to 1.30	—	12.3	24.8	—	53.5	
			1.40 to 1.60	—	10.2	20.5	—	21.1	
			1.65 to 1.95	—	10.2	19.5	—	20.5	
			2.3 to 2.7	—	9.5	18.5	—	19.5	
			3.0 to 3.6	—	8.6	14.8	—	16.3	

## CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Test Condition	Typical (T <sub>A</sub> = 25°C)	Unit
C <sub>IN</sub>	Input Capacitance	V <sub>CC</sub> = 0 V	2.0	pF
C <sub>OUT</sub>	Output Capacitance	V <sub>CC</sub> = 0 V	4.0	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Note 5)	f = 10 MHz, V <sub>CC</sub> = 0.9 to 3.6 V, V <sub>IN</sub> = 0 V or V <sub>CC</sub>	8.0	pF

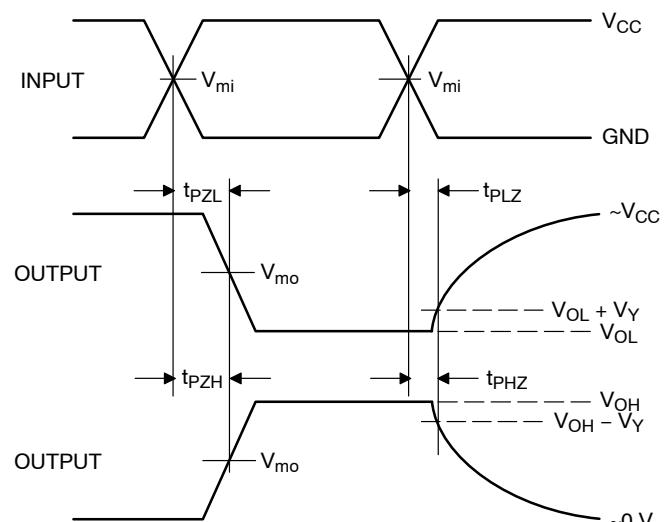
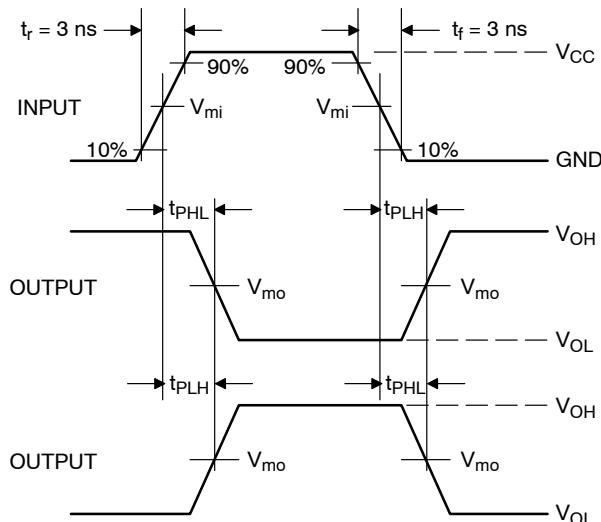
5. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation I<sub>CC(OPR)</sub> = C<sub>PD</sub> • V<sub>CC</sub> • f<sub>in</sub> + I<sub>CC</sub>. C<sub>PD</sub> is used to determine the no-load dynamic power consumption: P<sub>D</sub> = C<sub>PD</sub> • V<sub>CC</sub><sup>2</sup> • f<sub>in</sub> + I<sub>CC</sub> • V<sub>CC</sub>.

# NC7SP126



Test	Switch Position
$t_{PLH} / t_{PHL}$	Open
$t_{PLZ} / t_{PZL}$	$2 \times V_{CC}$
$t_{PHZ} / t_{PZH}$	GND

Figure 3. Test Circuit



$V_{CC}, V$	$V_{mi}, V$	$V_{mo}, V$	$V_Y, V$
0.9	$V_{CC} / 2$	$V_{CC} / 2$	0.1
1.1 to 1.3	$V_{CC} / 2$	$V_{CC} / 2$	0.1
1.4 to 1.6	$V_{CC} / 2$	$V_{CC} / 2$	0.1
1.65 to 1.95	$V_{CC} / 2$	$V_{CC} / 2$	0.15
2.3 to 2.7	$V_{CC} / 2$	$V_{CC} / 2$	0.15
3.0 to 3.6	1.5	1.5	0.3

Figure 4. Switching Waveforms

# NC7SP126

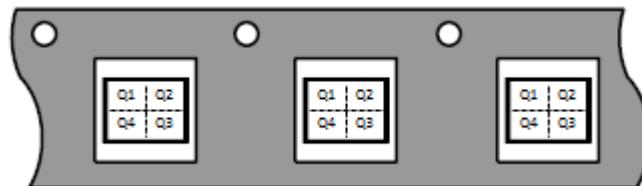
## ORDERING INFORMATION

Device	Package	Marking	Pin 1 Orientation (See below)	Shipping <sup>†</sup>
NC7SP126P5X	SC-88A	P26	Q4	3000 / Tape & Reel
NC7SP126P5X-L22735	SC-88A	P26	Q4	3000 / Tape & Reel
NC7SP126L6X	MicroPak	L6	Q4	5000 / Tape & Reel
NC7SP126FHX	MicroPak2	L6	Q4	5000 / Tape & Reel
NC7SP126FHX-L22780	MicroPak2	L6	Q4	5000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

### Pin 1 Orientation in Tape and Reel

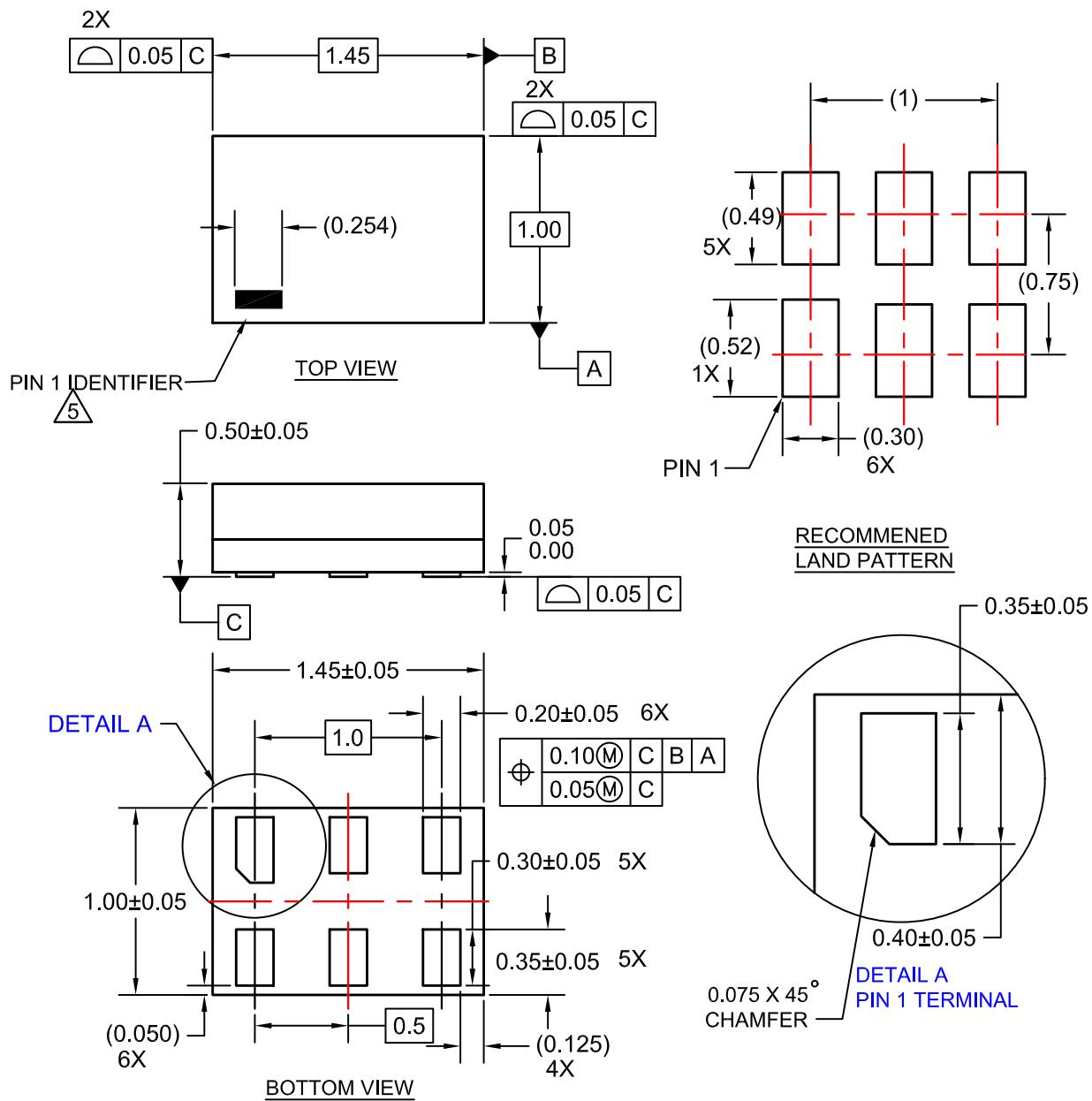
#### Direction of Feed



# NC7SP126

## PACKAGE DIMENSIONS

SIP6 1.45X1.0  
CASE 127EB  
ISSUE O

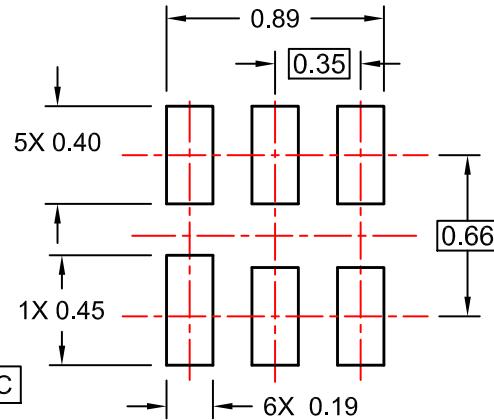
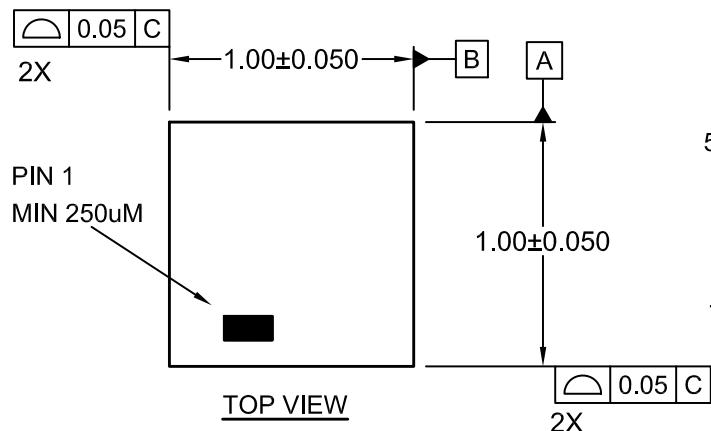


### NOTES:

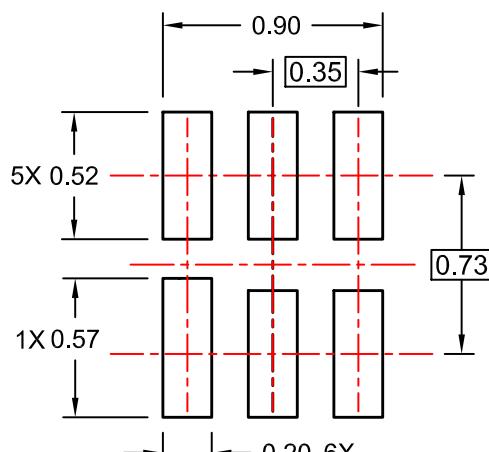
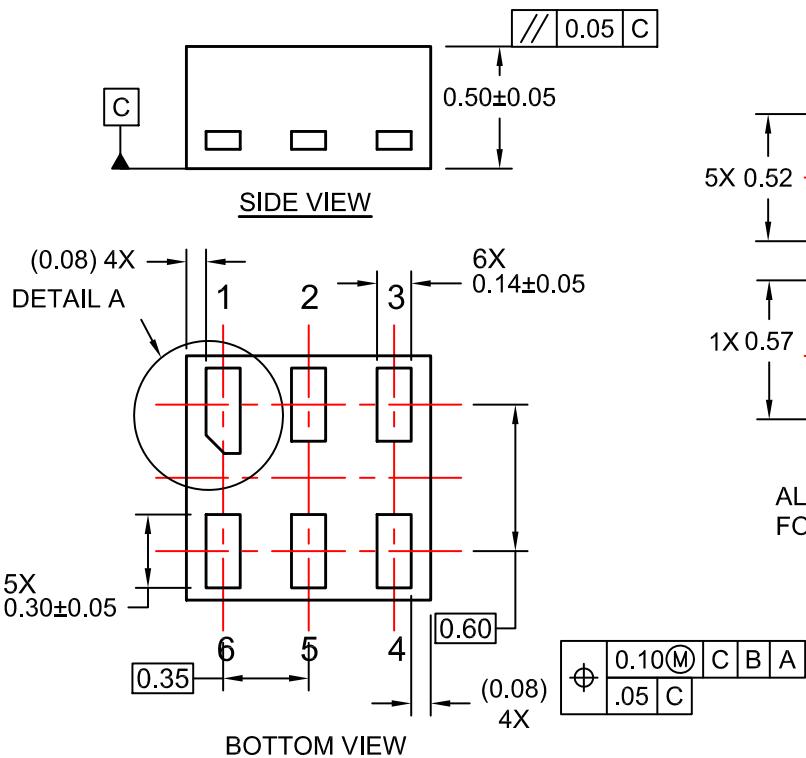
1. CONFORMS TO JEDEC STANDARD MO-252 VARIATION UAAD
2. DIMENSIONS ARE IN MILLIMETERS
3. DRAWING CONFORMS TO ASME Y14.5M-2009
4. PIN ONE IDENTIFIER IS 2X LENGTH OF ANY OTHER LINE IN THE MARK CODE LAYOUT.

## PACKAGE DIMENSIONS

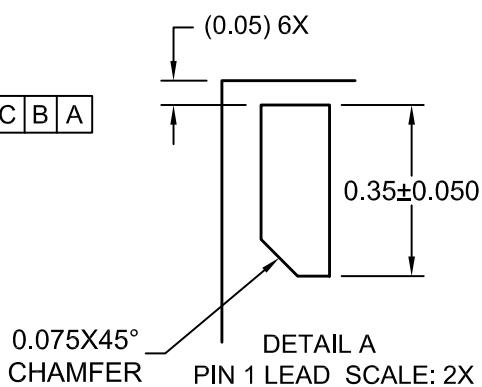
**UDFN6 1.0X1.0, 0.35P**  
CASE 517DP  
ISSUE O



RECOMMENDED LAND PATTERN  
FOR SPACE CONSTRAINED PCB



ALTERNATIVE LAND PATTERN  
FOR UNIVERSAL APPLICATION

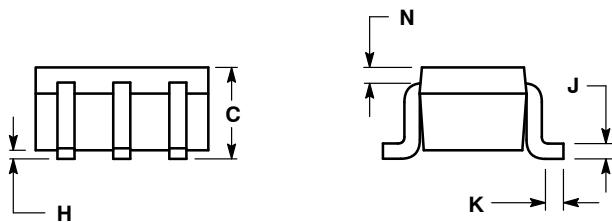
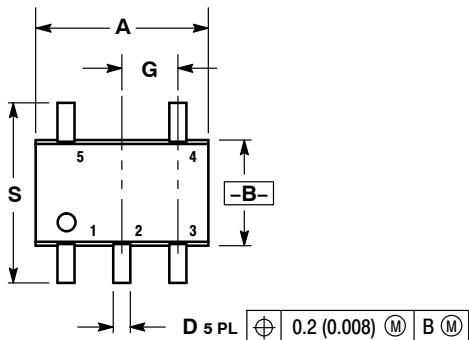


## NOTES:

- A. COMPLIES TO JEDEC MO-252 STANDARD
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 2009

## PACKAGE DIMENSIONS

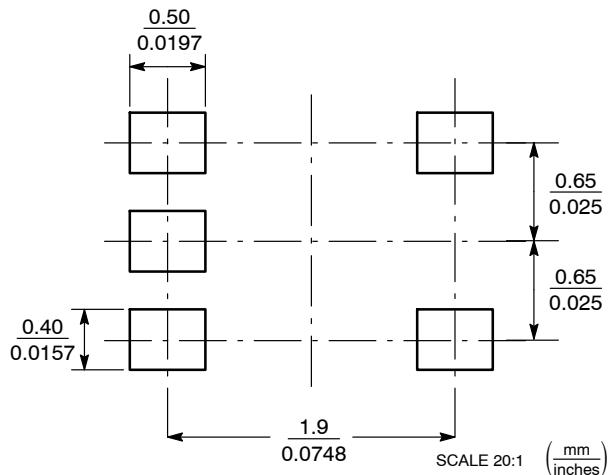
**SC-88A (SC-70-5/SOT-353)**  
CASE 419A-02  
ISSUE L



NOTES:  
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.  
 2. CONTROLLING DIMENSION: INCH.  
 3. 419A-01 OBSOLETE. NEW STANDARD 419A-02.  
 4. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.071	0.087	1.80	2.20
B	0.045	0.053	1.15	1.35
C	0.031	0.043	0.80	1.10
D	0.004	0.012	0.10	0.30
G	0.026 BSC		0.65 BSC	
H	---	0.004	---	0.10
J	0.004	0.010	0.10	0.25
K	0.004	0.012	0.10	0.30
N	0.008 REF		0.20 REF	
S	0.079	0.087	2.00	2.20

## SOLDER FOOTPRINT



STYLE 1:  
PIN 1. BASE  
2. Emitter  
3. BASE  
4. COLLECTOR  
5. COLLECTOR

STYLE 2:  
PIN 1. ANODE  
2. Emitter  
3. BASE  
4. COLLECTOR  
5. CATHODE

STYLE 3:  
PIN 1. ANODE 1  
2. N/C  
3. ANODE 2  
4. CATHODE 2  
5. CATHODE 1

STYLE 4:  
PIN 1. SOURCE 1  
2. DRAIN 1/2  
3. SOURCE 1  
4. GATE 1  
5. GATE 2

STYLE 5:  
PIN 1. CATHODE  
2. COMMON ANODE  
3. CATHODE 2  
4. CATHODE 3  
5. CATHODE 4

STYLE 6:  
PIN 1. Emitter 2  
2. BASE 2  
3. Emitter 1  
4. COLLECTOR  
5. COLLECTOR 2/BASE 1

STYLE 7:  
PIN 1. BASE  
2. Emitter  
3. BASE  
4. COLLECTOR  
5. COLLECTOR

STYLE 8:  
PIN 1. CATHODE  
2. COLLECTOR  
3. N/C  
4. BASE  
5. Emitter

STYLE 9:  
PIN 1. ANODE  
2. CATHODE  
3. ANODE  
4. ANODE  
5. ANODE

Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

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