SWITCHMODE Power Rectifier

Dual Schottky Rectifier

This device uses the Schottky Barrier technology with a platinum barrier metal. This state-of-the-art device is designed for use in high frequency switching power supplies and converters with up to 48 V outputs. They block up to 200 V and offer improved Schottky performance at frequencies from 250 kHz to 5.0 MHz.

Features

- 200 V Blocking Voltage
- Low Forward Voltage Drop
- Guardring for Stress Protection and High dv/dt Capability (10,000 V/μs)
- Dual Diode Construction Terminals 1 and 3 Must be Connected for Parallel Operation at Full Rating
- AEC-Q101 Qualified and PPAP Capable
- SBRB Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- All Packages are Pb-Free*

Mechanical Characteristics:

- Case: Epoxy, Molded, Epoxy Meets UL 94 V-0
- Weight: 1.7 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Device Meets MSL1 Requirements
- ESD Rating:
 - ♦ Human Body Model = 3B
 - ♦ Machine Model = C



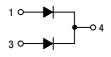
ON Semiconductor®

http://onsemi.com

SCHOTTKY BARRIER RECTIFIER 20 AMPERES, 200 V



D²PAK CASE 418B



MARKING DIAGRAM



A = Assembly Location

Y = Year

WW = Work Week

B20200 = Device Code

G = Pb-Free Package

AKA = Diode Polarity

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MAXIMUM RATINGS (Per Leg)

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	200	V
Average Rectified Forward Current (At Rated V_R , T_C = 134°C) Per Leg Per Device	I _{F(AV)}	10 20	A
Peak Repetitive Forward Current (At Rated V _R , Square Wave, 20 kHz, T _C = +137°C) Per Leg	I _{FRM}	20	А
Nonrepetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	150	Α
Peak Repetitive Reverse Surge Current (2.0 μs, 1.0 kHz)	I _{RRM}	1.0	Α
Storage Temperature Range	T _{stg}	-65 to +175	°C
Operating Junction Temperature	T _J	-65 to +150	°C
Voltage Rate of Change (Rated V _R)	dv/dt	10,000	V/μs

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS (Per Leg)

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	2.0	°C/W

ELECTRICAL CHARACTERISTICS (Per Leg)

Characteristic	Symbol	Value	Unit
Maximum Instantaneous Forward Voltage (Note 1) ($I_F = 10 \text{ A}$, $T_C = 25^{\circ}\text{C}$) ($I_F = 10 \text{ A}$, $T_C = 125^{\circ}\text{C}$) ($I_F = 20 \text{ A}$, $T_C = 25^{\circ}\text{C}$) ($I_F = 20 \text{ A}$, $T_C = 125^{\circ}\text{C}$)	V _F	0.9 0.8 1.0 0.9	V
Maximum Instantaneous Reverse Current (Note 1) (Rated dc Voltage, $T_C = 25^{\circ}C$) (Rated dc Voltage, $T_C = 125^{\circ}C$)	I _R	1.0 50	mA

DYNAMIC CHARACTERISTICS (Per Leg)

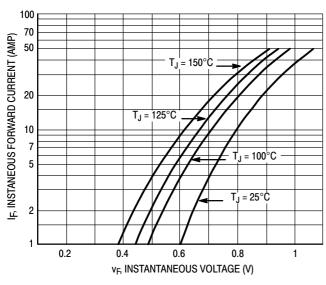
Capacitance	C _T		pF
$(V_{R} = -5.0 \text{ V}, T_{C} = 25^{\circ}\text{C}, \text{ Frequency} = 1.0 \text{ MHz})$		500	

^{1.} Pulse Test: Pulse Width = 300 μs, Duty Cycle ≤ 2.0%.

ORDERING INFORMATION

Device	Package	Shipping [†]
MBRB20200CTG	D ² PAK (Pb-Free)	50 Units / Rail
MBRB20200CTT4G	D ² PAK (Pb-Free)	800 Units / Tape & Reel
SBRB20200CTT4G	D ² PAK (Pb-Free)	800 Units / Tape & Reel

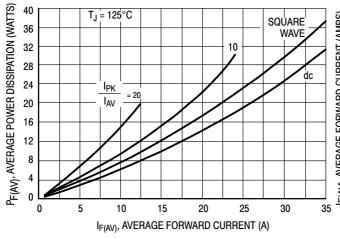
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



10,000 $T_J = 150^{\circ}C$ 1,000 T_J = 125°C , REVERSE CURRENT (µ.A) ₁ = 100°C <u>~</u> 0.1 $T_J = 25^{\circ}C$ 0.01 0 20 40 100 120 180 200 V_R, REVERSE CURRENT (V)

Figure 1. Typical Forward Voltage (Per Leg)

Figure 2. Typical Reverse Current (Per Leg)



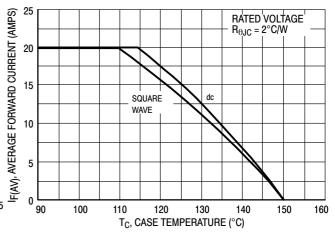
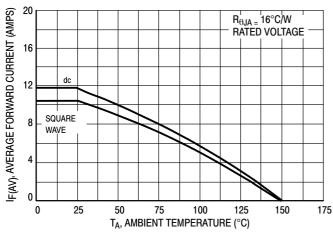


Figure 3. Forward Power Dissipation

Figure 4. Current Derating, Case



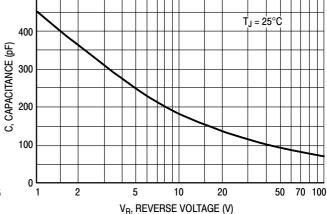


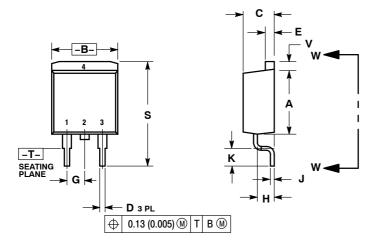
Figure 5. Current Derating, Ambient

Figure 6. Typical Capacitance (Per Leg)

500

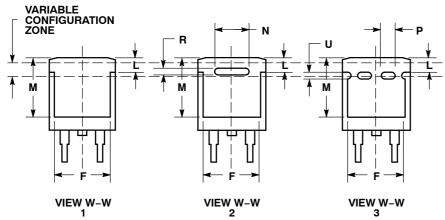
PACKAGE DIMENSIONS

D²PAK 3 CASE 418B-04 ISSUE K

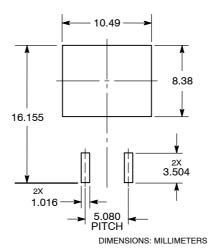


- NOTES:
 1. DIMENSIONING AND TOLERANCING
- PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. 418B-01 THRU 418B-03 OBSOLETE, NEW STANDARD 418B-04.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.340	0.380	8.64	9.65
В	0.380	0.405	9.65	10.29
С	0.160	0.190	4.06	4.83
D	0.020	0.035	0.51	0.89
Е	0.045	0.055	1.14	1.40
F	0.310	0.350	7.87	8.89
G	0.100 BSC		2.54 BSC	
Н	0.080	0.110	2.03 2.79	
J	0.018	0.025	0.46	0.64
K	0.090	0.110	2.29	2.79
L	0.052	0.072	1.32	1.83
М	0.280	0.320	7.11	8.13
N	0.197 REF		5.00	REF
Р	0.079 REF		2.00 REF	
R	0.039	REF	0.99 REF	
S	0.575	0.625	14.60	15.88
v	0.045	0.055	1.14	1.40



SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and un are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice on semiconductor and war engineer trademarks of semiconductor components industries, Ite (SciLLC) solitate services are injective to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center

Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative