19-1379; Rev 1; 4/99

EVALUATION KIT MANUAL
FOLLOWS DATA SHEET

MAXIM

High-Speed, Low-Power, Single-Supply, Multichannel, Video Multiplexer-Amplifiers

MAX4310–MAX4315

General Description

The MAX4310–MAX4315 single-supply mux-amps combine high-speed operation, low-glitch switching, and excellent video specifications. The six products in this family are differentiated by the number of multiplexer inputs and the gain configuration. The MAX4310/MAX4311/MAX4312 integrate 2-/4-/8-channel multiplexers, respectively, with an adjustable gain amplifier optimized for unity-gain stability. The MAX4313/MAX4314/MAX4315 integrate 2-/4-/8-channel multiplexers, respectively, with a +2V/V fixed-gain amplifier. All devices have 40ns channel switching time and low 10mVp-p switching transients, making them ideal for video-switching applications. They operate from a single +4V to +10.5V supply, or from dual supplies of ±2V to ±5.25V, and they feature Rail-to-Rail® outputs and an input common-mode voltage range that extends to the negative supply rail.

The MAX4310/MAX4311/MAX4312 have a -3dB bandwidth of 280MHz/345MHz/265MHz and up to a 460V/µs slew rate. The MAX4313/MAX4314/MAX4315, with 150MHz/127MHz/97MHz -3dB bandwidths up to a 540V/µs slew rate, and a fixed gain of +2V/V, are ideally suited for driving back-terminated cables. Quiescent supply current is as low as 6.1mA, while low-power shutdown mode reduces supply current to as low as 560µA and places the outputs in a high-impedance state. The MAX4310–MAX4315's internal amplifiers maintain an open-loop output impedance of only 8Ω over the full output voltage range, minimizing the gain error and bandwidth changes under loads typical of most rail-to-rail amplifiers. With differential gain and phase errors of 0.06% and 0.08°, respectively, these devices are ideal for broadcast video applications.

Applications

- Video Signal Multiplexing
- Video Crosspoint Switching
- Flash ADC Input Buffers
- 75Ω Video Cable Drivers
- High-Speed Signal Processing
- Broadcast Video
- Medical Imaging
- Multimedia Products

Features

- **Single-Supply Operation Down to +4V**
- **345MHz -3dB Bandwidth (MAX4311)**
  150MHz -3dB Bandwidth (MAX4313)
- **540V/µs Slew Rate (MAX4313)**
- **Low 6.1mA Quiescent Supply Current**
- **40ns Channel Switching Time**
- **Ultra-Low 10mVp-p Switching Transient**
- **0.06%/0.08° Differential Gain/Phase Error**
- **Rail-to-Rail Outputs: Drives 150Ω to within 730mV of the Rails**
- **Input Common-Mode Range Includes Negative Rail**
- **Low-Power Shutdown Mode**
- **Available in Space-Saving 8-Pin µMAX and 16-Pin QSOP Packages**

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX4310EUA	-40°C to +85°C	8 µMAX
MAX4310ESA	-40°C to +85°C	8 SO
MAX4311EEE	-40°C to +85°C	16 QSOP
MAX4311ESD	-40°C to +85°C	14 Narrow SO
MAX4312EEE	-40°C to +85°C	16 QSOP
MAX4312ESE	-40°C to +85°C	16 Narrow SO
MAX4313EUA	-40°C to +85°C	8 µMAX
MAX4313ESA	-40°C to +85°C	8 SO
MAX4314EEE	-40°C to +85°C	16 QSOP
MAX4314ESD	-40°C to +85°C	14 Narrow SO
MAX4315EEE	-40°C to +85°C	16 QSOP
MAX4315ESE	-40°C to +85°C	16 Narrow SO

Pin Configurations and Typical Operating Circuit appear at end of data sheet.

Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd.

Selector Guide

PART	NO. OF INPUT CHANNELS	AMPLIFIER GAIN (V/V)	PIN-PACKAGE
MAX4310	2	≥ +1	8-Pin SO/µMAX
MAX4311	4	≥ +1	14-Pin Narrow SO, 16-Pin QSOP
MAX4312	8	≥ +1	16-Pin Narrow SO/QSOP
MAX4313	2	+2	8-Pin SO/µMAX
MAX4314	4	+2	14-Pin Narrow SO, 16-Pin QSOP
MAX4315	8	+2	16-Pin Narrow SO/QSOP

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For small orders, phone 1-800-835-8769.***

ABSOLUTE MAXIMUM RATINGS

Supply Voltage (V_{CC} to V_{EE}) 12V
Input Voltage....(V_{EE} - 0.3V) to (V_{CC} + 0.3V)
All Other Pins ....(V_{EE} - 0.3V) to (V_{CC} + 0.3V)
Output Current....±120mA
Short-Circuit Duration (V_{OUT} to GND, V_{CC} or V_{EE}) ....Continuous
Continuous Power Dissipation (T_A = +70°C)
8-Pin SO (derate 5.9mW/°C above +70°C)....471mW
8-Pin µMAX (derate 4.1mW/°C above +70°C) 330mW
14-Pin SO (derate 8.3mW/°C above +70°C)....667mW
16-Pin SO (derate 8.7mW/°C above +70°C)....696mW
16-Pin QSOP (derate 8.3mW/°C above +70°C)....667mW
Operating Temperature Range ....-40°C to +85°C
Storage Temperature Range ....-65°C to +150°C
Lead Temperature (soldering, 10sec) ....+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

(V_{CC} = +5V, V_{EE} = 0, $\overline{SHDN}$ ≥ 4V, R_L = ∞, V_{OUT} = 2.5V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Operating Supply Voltage Range	V_{CC}	Inferred from PSRR test	4.0		10.5	V
Input Voltage Range		MAX4310/MAX4311/MAX4312, inferred from CMRR test	0.035		V_{CC} - 2.8	V
		MAX4313/MAX4314/1MAX4315, inferred from output voltage swing	0.035		V_{CC} - 2.7	
Common-Mode Rejection Ratio	CMRR	0 ≤ V_{CM} ≤ 2.2V, MAX4310/MAX4311/MAX4312 only	73	95		dB
Input Offset Voltage	V_{OS}			±5.0	±20	mV
Input Offset Voltage Drift	TC_{VOS}			±7		µV/°C
Input Offset Voltage Matching				±1		mV
Input Bias Current	I_B	$I_{IN\_}$		7	14	µA
Feedback Bias Current	I_{FB}	I_{FB}, MAX4310/MAX4311/MAX4312 only		7	14	µA
Input Offset Current	I_{OS}	MAX4310/MAX4311/MAX4312 only		0.1	2	µA
Common-Mode Input Resistance	R_{IN}	V_{IN} varied over V_{CM}, MAX4310/MAX4311/MAX4312 only		3		MΩ
Differential Input Resistance	R_{IN}			70		kΩ
Output Resistance	R_{OUT}	MAX4310/MAX4311/MAX4312 only, Open loop		8		Ω
		MAX4310/MAX4311/MAX4312 only, Closed loop, A_V = +1V/V		0.025		
		MAX4313/MAX4314/MAX4315		0.025		
Disabled Output Resistance	R_{OUT}	MAX4310/MAX4311/MAX4312, open loop		35		kΩ
		MAX4313/MAX4314/MAX4315		1		
Open-Loop Gain	A_{VOL}	MAX4310/MAX4311/MAX4312, R_L = 150Ω to GND, 0.25V ≤ V_{OUT} ≤ 4.2V	50	59		dB
Voltage Gain	A_{VCL}	MAX4313/MAX4314/MAX4315, R_L = 150Ω to GND, 0.25V ≤ V_{OUT} ≤ 4.2V	1.9	2.0	2.1	V/V

DC ELECTRICAL CHARACTERISTICS (continued)

(V_{CC} = +5V, V_{EE} = 0, $\overline{SHDN}$ ≥ 4V, R_L = ∞, V_{OUT} = 2.5V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Output Voltage Swing	V_{OUT}	R_L = 150Ω	V_{CC} - V_{OH}		0.73	0.9	V
			V_{OL} - V_{EE}		0.03	0.06	
		R_L = 10kΩ	V_{CC} - V_{OH}		0.25	0.4	
			V_{OL} - V_{EE}		0.04	0.07	
Output Current	I_{OUT}	R_L = 30Ω		±75	±95		mA
Power-Supply Rejection Ratio	PSRR	V_{CC} = 4.0V to 10.5V		52	63		dB
Quiescent Supply Current	I_{CC}	MAX4310/MAX4313			6.1	7.8	mA
		MAX4311/MAX4314			6.9	8.8	
		MAX4312/MAX4315			7.4	9.4	
Shutdown Supply Current		$\overline{SHDN}$ ≤ V_{IL}			560	750	µA
LOGIC CHARACTERISTICS ($\overline{SHDN}$, A0, A1, A2)							
Logic-Low Threshold	V_{IL}					V_{EE} + 1	V
Logic-High Threshold	V_{IH}			V_{CC} - 1			V
Logic-Low Input Current	I_{IL}	V_{IL} ≤ V_{EE} + 1V		-500	-320		µA
Logic-High Input Current	I_{IH}	V_{IH} ≥ V_{CC} - 1V			0.3	5	µA

AC ELECTRICAL CHARACTERISTICS

(V_{CC} = +5V; V_{EE} = 0; $\overline{SHDN}$ ≥ 4V; R_L = 150Ω; V_{CM} = 1.5V; A_{VCL} = +1V/V (MAX4310/MAX4311/MAX4312), A_{VCL} = +2V/V (MAX4313/MAX4314/MAX4315); T_A = +25°C; unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
-3dB Bandwidth	$BW_{(-3dB)}$	V_{OUT} = 100mVp-p	MAX4310		280		MHz
			MAX4311		345		
			MAX4312		265		
			MAX4313		150		
			MAX4314		127		
			MAX4315		97		
-0.1dB Bandwidth	$BW_{(-0.1dB)}$	V_{OUT} = 100mVp-p	MAX4310		60		MHz
			MAX4311		40		
			MAX4312		35		
			MAX4313		40		
			MAX4314		78		
			MAX4315		46		

AC ELECTRICAL CHARACTERISTICS (continued)

(V_{CC} = +5V; V_{EE} = 0; $\overline{SHDN}$ ≥ 4V; R_L = 150Ω; V_{CM} = 1.5V; A_{VCL} = +1V/V (MAX4310/MAX4311/MAX4312), A_{VCL} = +2V/V (MAX4313/MAX4314/MAX4315); T_A = +25°C; unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	CONDITIONS		MIN	TYP	MAX	UNIT
Full-Power Bandwidth	FPBW	V_{OUT} = 2Vp-p	MAX4310			110		MHz
			MAX4311			100		
			MAX4312			80		
			MAX4313			40		
			MAX4314			90		
			MAX4315			70		
Slew Rate	SR	V_{OUT} = 2Vp-p	MAX4310			460		V/µs
			MAX4311			430		
			MAX4312			345		
			MAX4313			540		
			MAX4314			430		
			MAX4315			310		
Settling Time to 0.1%	t_S	V_{OUT} = 2V step	MAX4310/MAX4311/MAX4312			42		ns
			MAX4313/MAX4314/MAX4315			25		
Gain Matching		Matching between channels over -3dB bandwidth				0.05		dB
Differential Gain Error	DG	A_{VCL} = +1V/V, R_L = 150Ω to V_{CC}/2	MAX4310/MAX4311/MAX4312			0.06		%
		R_L = 150Ω to V_{CC}/2	MAX4313/MAX4314/MAX4315			0.09		
Differential Phase Error	DG	A_{VCL} = +1V/V, R_L = 150Ω to V_{CC}/2	MAX4310/MAX4311/MAX4312			0.08		degrees
		R_L = 150Ω to V_{CC}/2	MAX4313/MAX4314/MAX4315			0.03		
Spurious-Free Dynamic Range	SFDR	V_{OUT} = 2Vp-p	MAX4310/ MAX4311/ MAX4312	f = 3kHz		-89		dBc
				f = 2MHz		-80		
				f = 20MHz		-47		
			MAX4313/ MAX4314/ MAX4315	f = 3kHz		-95		
				f = 2MHz		-72		
				f = 20MHz		-47		
Second Harmonic Distortion		f = 1MHz, V_{OUT} = 2Vp-p	MAX4310/MAX4311/MAX4312			-85		dBc
			MAX4313/MAX4314/MAX4315			-76		
Third Harmonic Distortion		f = 1MHz, V_{OUT} = 2Vp-p	MAX4310/MAX4311/MAX4312			-88		dBc
			MAX4313/MAX4314/MAX4315			-95		
Total Harmonic Distortion	THD	f = 1MHz, V_{OUT} = 2Vp-p	MAX4310/MAX4311/MAX4312			-83		dB
			MAX4313/MAX4314/MAX4315			-76		

AC ELECTRICAL CHARACTERISTICS (continued)

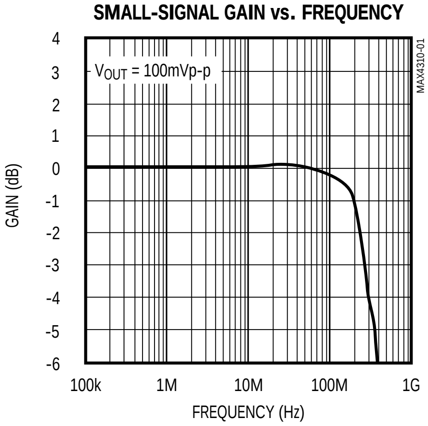
(V_{CC} = +5V; V_{EE} = 0; $\overline{SHDN}$ ≥ 4V; R_L = 150Ω; V_{CM} = 1.5V; A_{VCL} = +1V/V (MAX4310/MAX4311/MAX4312), A_{VCL} = +2V/V (MAX4313/MAX4314/MAX4315); T_A = +25°C; unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
All-Hostile Crossstalk		f = 10MHz, V_{IN} = 2Vp-p	MAX4310/MAX4313		-95		dB
			MAX4311/MAX4314		-60		
			MAX4312/MAX4315		-52		
Off-Isolation		$\overline{SHDN}$ = 0, f = 10MHz, V_{IN} = 2Vp-p			-82		dB
Output Impedance	Z_{OUT}	f = 10MHz			3		Ω
Input Capacitance	C_{IN}	Channel on or off			2		pF
Input Voltage Noise Density	e_n	f = 10kHz			14		nV/√Hz
Input Current Noise Density	i_n	f = 10kHz			1.3		pA/√Hz
SWITCHING CHARACTERISTICS							
Channel Switching Time	t_{SW}				40		ns
Enable Time from Shutdown	t_{ON}				50		ns
Disable Time to Shutdown	t_{OFF}				120		ns
Switching Transient					10		mVp-p

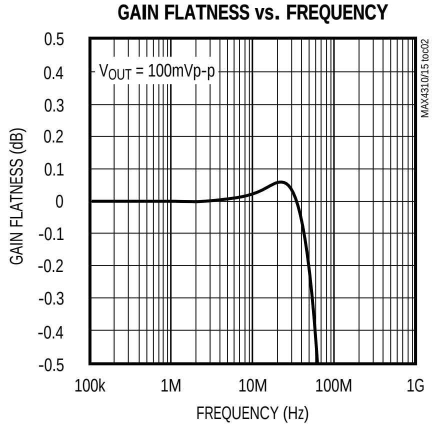
Typical Operating Characteristics

(V_{CC} = +5V; V_{EE} = 0; $\overline{SHDN}$ ≥ 4V; R_L = 150Ω to V_{CC}/2; V_{CM} = 1.5V; A_{VCL} = +1V/V (MAX4310/MAX4311/MAX4312), A_{VCL} = +2V/V (MAX4313/MAX4314/MAX4315); T_A = +25°C; unless otherwise noted.)

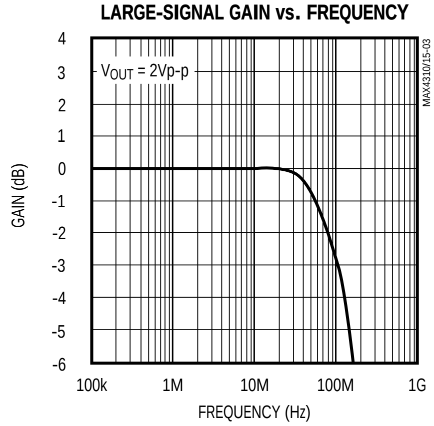
MAX4310
SMALL-SIGNAL GAIN vs. FREQUENCY



MAX4310
GAIN FLATNESS vs. FREQUENCY



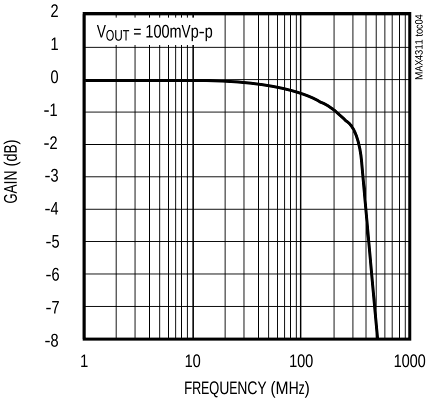
MAX4310
LARGE-SIGNAL GAIN vs. FREQUENCY



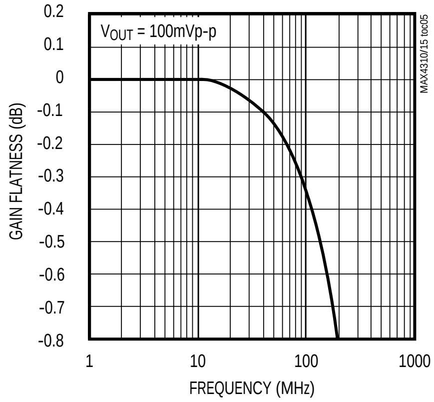
Typical Operating Characteristics (continued)

(V_{CC} = +5V; V_{EE} = 0; $\overline{SHDN}$ ≥ 4V; R_L = 150Ω to V_{CC}/2; V_{CM} = 1.5V; A_{VCL} = +1V/V (MAX4310/MAX4311/MAX4312), A_{VCL} = +2V/V (MAX4313/MAX4314/MAX4315); T_A = +25°C; unless otherwise noted.)

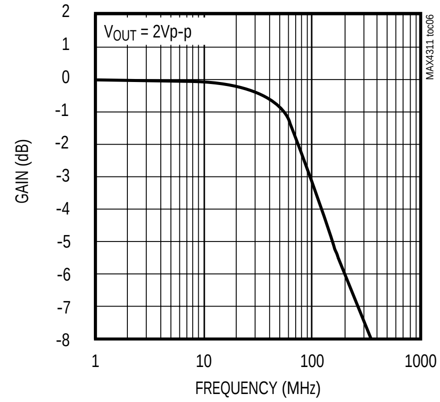
MAX4311
SMALL-SIGNAL GAIN vs. FREQUENCY



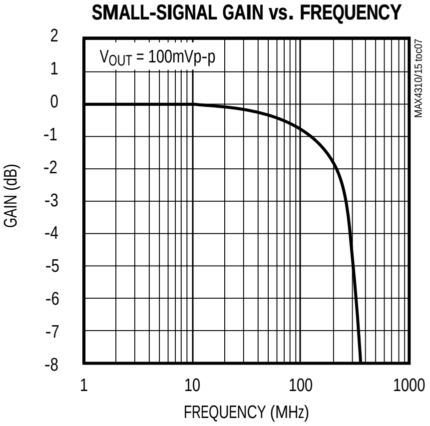
MAX4311
GAIN FLATNESS vs. FREQUENCY



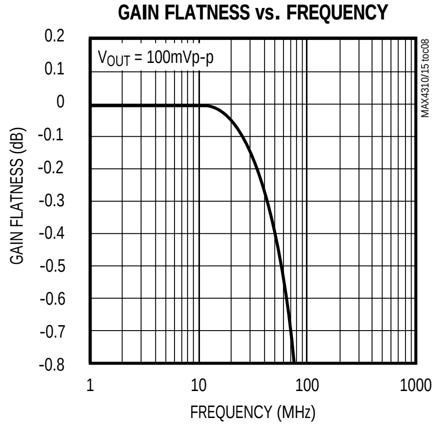
MAX4311
LARGE-SIGNAL GAIN vs. FREQUENCY



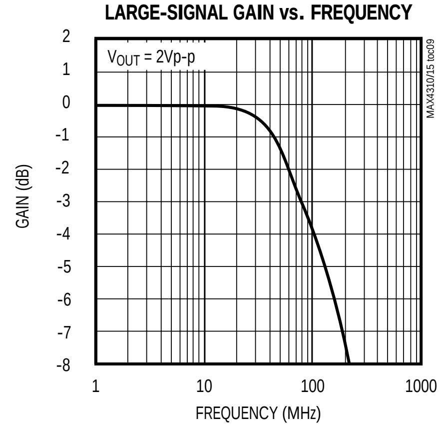
MAX4312
SMALL-SIGNAL GAIN vs. FREQUENCY



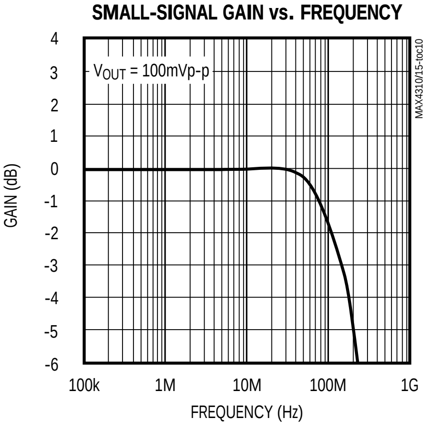
MAX4312
GAIN FLATNESS vs. FREQUENCY



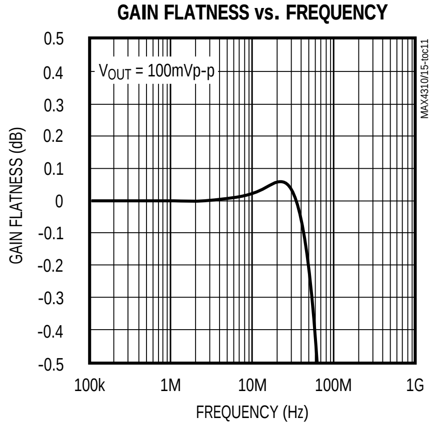
MAX4312
LARGE-SIGNAL GAIN vs. FREQUENCY



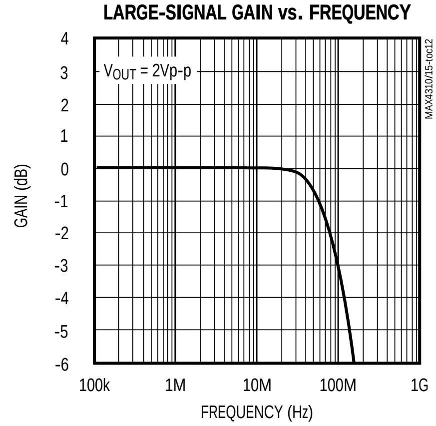
MAX4313
SMALL-SIGNAL GAIN vs. FREQUENCY



MAX4313
GAIN FLATNESS vs. FREQUENCY



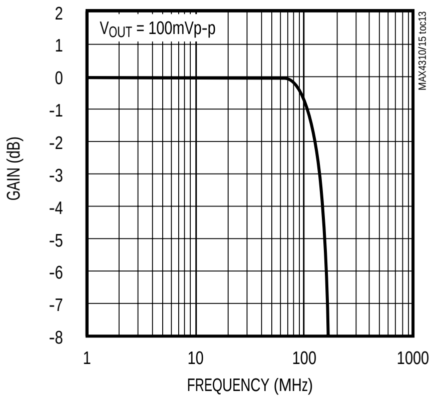
MAX4313
LARGE-SIGNAL GAIN vs. FREQUENCY



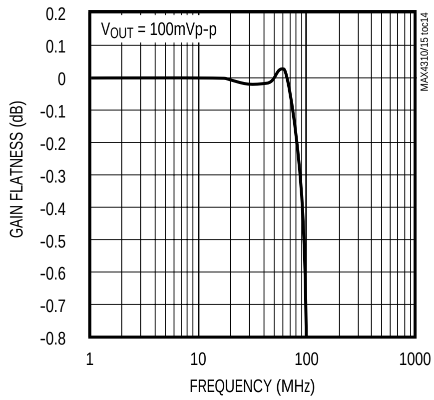
Typical Operating Characteristics (continued)

(V_{CC} = +5V; V_{EE} = 0; $\overline{SHDN}$ ≥ 4V; R_L = 150Ω to V_{CC}/2; V_{CM} = 1.5V; A_{VCL} = +1V/V (MAX4310/MAX4311/MAX4312), A_{VCL} = +2V/V (MAX4313/MAX4314/MAX4315); T_A = +25°C; unless otherwise noted.)

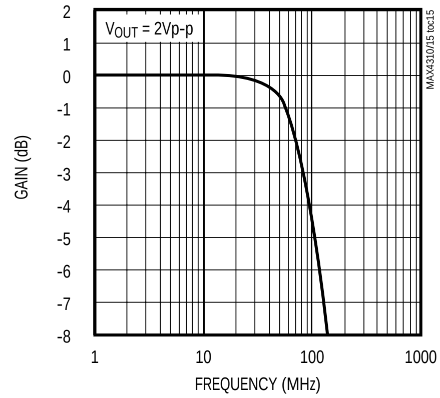
MAX4314
SMALL-SIGNAL GAIN vs. FREQUENCY



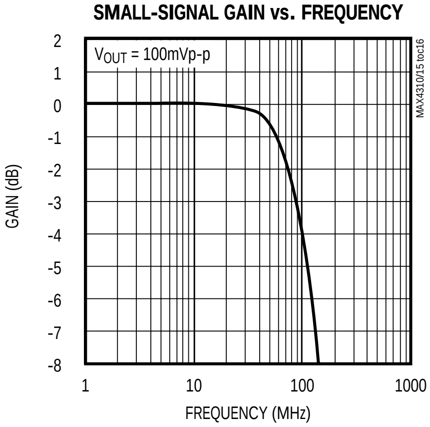
MAX4314
GAIN FLATNESS vs. FREQUENCY



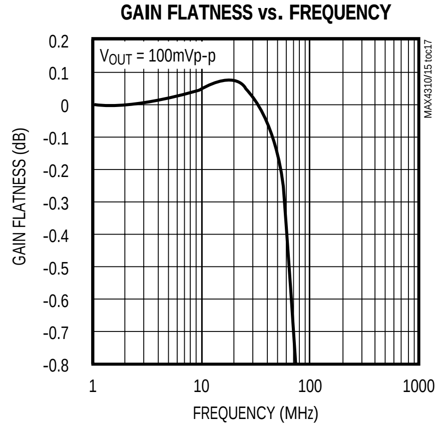
MAX4314
LARGE-SIGNAL GAIN vs. FREQUENCY



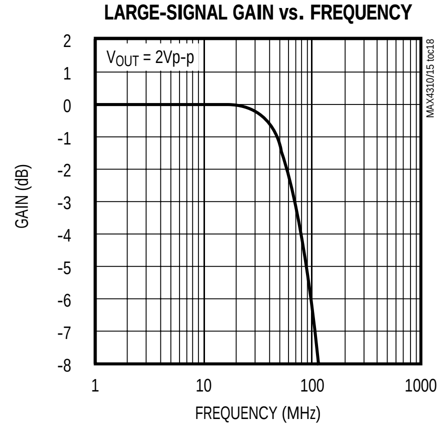
MAX4315
SMALL-SIGNAL GAIN vs. FREQUENCY



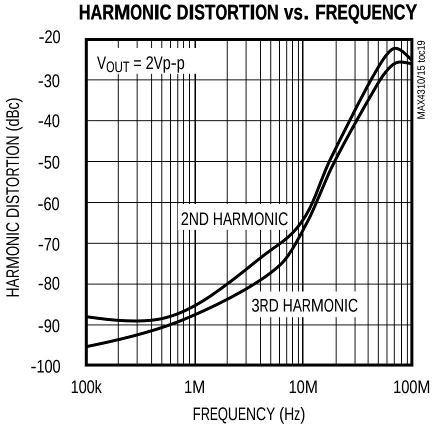
MAX4315
GAIN FLATNESS vs. FREQUENCY



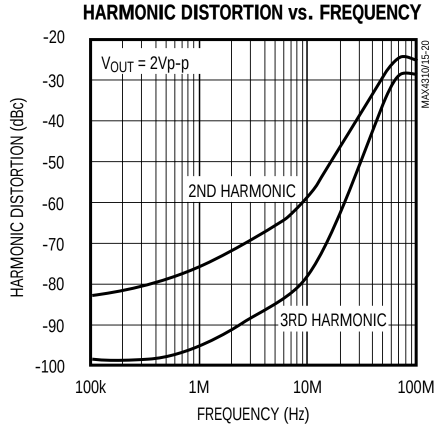
MAX4315
LARGE-SIGNAL GAIN vs. FREQUENCY



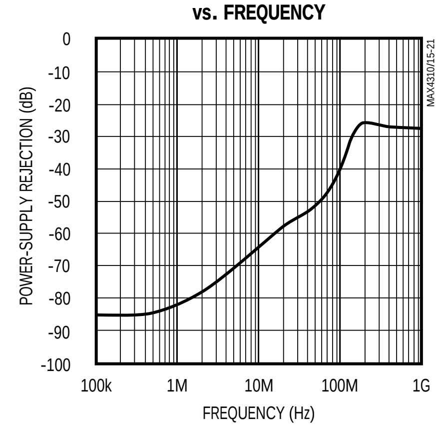
MAX4310/MAX4311/MAX4312
HARMONIC DISTORTION vs. FREQUENCY



MAX4313/MAX4314/MAX4315
HARMONIC DISTORTION vs. FREQUENCY



POWER-SUPPLY REJECTION
vs. FREQUENCY

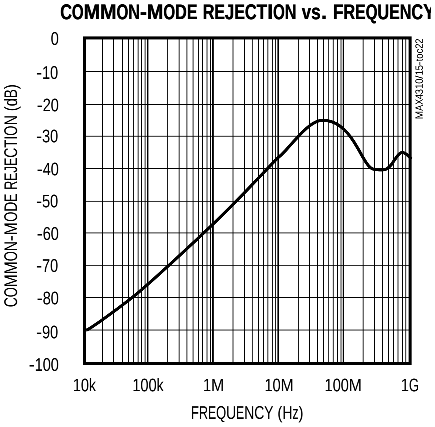


High-Speed, Low-Power, Single-Supply, Multichannel, Video Multiplexer-Amplifiers

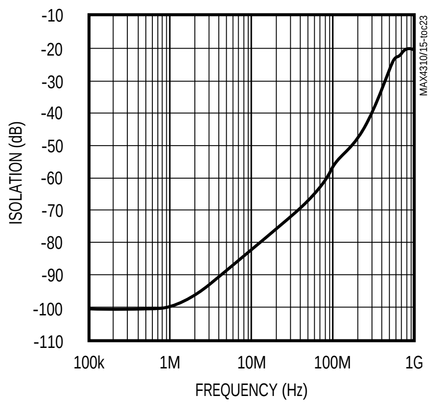
Typical Operating Characteristics (continued)

(V_{CC} = +5V; V_{EE} = 0; $\overline{SHDN}$ ≥ 4V; R_L = 150Ω to V_{CC}/2; V_{CM} = 1.5V; A_{VCL} = +1V/V (MAX4310/MAX4311/MAX4312), A_{VCL} = +2V/V (MAX4313/MAX4314/MAX4315); T_A = +25°C; unless otherwise noted.)

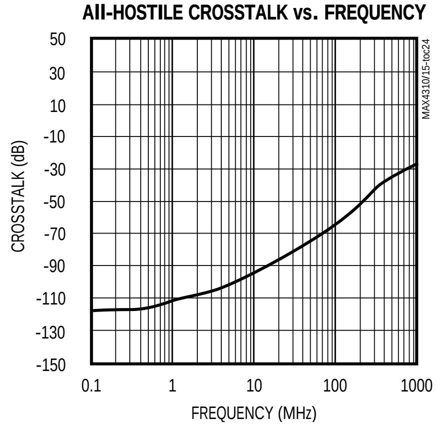
MAX4310/MAX4311/MAX4312
COMMON-MODE REJECTION vs. FREQUENCY



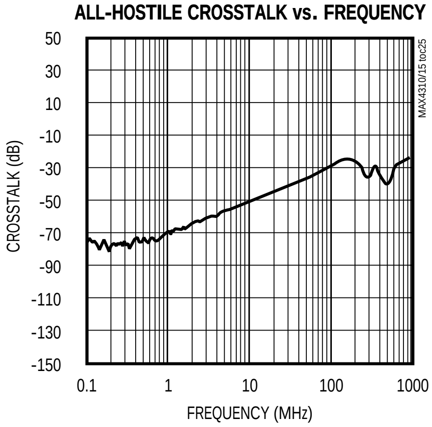
OFF-ISOLATION vs. FREQUENCY



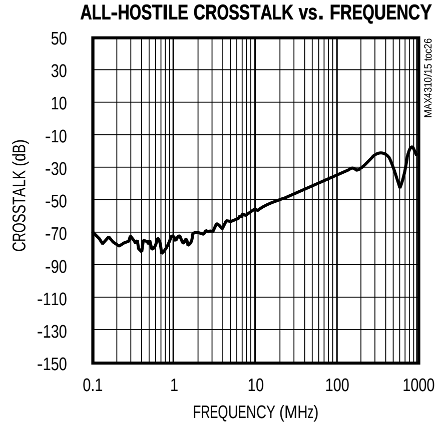
MAX4310/MAX4313
All-HOSTILE CROSSTALK vs. FREQUENCY



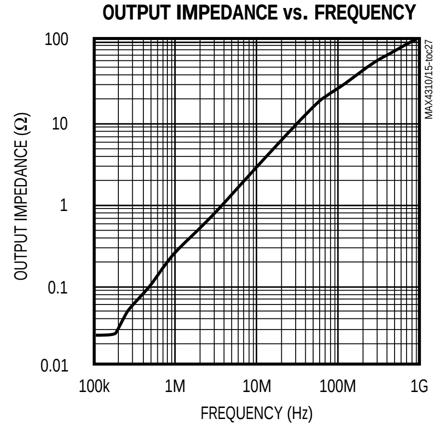
MAX4312/MAX4315
ALL-HOSTILE CROSSTALK vs. FREQUENCY



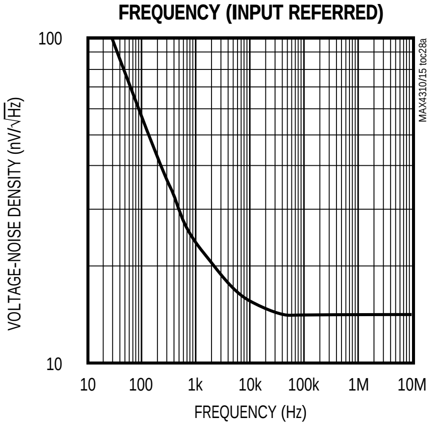
MAX4311/MAX4314
ALL-HOSTILE CROSSTALK vs. FREQUENCY



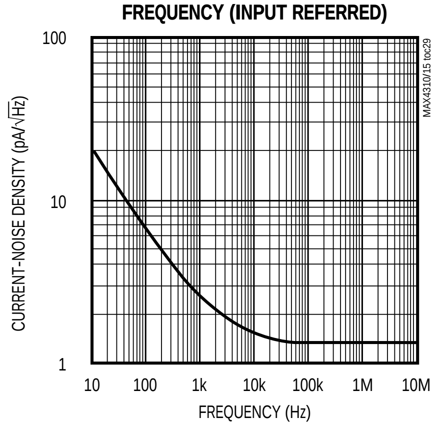
OUTPUT IMPEDANCE vs. FREQUENCY



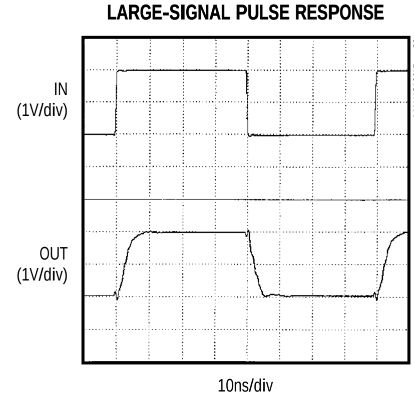
VOLTAGE-NOISE DENSITY vs.
FREQUENCY (INPUT REFERRED)



CURRENT-NOISE DENSITY vs.
FREQUENCY (INPUT REFERRED)



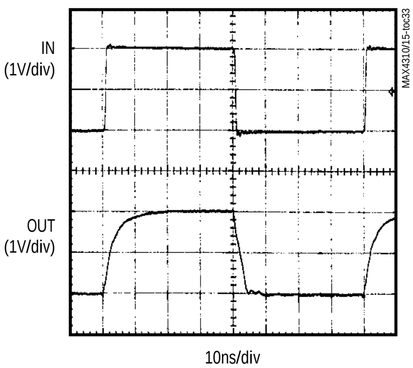
MAX4310
LARGE-SIGNAL PULSE RESPONSE



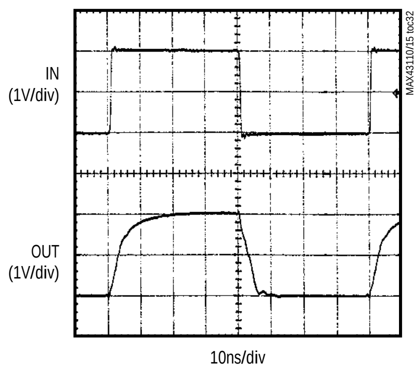
Typical Operating Characteristics (continued)

(V_{CC} = +5V; V_{EE} = 0; $\overline{SHDN}$ ≥ 4V; R_L = 150Ω to V_{CC}/2; V_{CM} = 1.5V; A_{VCL} = +1V/V (MAX4310/MAX4311/MAX4312), A_{VCL} = +2V/V (MAX4313/MAX4314/MAX4315); T_A = +25°C; unless otherwise noted.)

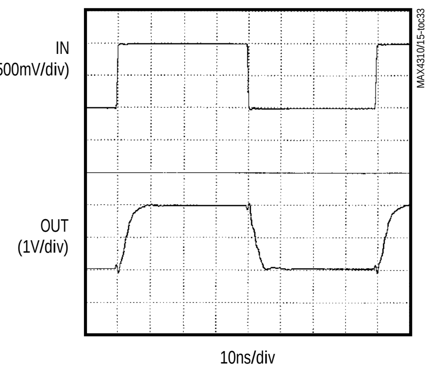
MAX4311
LARGE-SIGNAL PULSE RESPONSE



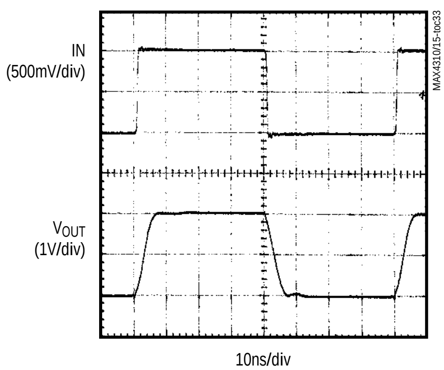
MAX4312
LARGE-SIGNAL PULSE RESPONSE



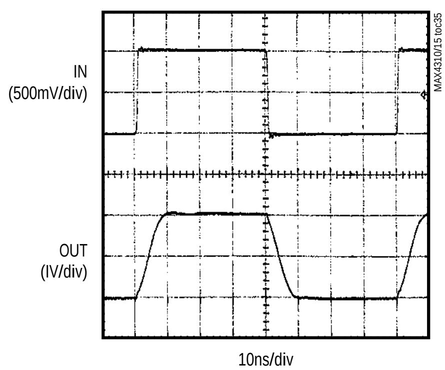
MAX4313
LARGE-SIGNAL PULSE RESPONSE



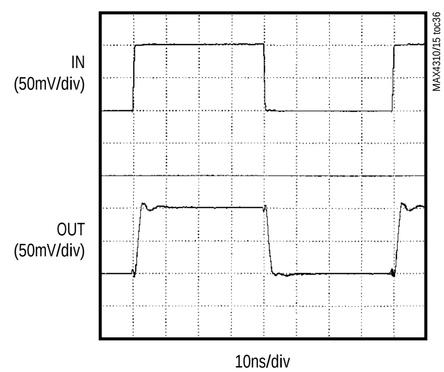
MAX4314
LARGE-SIGNAL PULSE RESPONSE



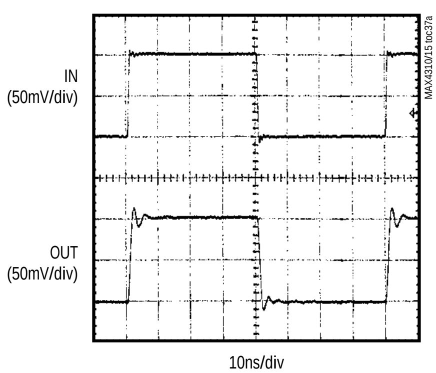
MAX4315
LARGE-SIGNAL PULSE RESPONSE



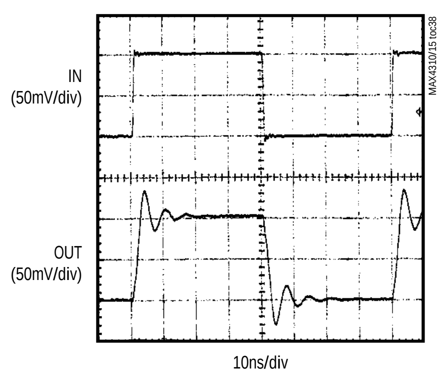
MAX4310
SMALL-SIGNAL PULSE RESPONSE



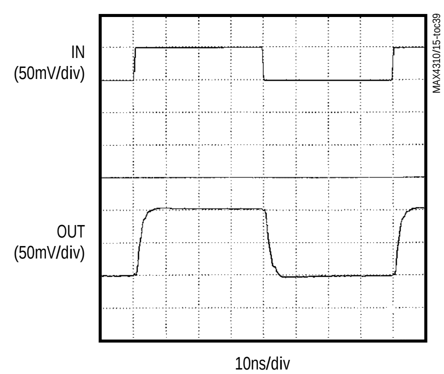
MAX4311
SMALL-SIGNAL PULSE RESPONSE



MAX4312
SMALL-SIGNAL PULSE RESPONSE



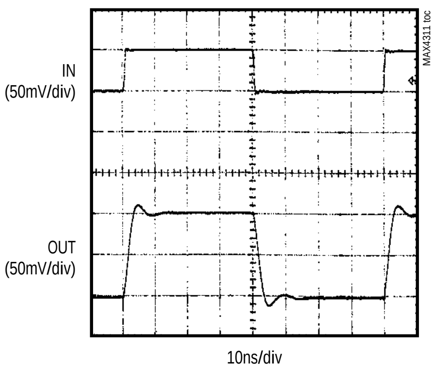
MAX4313
SMALL-SIGNAL PULSE RESPONSE



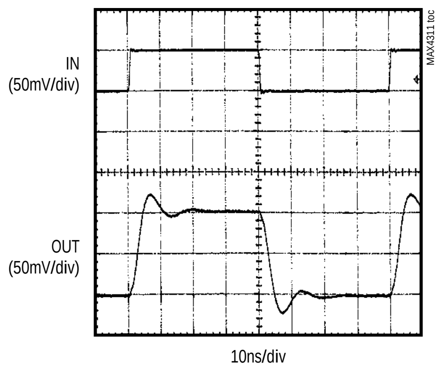
Typical Operating Characteristics (continued)

(V_{CC} = +5V; V_{EE} = 0; $\overline{SHDN}$ ≥ 4V; R_L = 150Ω to $V_{CC}/2$; V_{CM} = 1.5V; A_{VCL} = +1V/V (MAX4310/MAX4311/MAX4312), A_{VCL} = +2V/V (MAX4313/MAX4314/MAX4315); T_A = +25°C; unless otherwise noted.)

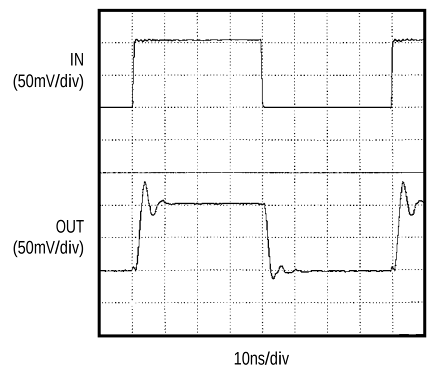
MAX4314
SMALL-SIGNAL PULSE RESPONSE



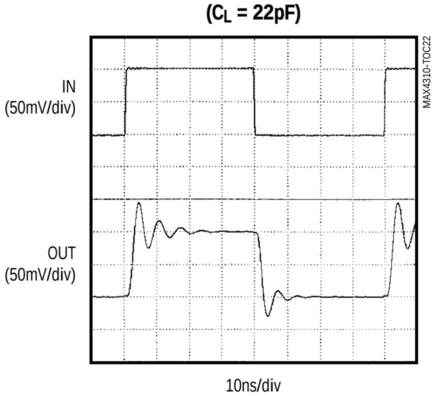
MAX4315
SMALL-SIGNAL PULSE RESPONSE



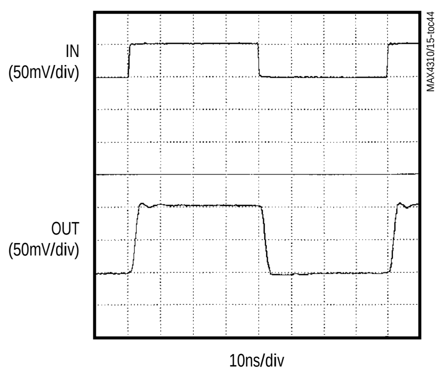
MAX4310
SMALL-SIGNAL PULSE RESPONSE
(C_L = 10pF)



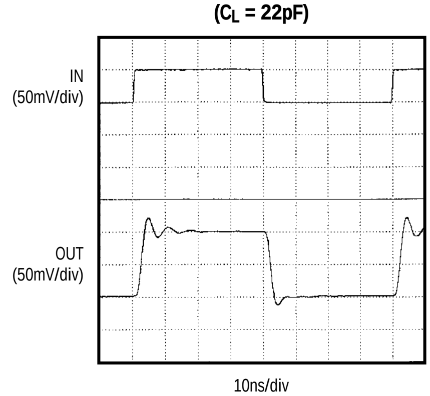
MAX4310
SMALL-SIGNAL PULSE RESPONSE
(C_L = 22pF)



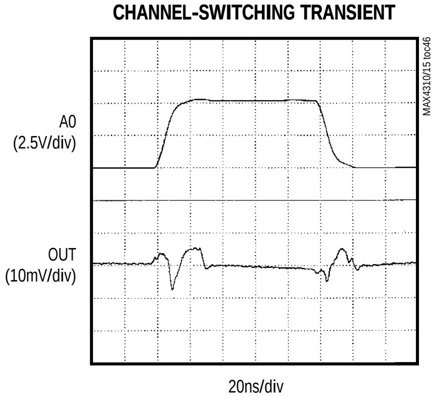
MAX4313
SMALL-SIGNAL PULSE RESPONSE
(C_L = 10pF)



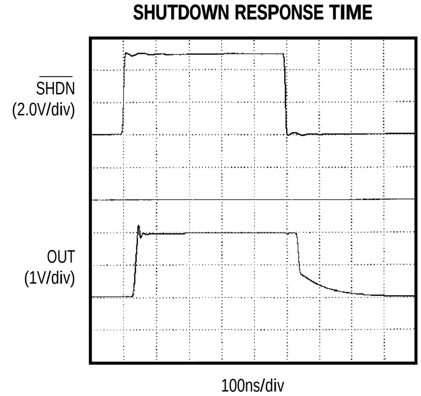
MAX4313
SMALL-SIGNAL PULSE RESPONSE
(C_L = 22pF)



CHANNEL-SWITCHING TRANSIENT



SHUTDOWN RESPONSE TIME



Pin Description

PIN								NAME	FUNCTION
MAX4310 SO/ µMAX	MAX4311		MAX4312 SO/ QSOP	MAX4313 SO/ µMAX	MAX4314		MAX4315 SO/ QSOP		
	SO	QSOP			SO	QSOP			
1	2	2	3	1	2	2	3	A0	Channel Address Logic Input 0
—	1	1	2	—	1	1	2	A1	Channel Address Logic Input 1
—	—	—	1	—	—	—	1	A2	Channel Address Logic Input 2
2	12	14	14	2	12	14	14	$\overline{SHDN}$	Shutdown Input
3	4	4	4	3	4	4	4	V_{CC}	Positive Power Supply
4	5	5	5	4	5	5	5	IN0	Amplifier Input 0
5	7	7	6	5	7	7	6	IN1	Amplifier Input 1
—	8	10	7	—	8	10	7	IN2	Amplifier Input 2
—	10	12	8	—	10	12	8	IN3	Amplifier Input 3
—	—	—	9	—	—	—	9	IN4	Amplifier Input 4
—	—	—	10	—	—	—	10	IN5	Amplifier Input 5
—	—	—	11	—	—	—	11	IN6	Amplifier Input 6
—	—	—	12	—	—	—	12	IN7	Amplifier Input 7
6	11	13	13	6	11	13	13	V_{EE}	Negative Power Supply. Ground for single-supply operation.
7	13	15	15	—	—	—	—	FB	Amplifier Feedback Input
—	—	—	—	7	13	15	15	GND	Ground
8	14	16	16	8	14	16	16	OUT	Amplifier Output
—	3, 6, 9	3, 6, 8, 9, 11	—	—	3, 6, 9	3, 6, 8, 9, 11	—	N.C.	Not connected. Tie to ground plane for optimal performance.

Detailed Description

The MAX4310/MAX4311/MAX4312 combine 2-channel, 4-channel, or 8-channel multiplexers, respectively, with an adjustable-gain output amplifier optimized for closed-loop gains of +1V/V (0dB) or greater. The MAX4313/MAX4314/MAX4315 combine 2-channel, 4-channel, or 8-channel multiplexers, respectively, with a +2V/V (6dB) fixed-gain amplifier, optimized for driving back-terminated cables. These devices operate from a single supply voltage of +4V to +10.5V, or from dual supplies of ±2V to ±5.25V. The outputs may be placed in a high-impedance state and the supply current minimized by forcing the $\overline{SHDN}$ pin low. The input multiplexers feature short 40ns channel-switching times and small 10mVp-p switching transients. The input capacitance remains constant at 1pF whether the channel is on or off, providing a predictable input impedance to the signal source. These devices feature single-supply, rail-to-rail, voltage-feedback output amplifiers that achieve up to 540V/µs slew rates and up to 345MHz -3dB bandwidths. These devices also feature excellent harmonic distortion and differential gain/phase performance.

Applications Information

Rail-to-Rail Outputs, Ground-Sensing Input

The input common-mode range extends from the negative supply rail to V_{CC} - 2.7V with excellent common-mode rejection. Beyond this range, multiplexer switching times may increase and the amplifier output is a nonlinear function of the input, but does not undergo phase reversal or latchup.

The output swings to within 250mV of V_{CC} and 40mV of V_{EE} with a 10kΩ load. With a 150Ω load to ground, the output swings from 30mV above V_{EE} to within 730mV of the supply rail. Local feedback around the output stage ensures low open-loop output impedance to reduce gain sensitivity to load variations. This feedback also produces demand-driven bias current to the output transistors for ±95mA drive capability while constraining total supply current to only 6.1mA.

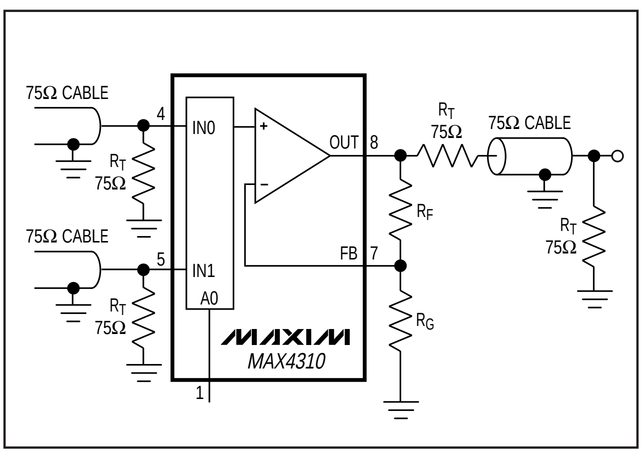


Figure 1. MAX4310 Noninverting Gain Configuration

Feedback and Gain Resistor Selection (MAX4310/MAX4311/MAX4312)

Select the MAX4310/MAX4311/MAX4312 gain-setting feedback (R_F) and input (R_G) resistors to fit your application. Large resistor values increase voltage noise and interact with the amplifier's input and PC board capacitance. This can generate undesirable poles and zeros, and can decrease bandwidth or cause oscillations. For example, a noninverting gain of +2V/V configuration (R_F = R_G) using 1kΩ resistors, combined with 2pF of input capacitance and 1pF of PC board capacitance, causes a pole at 159MHz. Since this pole is within the amplifier bandwidth, it jeopardizes stability. Reducing the 1kΩ resistors to 100Ω extends the pole frequency to 1.59GHz, but could limit output swing by adding 200Ω in parallel with the amplifier's load resistor.

Table 1 shows suggested R_F and R_G values for the MAX4310/MAX4311/MAX4312 when operating in the noninverting configuration (shown in Figure 1). These values provide optimal AC response using surface-mount resistors and good layout techniques, as discussed in the *Layout and Power-Supply Bypassing* section.

Stray capacitance at the FB pin causes feedback resistor decoupling and produces peaking in the frequency-response curve. Keep the capacitance at FB as low as possible by using surface-mount resistors and by avoiding the use of a ground plane beneath or beside these resistors and the FB pin. Some capacitance is unavoidable; if necessary, its effects can be neutralized by adjusting R_F. Use 1% resistors to maintain consistency over a wide range of production lots.

Table 1. Bandwidth and Gain with Suggested Gain-Setting Resistors (MAX4310/MAX4311/MAX4312)

GAIN (V/V)	GAIN (dB)	R_F (Ω)	R_G (Ω)	-3dB BW (MHz)	0.1dB BW (MHz)
1	0	0	∞	280	60
2	6	500	500	80	30
5	14	500	120	20	4
10	20	500	56	10	2

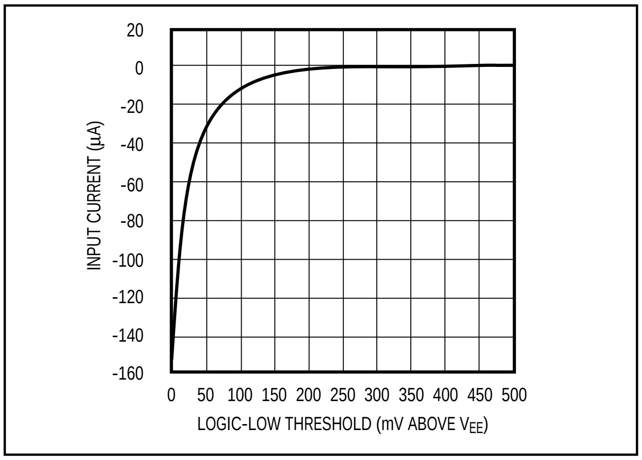


Figure 2. Logic-Low Input Current vs. V_{IL} ($\overline{SHDN}$, A0, A1, A2)

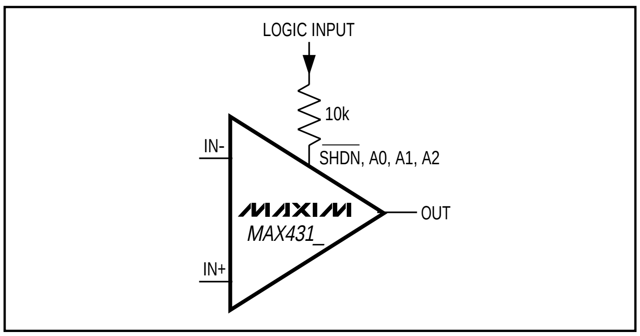


Figure 3. Circuit to Reduce Logic-Low Input Current

Low-Power Shutdown Mode

All parts feature a low-power shutdown mode that is activated by driving the $\overline{SHDN}$ input low. Placing the amplifier in shutdown mode reduces the quiescent supply current to 560µA and places the output into a high-impedance state, typically 35kΩ. By tying the outputs of several devices together and disabling all but one of the paralleled amplifiers' outputs, multiple devices may be paralleled to construct larger switch matrices.

For MAX4310/MAX4311/MAX4312 application circuits operating with a closed-loop gain of +2V/V or greater, consider the external-feedback network impedance of **all** devices used in the mux application when calculating the total load on the output amplifier of the active device. The MAX4313/MAX4314/MAX4315 have a fixed gain of +2V/V that is internally set with two 500Ω thin-film resistors. The impedance of the internal feedback resistors must be taken into account when operating multiple MAX4313/MAX4314/MAX4315s in large multiplexer applications.

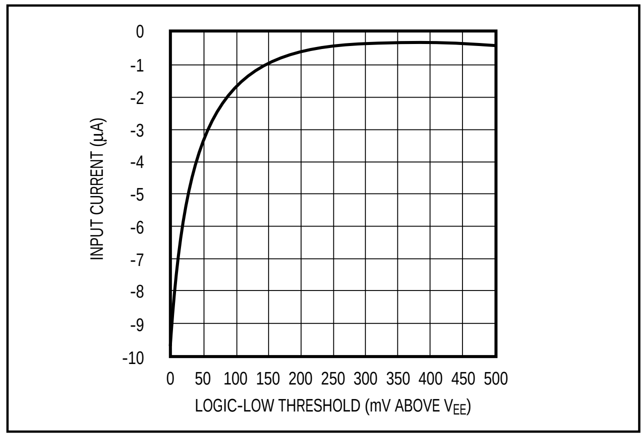


Figure 4. Logic-Low Input Current vs. V_{IL} with 10kΩ Series Resistor

For normal operation, drive $\overline{SHDN}$ high. If the shutdown function is not used, connect $\overline{SHDN}$ to V_{CC}.

Layout and Power-Supply Bypassing

The MAX4310–MAX4315 have very high bandwidths and consequently require careful board layout, including the possible use of constant-impedance microstrip or stripline techniques.

To realize the full AC performance of these high-speed amplifiers, pay careful attention to power-supply bypassing and board layout. The PC board should have at least two layers: a signal and power layer on one side, and a large, low-impedance ground plane on the other side. The ground plane should be as free of voids as possible, with one exception: the feedback (FB) should have as low a capacitance to ground as possible. Therefore, layers that do not incorporate a signal or power trace should not have a ground plane.

Whether or not a constant-impedance board is used, it is best to observe the following guidelines when designing the board:

1) Do not use wire-wrapped boards (they are too inductive) or breadboards (they are too capacitive).
2) Do not use IC sockets; they increase parasitic capacitance and inductance.
3) Keep signal lines as short and straight as possible. Do not make 90° turns; round all corners.
4) Observe high-frequency bypassing techniques to maintain the amplifier's accuracy and stability.
5) Use surface-mount components. They generally have shorter bodies and lower parasitic reactance, yielding better high-frequency performance than through-hole components.

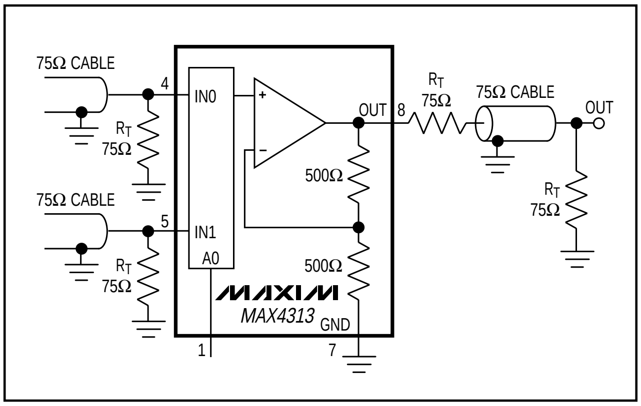


Figure 5. Video Line Driver

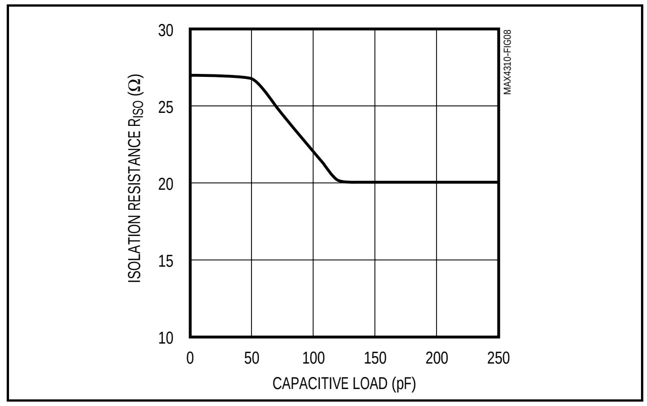


Figure 8. Optimal Isolation Resistance vs. Capacitive Load

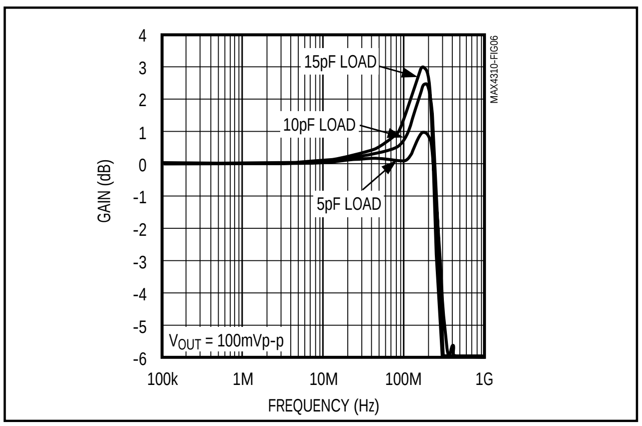


Figure 6. Small-Signal Gain vs. Frequency with Capacitive Load and No Isolation Resistor

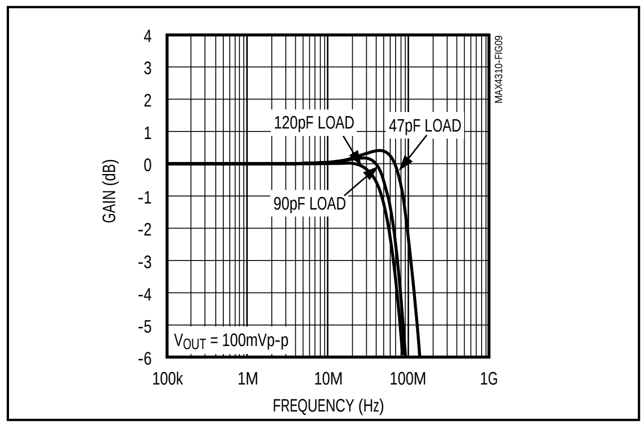


Figure 9. Small-Signal Gain vs. Frequency with Load Capacitance and 27Ω Isolation Resistor

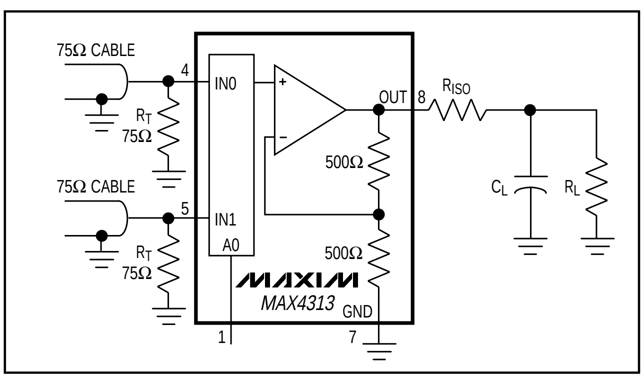


Figure 7. Using an Isolation Resistor (R_{ISO}) for High Capacitive Loads

The bypass capacitors should include a 100nF, ceramic surface-mount capacitor between each supply pin and the ground plane, located as close to the package as possible. Optionally, place a 10µF tantalum capacitor at the power-supply pins' point of entry to the PC board to ensure the integrity of incoming supplies. The power-supply trace should lead directly from the tantalum capacitor to the V_{CC} and V_{EE} pins. To minimize parasitic inductance, keep PC traces short and use surface-mount components.

If input termination resistors and output back-termination resistors are used, they should be surface-mount types, and should be placed as close to the IC pins as possible.

Video Line Driver

The MAX4310–MAX4315 are well-suited to drive coaxial transmission lines when the cable is terminated at both ends, as shown in Figure 5. Cable frequency response can cause variations in the signal's flatness.

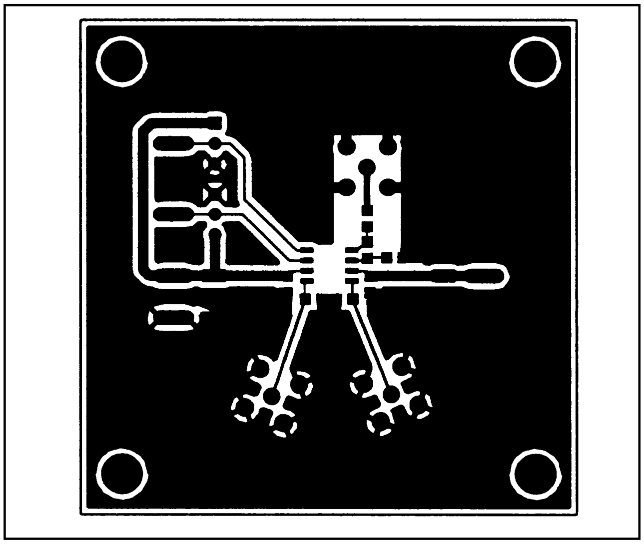


Figure 10. High-Speed EV Board Layout—Component Side

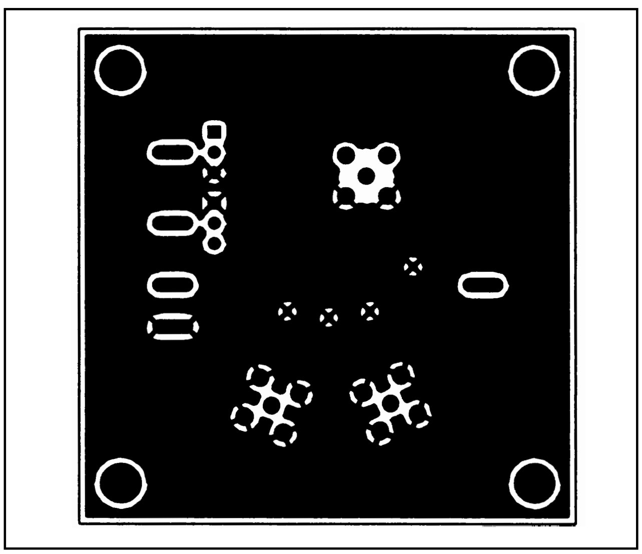


Figure 11. High-Speed EV Board Layout—Solder Side

Driving Capacitive Loads

A correctly terminated transmission line is purely resistive and presents no capacitive load to the amplifier. Reactive loads decrease phase margin and may produce excessive ringing and oscillation (see *Typical Operating Characteristics*).

Another concern when driving capacitive loads originates from the amplifier's output impedance, which appears inductive at high frequencies. This inductance forms an L-C resonant circuit with the capacitive load, which causes peaking in the frequency response and degrades the amplifier's phase margin.

Although the MAX4310–MAX4315 are optimized for AC performance and are not designed to drive highly capacitive loads, they are capable of driving up to 20pF without oscillations. However, some peaking may occur in the frequency domain (Figure 6). To drive larger capacitive loads or to reduce ringing, add an isolation resistor between the amplifier's output and the load (Figure 7).

The value of R_{ISO} depends on the circuit's gain and the capacitive load (Figure 8). Figure 9 shows the MAX4310–MAX4315 frequency response with the isolation resistor and a capacitive load. With higher capacitive values, bandwidth is dominated by the RC network formed by R_{ISO} and C_L; the bandwidth of the amplifier itself is much higher. Also note that the isolation resistor forms a divider that decreases the voltage delivered to the load.

Table 2. Input Control Logic

MAX4310/MAX4313				
$\overline{SHDN}$	**A2**	**A1**	**A0**	**CHANNEL SELECTED**
0	—	—	X	None, High-Z Output
1	—	—	0	0
1	—	—	1	1
MAX4311/MAX4314				
$\overline{SHDN}$	**A2**	**A1**	**A0**	**CHANNEL SELECTED**
0	—	X	X	None, High-Z Output
1	—	0	0	0
1	—	0	1	1
1	—	1	0	2
1	—	1	1	3
MAX4312/MAX4315				
$\overline{SHDN}$	**A2**	**A1**	**A0**	**CHANNEL SELECTED**
0	X	X	X	None, High-Z Output
1	0	0	0	0
1	0	0	1	1
1	0	1	0	2
1	0	1	1	3
1	1	0	0	4
1	1	0	1	5
1	1	1	0	6
1	1	1	1	7

High-Speed, Low-Power, Single-Supply, Multichannel, Video Multiplexer-Amplifiers

Digital Interface

The multiplexer architecture of the MAX4310–MAX4315 ensures that no two input channels are ever connected together. Channel selection is accomplished by applying a binary code to channel address inputs. The address decoder selects input channels, as shown in Table 2. All digital inputs are CMOS compatible.

High-Speed Evaluation Board

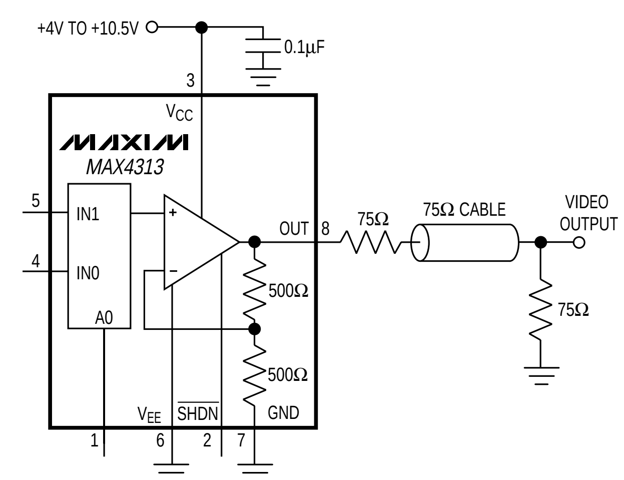
Figures 10 and 11 show the evaluation board and present a suggested layout for the circuits. This board was developed using the techniques described in the *Layout and Power-Supply Bypassing* section of this data sheet. The smallest available surface-mount resistors were used for feedback and back-termination to minimize their distance from the part, reducing the capacitance associated with longer lead lengths. SMA connectors were used for best high-frequency performance. Inputs and outputs do not match a 75Ω line, but this does not affect performance since distances are extremely short. However, in applications that require lead lengths greater than one-quarter of the wavelength of the highest frequency of interest, use constant-impedance traces.

Fully assembled evaluation boards are available for the MAX4313 in an SO package.

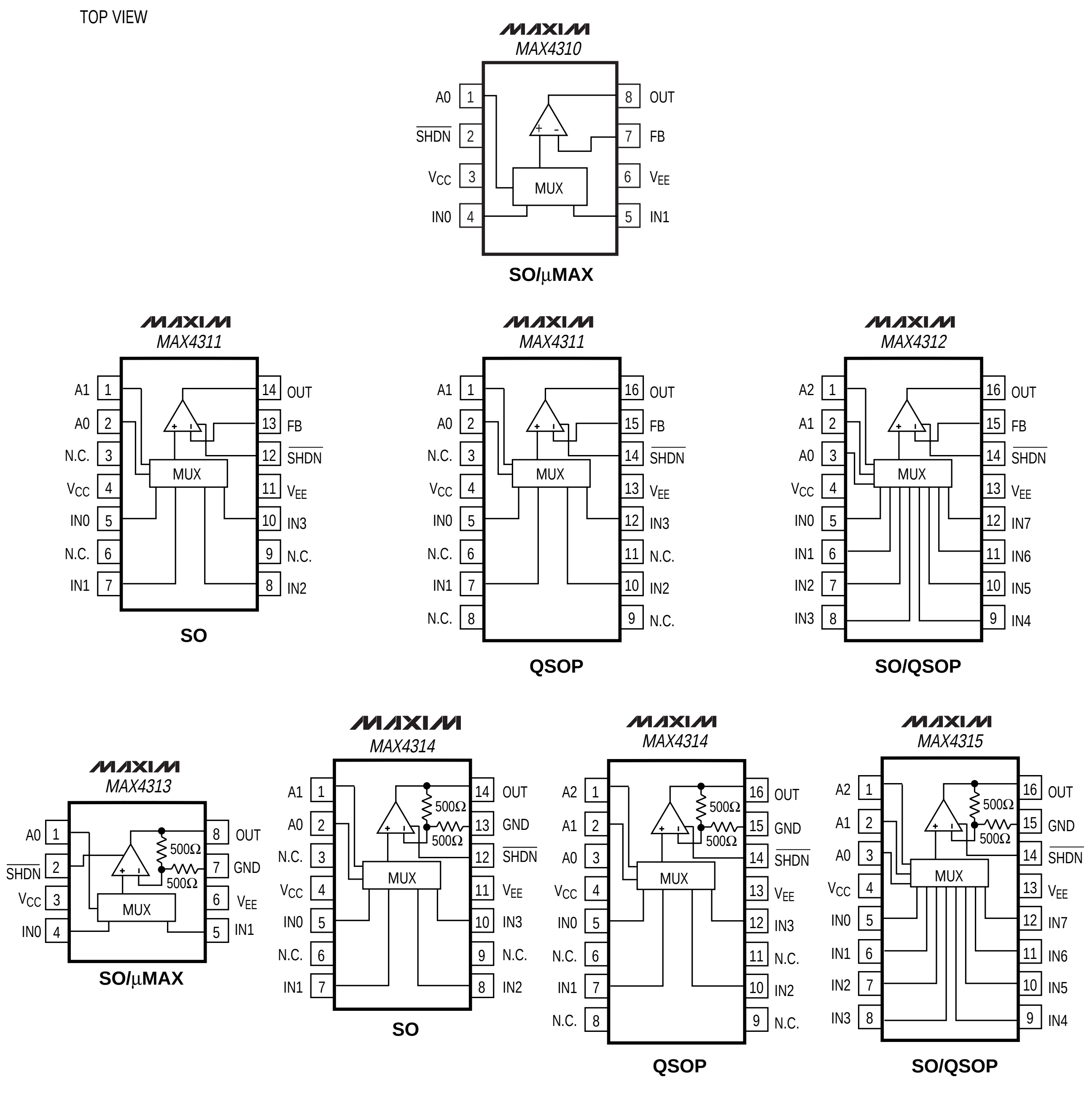
Chip Information

TRANSISTOR COUNT: 156

Typical Operating Circuit



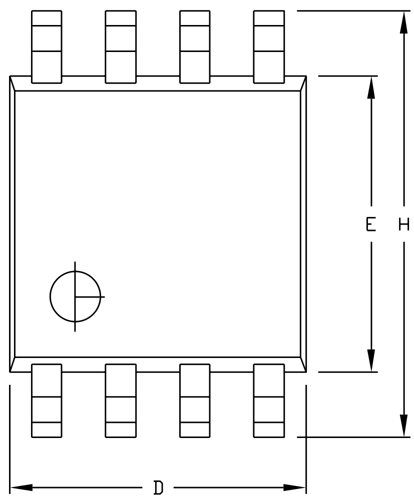
Pin Configurations



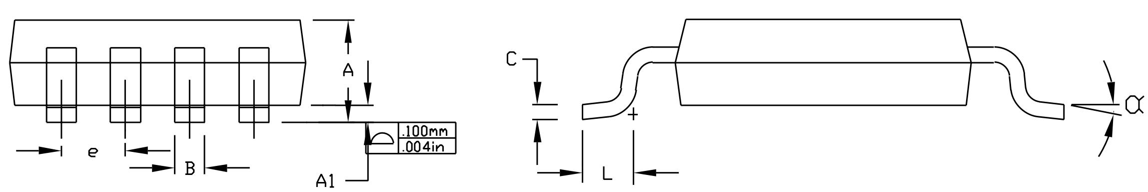
N.C. = NOT INTERNALLY CONNECTED. TIE TO GROUND PLANE FOR OPTIMAL PERFORMANCE.

Package Information

8LUMAXD.EPS



					JEDEC			
	INCHES		MILLIMETERS		INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX
A	0.037	0.043	0.94	1.10	---	0.043	---	1.10
A1	0.002	0.006	0.05	0.15	0.002	0.006	0.05	0.15
B	0.010	0.014	0.25	0.36	0.010	0.016	0.25	0.40
C	0.005	0.007	0.13	0.18	0.005	0.009	0.13	0.23
D	0.116	0.120	2.95	3.05	0.114	0.122	2.9	3.1
e	0.0256 BSC		0.65 BSC		0.0256 BSC		0.64 BSC	
E	0.116	0.120	2.95	3.05	0.114	0.122	2.9	3.1
H	0.188	0.198	4.78	5.03	0.193 BSC		4.9 BSC	
L	0.016	0.026	0.41	0.66	0.016	0.027	0.40	0.70
α	0°	6°	0°	6°	0°	6°	0°	6°



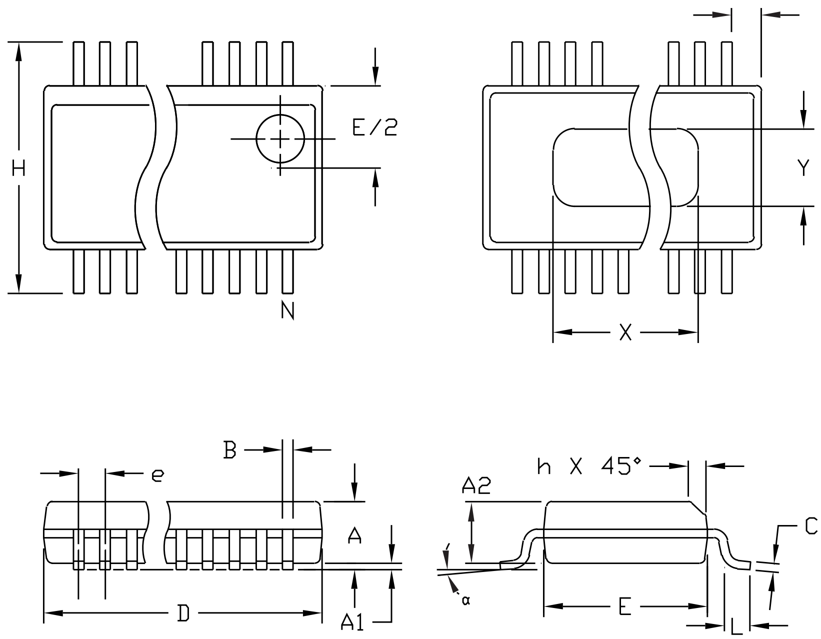
NOTES:
1. D&E DO NOT INCLUDE MOLD FLASH.
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED .15mm(.006").
3. CONTROLLING DIMENSION: MILLIMETERS.
4. MEETS JEDEC MO-187.

MAXIM
PROPRIETARY INFORMATION
TITLE:
8L uMAX PACKAGE OUTLINE DWG.

APPROVAL	DOCUMENT CONTROL NO.	REV	
	21-0036	F	1/1

Package Information (continued)

QSOP.EPS



	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
A	.061	.068	1.55	1.73
A1	.004	.0098	0.102	0.249
A2	.055	.061	1.40	1.55
B	.008	.012	0.20	0.31
C	.0075	.0098	0.191	0.249
D	SEE VARIATIONS			
E	.150	.157	3.81	3.99
e	.025 BSC		0.635 BSC	
H	.230	.244	5.84	6.20
h	.010	.016	0.25	0.41
L	.016	.035	0.41	0.89
N	SEE VARIATIONS			
X	SEE VARIATIONS			
Y	.071	.087	1.803	2.209
α	0°	8°	0°	8°

VARIATIONS:

	INCHES		MILLIMETERS			
	MIN.	MAX.	MIN.	MAX.	N	
D	.189	.196	4.80	4.98	16	AA
S	.0020	.0070	0.05	0.18		
X	.107	.123	2.72	3.12		
D	.337	.344	8.56	8.74	20	AB
S	.0500	.0550	1.270	1.397		
D	.337	.344	8.56	8.74	24	AC
S	.0250	.0300	0.635	0.762		
D	.386	.393	9.80	9.98	28	AD
S	.0250	.0300	0.635	0.762		
X	.271	.287	6.88	7.29		

NOTES:

1. D & E DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED .006" PER SIDE.
3. HEAT SLUG DIMENSIONS X AND Y APPLY ONLY TO 16 AND 28 LEAD POWER-QSOP PACKAGES.
4. CONTROLLING DIMENSIONS: INCHES.

MAXIM

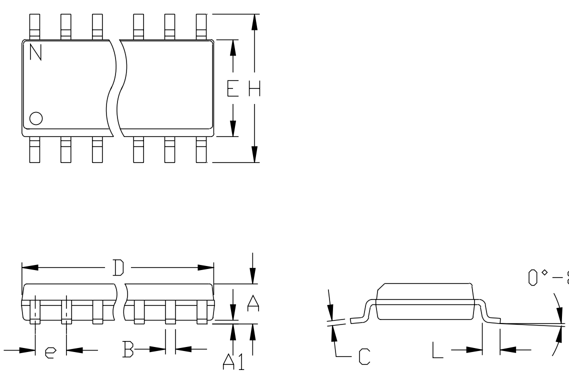
PROPRIETARY INFORMATION

TITLE:

PACKAGE OUTLINE, QSOP, .150", .025" LEAD PITCH

APPROVAL	DOCUMENT CONTROL NO.	REV	
	21-0055	B	1/1

Package Information (continued)



	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.053	0.069	1.35	1.75
A1	0.004	0.010	0.10	0.25
B	0.014	0.019	0.35	0.49
C	0.007	0.010	0.19	0.25
e	0.050		1.27	
E	0.150	0.157	3.80	4.00
H	0.228	0.244	5.80	6.20
h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27

	INCHES		MILLIMETERS			
	MIN	MAX	MIN	MAX	N	MS012
D	0.189	0.197	4.80	5.00	8	A
D	0.337	0.344	8.55	8.75	14	B
D	0.386	0.394	9.80	10.00	16	C

NOTES:
1. D&E DO NOT INCLUDE MOLD FLASH
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED .15mm (.006")
3. LEADS TO BE COPLANAR WITHIN .102mm (.004")
4. CONTROLLING DIMENSION: MILLIMETER
5. MEETS JEDEC MS012-XX AS SHOWN IN ABOVE TABLE
6. N = NUMBER OF PINS

MAXIM 120 SAN GABRIEL DR. SUNNYVALE CA 94086 FAX (408) 737 7194 PROPRIETARY INFORMATION | TITLE: PACKAGE FAMILY OUTLINE: SOIC .150" | 1/1 | DOCUMENT CONTROL NUMBER: 21-0041 REV: A