JVI JXI JVI 500mA Low-Dropout Linear Regulator in µMAX

General Description

The MAX1792 low-dropout linear regulator operates from a +2.5V to +5.5V supply and delivers a guaranteed 500mA load current with low 130mV dropout. The high-accuracy (\pm 1%) output voltage is preset at an internally trimmed voltage (see *Selector Guide*) or can be adjusted from 1.25V to 5.0V with an external resistive divider.

An internal PMOS pass transistor allows the low 80µA supply current to remain independent of load, making this device ideal for portable battery-operated equipment such as personal digital assistants (PDAs), cellular phones, cordless phones, base stations, and notebook computers.

Other features include an active-low open-drain reset output with a 4ms timeout period that indicates when the output is out of regulation, a 0.1 μ A shutdown mode, short-circuit protection, and thermal shutdown protection. The device is available in a miniature 1.3W, 8-pin power- μ MAX package with a metal pad on the underside of the package.

Applications

- Notebook Computers
- Cellular and Cordless Telephones
- Personal Digital Assistants (PDAs)
- Palmtop Computers
- **Base Stations**
- USB Hubs
- **Docking Stations**



Typical Operating Circuit

For price, delivery, and to place orders, please contact Maxim Distribution at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

Features

- Guaranteed 500mA Output Current
- Low 130mV Dropout at 500mA
- Up to ±1% Output Voltage Accuracy Preset at 1.5V, 1.8V, 2.5V, 3.3V, or 5.0V Adjustable from 1.25V to 5.0V
- Reset Output with 4ms Timeout Period
- Low 80µA Ground Current
- 0.1µA Shutdown Current
- Thermal Overload Protection
- Output Current Limit
- Tiny 1.3W Power-µMAX Package

Ordering Information

PART*	TEMP RANGE	PIN-PACKAGE
MAX1792EUA	-40°C to +85°C	8 Power-µMAX
*Incort the desired tw	a diait auffix (and Ca	laatar Cuida) into tha

*Insert the desired two-digit suffix (see Selector Guide) into the blanks to complete the part number.

Contact factory for other preset output voltages.

_Selector Guide

PART AND SUFFIX	Vout	μMAX TOP MARK
MAX1792EUA15	1.5V or Adj.	AAAE
MAX1792EUA18	1.8V or Adj.	AAAA
MAX1792EUA25	2.5V or Adj.	AAAB
MAX1792EUA33	3.3V or Adj.	AAAC
MAX1792EUA50	5.0V or Adj.	AAAD

Pin Configuration

Maxim Integrated Products 1



ABSOLUTE MAXIMUM RATINGS

IN, SHDN, RST, SET to GND	0.3V to +6V
OUT to GND	
Output Short-Circuit Duration	Indefinite
Continuous Power Dissipation ($T_A = +7$	0°C)
8-Pin Power-µMAX (derate 17mW/°C	above +70°C)1.3W

Operating Temperature Range	40°C to +85°C
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{IN} = V_{OUT(NOM)} + 500mV, \text{ or } V_{IN} = +2.5V \text{ (whichever is greater)}, \overline{SHDN} = IN, T_A = 0^{\circ}C \text{ to } +85^{\circ}C, \text{ unless otherwise noted}. Typical values are at T_A = +25^{\circ}C.)$

SYMBOL	COND	ITIONS	MIN	TYP	MAX	UNITS	
VIN			2.5		5.5	V	
Vuvlo	Rising, 75mV hysteres	sis	2.0	2.15	2.3	V	
	I _{OUT} = 100mA,	Preset V _{OUT} ≥ 2.5V	-1		+1	%	
	T _A = +85°C	Preset V _{OUT} < 2.5V	-1.5		+1.5		
Vout	$I_{OUT} = 100 \text{mA}, T_{A} = 0$)°C to +85°C	-2		+2		
	$I_{OUT} = 1$ mA to 500mA T _A = 0°C to +85°C	$V_{\rm N}, V_{\rm IN} > V_{\rm OUT} + 0.5V,$	-3		+3	%	
			1.25		5	V	
	$V_{IN} = +2.7V,$	$T_A = +85^{\circ}C$	1.229	1.250	1.271		
VSET	V_{OUT} set to 2.0V, $I_{OUT} = 100 \text{mA}$	$T_A = 0^{\circ}C \text{ to } +85^{\circ}C$	1.219		1.281	V	
lout	$V_{IN} \ge 2.7V$	·	500			mA _{RMS}	
ILIM	$V_{OUT} = 0, V_{IN} \ge 2.7V$		0.55	0.8	1.8	А	
	$V_{OUT} > 96\%$ of nominal value, $V_{IN} \ge 2.7V$			1.6		А	
			50	100	150	mV	
ISET	$V_{SET} = 1.25V$		-100		+100	nA	
IQ	I _{OUT} = 1mA			80	250		
	$I_{OUT} = 500 \text{mA}$			110		μA	
V _{IN} - V _{OUT}	Vout = 5.0V Vout = 3.3V Vout = 2.5V	V _{OUT} = 5.0V		120	225	mV	
		$V_{OUT} = 3.3V$		130	250		
			210	360			
ΔV_{LNR}	V _{IN} from (V _{OUT} + 100 I _{LOAD} = 5mA	mV) to 5.5V,	-0.15	0	+0.15	%/V	
ΔV_{LDR}	I _{OUT} = 1mA to 500mA			0.4	1.0	%	
	10Hz to 1MHz, Cout = 3.3μ F (ESR < 0.1Ω)			115		μV_{RMS}	
		· · ·					
IOFF	$\overline{\text{SHDN}} = \text{GND}, \text{V}_{\text{IN}} = 5.5\text{V}$			0.1	15	μΑ	
VIH	H 2.5V < V _{IN} < 5.5V 1.		1.6			V	
VIL	$2.5V < V_{IN} < 5.5V$				0.6	v	
	$\overline{\text{SHDN}} = \text{IN or GND}$			10	100	nA	
	VIN VUVLO VOUT VOUT VSET IOUT ILIM ISET IQ VIN - VOUT VIN AVLNR AVLDR IOFF VIH	VINVINVUVLORising, 75mV hysteresIOUT = 100mA, TA = +85°CVOUTIOUT = 100mA, TA = 0IOUT = 1mA to 500mATA = 0°C to +85°CVSETVIN = +2.7V, VOUT set to 2.0V, IOUT = 100mAIOUTVIN $\ge 2.7V$ ILIMVOUT = 0, VIN $\ge 2.7V$ VOUTIOUT = 100mAIOUTVIN $\ge 2.7V$ ILIMVOUT = 0, VIN $\ge 2.7V$ VOUT > 96% of nominIOUTVIN $\ge 2.7V$ IOUT = 1mAIOUT = 500mAVOUTIOUT = 500mAVIN - VOUTVIN from (VOUT + 100) ILOAD = 5mA $\Delta VLDR$ IOUT = 1mA to 500mAOVILDRIOUT = 1mA to 500mAVIH2.5V < VIN < 5.5V	$\begin{tabular}{ c c c c c } \hline V_{IN} & $$V_{UVLO}$ & $$Rising, 75mV hysteresis$ \\ \hline V_{UVLO}$ & $$Rising, 75mV hysteresis$ \\ \hline Preset V_{OUT} \ge 2.5V$ \\ \hline $$Preset V_{OUT} \ge 2.5V$ \\ \hline $$Preset V_{OUT} < 2.5V$ \\ \hline $$Preset V_{OUT} < 2.5V$ \\ \hline $$Preset V_{OUT} < 2.5V$ \\ \hline $$IouT = 100mA, T_A = 0°C to +85°C$ \\ \hline $$IouT = 1mA to 500mA, V_{IN} > V_{OUT} + 0.5V$, $$TA = 0°C to +85°C$ \\ \hline $$IouT = 1mA to 500mA, V_{IN} > V_{OUT} + 0.5V$, $$TA = 0°C to +85°C$ \\ \hline $$IouT = 100mA$ & $$TA = +85°C$ \\ \hline $$VSET$ & $$V_{OUT} set to 2.0V$, $$IouT = 100mA$ & $$TA = +85°C$ \\ \hline $$IouT $$V_{IN} = +2.7V$, $$V_{OUT} set to 2.0V$, $$IouT = 0, $$V_{IN} \ge 2.7V$ \\ \hline $$VouT $$VOUT set to 2.0V$, $$IouT = 0, $$V_{IN} \ge 2.7V$ \\ \hline $$V_{OUT} = 0, $$V_{IN} \ge 2.7V$ \\ \hline $$V_{OUT} > 96\% of nominal value, $$V_{IN} \ge 2.7V$ \\ \hline $$V_{OUT} > 96\% of nominal value, $$V_{IN} \ge 2.7V$ \\ \hline $$V_{OUT} = 1mA$ $$IoUT = 1mA$ $$IoUT = 1mA$ $$IoUT = 5.0V$ \\ \hline $$IoUT = 1mA$ $$IoUT = 500mA$ \\ \hline $$V_{IN} - $$V_{OUT} = 500mA$ \\ \hline $$V_{OUT} = 500mA$ \\ \hline $$V_{OUT} = 2.5V$ \\ \hline $$V_{OUT} $$V_{IN} from ($V_{OUT} + 100mV$) to 5.5V$, $$I_{LOAD} = 5mA$ \\ \hline $$\Delta V_{LNR}$ & $$IoUT = 1mA to 500mA$ \\ \hline $$IOHz to 1MHz, $CouT = 3.3\mu$F (ESR < 0.1\Omega$) \\ \hline $$V_{IH} $$$2.5V < $$V_{IN} < 5.5V$ \\ \hline $$V_{IL} $$$$2.5V < $$V_{IN} < 5.5V$ \\ \hline $$V_{IL} $$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$	$\begin{tabular}{ c c c c c c c c c c c } \hline V_{IN} & 2.5 \\ \hline V_{UVLO} & Rising, 75mV hysteresis & 2.0 \\ \hline I_{OUT} = 100mA, & Preset V_{OUT} \ge 2.5V & -1 \\ \hline T_A = +85^\circ C & Preset V_{OUT} \ge 2.5V & -1.5 \\ \hline V_{OUT} & I_{OUT} = 100mA, T_A = 0^\circ C to +85^\circ C & -2 \\ \hline I_{OUT} = 1mA to 500mA, V_{IN} > V_{OUT} + 0.5V, & -3 \\ \hline I_{OUT} = 1mA to 500mA, V_{IN} > V_{OUT} + 0.5V, & -3 \\ \hline V_{SET} & V_{IN} = +2.7V, & T_A = +85^\circ C & 1.229 \\ \hline V_{SET} & V_{IN} = 2.7V, & T_A = +85^\circ C & 1.219 \\ \hline I_{OUT} & V_{IN} \ge 2.7V & 500 \\ \hline I_{LIM} & V_{OUT} = 0, V_{IN} \ge 2.7V & 0.55 \\ \hline & V_{OUT} > 96\% of nominal value, V_{IN} \ge 2.7V \\ \hline & & 50 \\ \hline I_{SET} & V_{SET} = 1.25V & -100 \\ \hline I_{Q} & I_{OUT} = 500mA & V_{OUT} = 5.0V \\ \hline & V_{OUT} & I_{OUT} = 500mA & V_{OUT} = 2.5V \\ \hline & V_{IN} - V_{OUT} & I_{OUT} = 1mA to 500mA \\ \hline & V_{IN} R & V_{IN} from (V_{OUT} + 100mV) to 5.5V, & -0.15 \\ \hline & \Delta V_{LNR} & V_{IN} from (V_{OUT} + 100mV) to 5.5V, & -0.15 \\ \hline & \Delta V_{LDR} & I_{OUT} = 1mA to 500mA & 0 \\ \hline & I_{OFF} & SHDN = GND, V_{IN} = 5.5V & 1.6 \\ \hline & V_{IL} & 2.5V < V_{IN} < 5.5V & 1.6 \\ \hline & V_{IL} & 2.5V < V_{IN} < 5.5V & 1.6 \\ \hline \end{array}$	$\begin{tabular}{ c c c c c c c } \hline V_{IN} & 2.5 & 2.0 & 2.15 \\ \hline V_{UVLO} & Rising, 75mV hysteresis & 2.0 & 2.15 \\ \hline I_{OUT} = 100mA, & Preset V_{OUT} \ge 2.5V & -1 & Preset V_{OUT} \ge 2.5V & -1.5 & Preset V_{OUT} < 2.5V & Preset V_{OUT} < 0.5V, & Preset V_{OUT} < 0.5V, & Preset V_{OUT} < 0.5V & 1.229 & 1.250 & Preset V_{OUT} < 1.250 & Preset V_{OUT} < 1.219 & Preset V_{OUT} < 0.55 & 0.8 & Preset V_{OUT} < 0.55 & 0.8 & Preset V_{OUT} > 0.55 & 0.100 & Preset V_{OUT} > 0.55 & 0.8 & Preset V_{OUT} > 0.55 & Preset V_{OUT} > 0.15 & Preset V_{OUT} > 0.15 & Preset V_{OUT} > 0.15 & Preset V_{OUT} > 0.10 & Preset V_{OUT} > 0.55 & Preset V_{OUT} > 0.10 & Preset V_{OUT} > 0.55 & Preset V_{OUT} > 0.10 & Preset V_{OUT} > 0.55 & Preset V_{OUT} > 0.10 & Preset V_{OUT} > 0.55 & Preset V_{OUT} > 0.10 & Preset V_{OUT} > 0.10 & Prese$	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	

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ELECTRICAL CHARACTERISTICS (continued)

 $(V_{IN} = V_{OUT(NOM)} + 500mV, \text{ or } V_{IN} = +2.5V \text{ (whichever is greater)}, \overline{SHDN} = IN, T_A = 0°C \text{ to } +85°C, unless otherwise noted. Typical values are at T_A = +25°C.)$

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
RESET OUTPUT						
Reset Output Low Voltage	V _{OL}	RST sinking 1mA		0.01	0.1	V
Operating Voltage Range for Valid Reset		RST sinking 100μA	1.0		5.5	V
RST Output High Leakage Current		$V_{\overline{\text{RST}}} = +5.5V$			100	nA
RST Threshold		Rising edge, referred to VOUT(NOMINAL)	90	93	96	%
RST Release Delay	t _{RP}	Rising edge of OUT to rising edge of $\overline{\text{RST}}$	1.5	4.5	8	ms
THERMAL PROTECTION						
Thermal Shutdown Temperature	T _{SHDN}			170		°C
Thermal Shutdown Hysteresis	ΔT_{SHDN}			20		°C

ELECTRICAL CHARACTERISTICS

(VIN = V_{OUT(NOM)} + 500mV, or VIN = +2.5V (whichever is greater), SHDN = IN, T_A = -40°C to +85°C, unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS		MIN	MAX	UNITS	
Input Voltage	VIN			2.5	5.5	V	
Input Undervoltage Lockout	Vuvlo	Rising or falling		2.0	2.3	V	
Output Voltage Accuracy		$I_{OUT} = 100 \text{mA}$		-2	+2	0/	
(Preset Mode)	Vout	I _{OUT} = 1mA to 500	mA	-3	+3	%	
Adjustable Output Voltage Range				1.25	5	V	
SET Voltage Threshold (Adjustable Mode)	V _{SET}	I _{OUT} = 100mA		1.212	1.288	V	
Maximum Output Current	Iout			500		mA _{RMS}	
Short-Circuit Current Limit	ILIM	V _{OUT} = 0		0.55	1.8	Α	
SET Dual Mode Threshold				50	150	mV	
SET Input Bias Current	I _{SET}	$V_{SET} = 1.25V$		-100	+100	nA	
Ground-Pin Current	IQ	$I_{OUT} = 1mA$			250	μΑ	
			$V_{OUT} = 5.0V$		225		
Dropout Voltage (Note 1)	V _{IN} -	VIN - VOUT IOUT = 500mA	$V_{OUT} = 3.3V$		250	mV	
	V001		V _{OUT} = 2.5V		360		
Line Regulation	ΔV_{LNR}	V _{IN} from (V _{OUT} + 1 I _{LOAD} = 5mA	V_{IN} from (V_{OUT} + 100mV) to 5.5V,		+0.15	%/V	
Load Regulation	ΔV_{LDR}	I _{OUT} = 1mA to 500	mA		1.0	%	
SHUTDOWN				·		<u>.</u>	
Shutdown Supply Current	IOFF	$\overline{SHDN} = GND, V_{IN}$	= +5.5V		15	μΑ	
SHDN Input	VIH	$2.5V < V_{IN} < 5.5V$		1.6		V	
Threshold	VIL	$2.5V < V_{IN} < 5.5V$			0.6	- V	
SHDN Input Bias Current	ISHDN	SHDN = IN or GND			100	nA	



ELECTRICAL CHARACTERISTICS (continued)

(VIN = V_{OUT(NOM)} + 500mV, or VIN = +2.5V (whichever is greater), SHDN = IN, T_A = -40°C to +85°C, unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	МАХ	UNITS
RESET OUTPUT					
Reset Output Low Voltage	VOL	RST sinking 1mA		0.1	V
Operating Voltage Range for Valid Reset		RST sinking 100μA	1.0	5.5	V
RST Output High Leakage Current		V RST = +5.5V		100	nA
RST Threshold		Rising edge, referred to VOUT(NOMINAL)	90	96	%
RST Release Delay	t _{RP}	Rising edge of OUT to rising edge of \overline{RST}	1.5	8	ms

Note 1: Dropout voltage is defined as V_{IN} - V_{OUT} , when V_{OUT} is 100mV below the value of V_{OUT} measured when $V_{IN} = V_{OUT}(NOM)$ + 0.5V. Since the minimum input voltage is 2.5V, this specification is only meaningful when VOUT(NOM) ≥ 2.5V. For VOUT(NOM) between 2.5V and 3.5V, use the following equations: Typical Dropout = -93mV/V × V_{OUT(NOM)} + 445mV; Guaranteed Maximum Dropout = -137mV/V × VOUT(NOM) + 704mV. For VOUT(NOM) ≥ 3.5V: Typical Dropout = 120mV; Guaranteed Maximum Dropout = 225mV.

Typical Operating Characteristics

Note 2: Specifications to -40°C are guaranteed by design, not production tested.

(MAX1792EUA33, VIN = VOUT + 500mV, SHDN = IN, CIN = 1µF, COUT = 3.3µF, TA = +25°C, unless otherwise noted.) **OUTPUT VOLTAGE OUTPUT VOLTAGE OUTPUT VOLTAGE** vs. INPUT VOLTAGE vs. TEMPERATURE vs. LOAD CURRENT 3.5 3.33 3.33 $V_{IN} = V_{OUT} + 500 \text{mV}$ $I_{OUT} = 0$ 3.0 $I_{OUT} = 0$ 3.32 3.32 2.5 I_{OUT} = 500mA OUTPUT VOLTAGE (V) OUTPUT VOLTAGE (V) **DUTPUT VOLTAGE (V)** 3.31 3.31 2.0 3.30 3.30 1.5 3.29 3.29 1.0 3.28 3.28 0.5 0 3.27 3.27 2.5 2.0 3.0 3.5 4.0 4.5 5.0 5.5 100 200 300 400 500 -40 -15 10 35 60 85 0 600 700 800 INPUT VOLTAGE (V) TEMPERATURE (°C) LOAD CURRENT (mA) **DROPOUT VOLTAGE GROUND-PIN CURRENT GROUND-PIN CURRENT** vs. LOAD CURRENT vs. INPUT VOLTAGE vs. LOAD CURRENT 140 250 140 $I_{OUT} = 500 \text{mA}$ $T_A = +85^{\circ}C$ 130 120 $V_{IN} = 5.5V$ 200 GROUND-PIN CURRENT (μA) GROUND-PIN CURRENT (µA) 120 DROPOUT VOLTAGE (mV) 100 110 150 $V_{IN} = 3.8V$ $T_A = +25^{\circ}C$ 80 $I_{OUT} = 0$ 100 60 100 90 $T_A = -40^{\circ}C$ 40 80 50 20 70 0 0 60 100 200 300 400 500 600 700 800 100 200 300 400 500 600 700 800 0 $0 \quad 0.5 \ 1.0 \ 1.5 \ 2.0 \ 2.5 \ 3.0 \ 3.5 \ 4.0 \ 4.5 \ 5.0 \ 5.5$ 0 LOAD CURRENT (mA) INPUT VOLTAGE (V) LOAD CURRENT (mA) /N/IXI/N

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Typical Operating Characteristics (continued)

(MAX1792EUA33, $V_{IN} = V_{OUT} + 500$ mV, $\overline{SHDN} = IN$, $C_{IN} = 1\mu$ F, $C_{OUT} = 3.3\mu$ F, $T_A = +25^{\circ}$ C, unless otherwise noted.)









LOAD CURRENT (mA)

/N/IXI/N







MAX1792

Typical Operating Characteristics (continued)

(MAX1792EUA33, V_{IN} = V_{OUT} + 500mV, \overline{SHDN} = IN, C_{IN} = 1µF, C_{OUT} = 3.3µF, T_A = +25°C, unless otherwise noted.)

3V V_{OUT}

1V/div

0



 $R_{0UT} = 6.6\Omega$ (500mA)

20µs/div



Pin Description

PIN	NAME	FUNCTION
1, 2	IN	Regulator Input. Supply voltage can range from +2.5V to +5.5V. Bypass with a 1μ F capacitor to GND (see <i>Capacitor Selection and Regulator Stability</i>). Connect both input pins together externally.
3	RST	Open-Drain Active-Low Reset Output. $\overline{\text{RST}}$ remains low while the output voltage (V _{OUT}) is below the reset threshold and for at least 4ms after V _{OUT} rises above the reset threshold. Connect a 100k Ω pullup resistor to OUT to obtain an output voltage.
4	SHDN	Active-Low Shutdown Input. A logic low reduces supply current to 0.1 μ A. In shutdown, the $\overline{\text{RST}}$ output is low and OUT is pulled low through an internal 5k Ω resistor. Connect to IN for normal operation.
5	GND	Ground. This pin and the exposed pad also function as a heatsink. Solder both to a large pad or to the circuit-board ground plane to maximize power dissipation.
6	SET	Voltage-Setting Input. Connect to GND for preset output. Connect to a resistive divider between OUT and GND to set the output voltage between 1.25V and 5.0V.
7, 8	OUT	Regulator Output. Sources up to 500mA. Bypass with a 3.3μ F low-ESR capacitor to GND. Use a 4.7μ F capacitor for output voltages below 2V. Connect both output pins together externally.

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Figure 1. Functional Diagram

Detailed Description

The MAX1792 is a low-dropout, low-quiescent-current linear regulator designed primarily for battery-powered applications. The device supplies loads up to 500mA and is available with preset output voltages. As illustrated in Figure 1, the MAX1792 consists of a 1.25V reference, error amplifier, P-channel pass transistor, and internal feedback voltage-divider.

The 1.25V reference is connected to the error amplifier, which compares this reference with the feedback voltage and amplifies the difference. If the feedback voltage is lower than the reference voltage, the pass-transistor gate is pulled lower, which allows more current to pass to the output and increases the output voltage. If the feedback voltage is too high, the passtransistor gate is pulled up, allowing less current to pass to the output. The output voltage is fed back through either an internal resistive divider connected to OUT or an external resistor network connected to SET. The Dual Mode comparator examines V_{SET} and selects the feedback path. If V_{SET} is below 50mV, the internal feedback path is used and the output is regulated to the factory-preset voltage.

Additional blocks include an output current limiter, thermal sensor, and shutdown logic.

Internal P-Channel Pass Transistor

The MAX1792 features a 0.25Ω P-channel MOSFET pass transistor. Unlike similar designs using PNP pass transistors, P-channel MOSFETs require no base drive, which reduces quiescent current. PNP-based regulators also waste considerable current in dropout when the pass transistor saturates, and use high base-drive currents under large loads.



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MAX1792



Figure 2. Adjustable Output Using External Feedback Resistors

The MAX1792 does not suffer from these problems and consumes only $110\mu A$ of quiescent current under heavy loads as well as in dropout.

Output Voltage Selection

The MAX1792's Dual Mode operation allows operation in either a preset voltage mode or an adjustable mode. Connect SET to GND to select the preset output voltage. The two-digit part number suffix identifies the output voltage (see *Selector Guide*). For example, the MAX1792EUA33 has a preset 3.3V output voltage.

The output voltage may also be adjusted by connecting a voltage-divider from OUT to SET to GND (Figure 2). Select R2 in the $25k\Omega$ to $100k\Omega$ range. Calculate R1 with the following equation:

where $V_{SET} = 1.25V$ and V_{OUT} may range from 1.25V to 5.0V.

Shutdown

Pull \overline{SHDN} low to enter shutdown. During shutdown, the output is disconnected from the input and supply current drops to 0.1µA. When in shutdown, \overline{RST} pulls low and OUT is discharged through an internal 5k Ω resistor. The capacitance and load at OUT determine the rate at which V_{OUT} decays. SHDN can be pulled as high as +6V, regardless of the input and output voltage.

Reset Output

The reset output (RST) pulls low when OUT is less than 93% of the nominal regulation voltage. Once OUT exceeds 93% of the nominal voltage, RST goes high impedance after 4ms. RST is an open-drain N-channel output. To obtain a voltage output, connect a pullup resistor from RST to OUT. A 100k Ω resistor works well for most applications. RST can be used as a power-on-reset (POR) signal to a microcontroller (μ C), or drive an external LED to indicate power failure. When the MAX1792 is shut down, RST is held low independent of the output voltage. If unused, leave RST grounded or unconnected.

Current Limit

The MAX1792 monitors and controls the pass transistor's gate voltage, limiting the output current to 0.8A (typ). This current limit doubles when the output voltage is within 4% of the nominal value to improve performance with large load transients. The output can be shorted to ground for an indefinite period of time without damaging the part.

Thermal Overload Protection

Thermal overload protection limits total power dissipation in the MAX1792. When the junction temperature exceeds $T_J = +170^{\circ}$ C, a thermal sensor turns off the pass transistor, allowing the device to cool. The thermal sensor turns the pass transistor on again after the junction temperature cools by 20°C, resulting in a pulsed output during continuous thermal overload conditions. Thermal overload protection protects the MAX1792 in the event of fault conditions. For continuous operation, do not exceed the absolute maximum junction-temperature rating of $T_J = +150^{\circ}$ C.

Operating Region and Power Dissipation

The MAX1792's maximum power dissipation depends on the thermal resistance of the IC package and circuit board, the temperature difference between the die junction and ambient air, and the rate of air flow. The power dissipated in the device is $P = I_{OUT} \times (V_{IN} - V_{OUT})$. The maximum allowed power dissipation is 1.3W or:

$$P_{MAX} = (T_{J(MAX)} - T_{A}) / (\theta_{JC} + \theta_{CA})$$

where T_J - T_A is the temperature difference between the MAX1792 die junction and the surrounding air, θ_{JC} is the thermal resistance from the junction to the case, and θ_{CA} is the thermal resistance from the case through the PC board, copper traces, and other materials to the surrounding air.

The MAX1792 package features an exposed thermal pad on its underside. This pad lowers the thermal resistance of the package by providing a direct heat con-



MAX1792



Figure 3. Power Operating Regions: Maximum Output Current vs. Supply Voltage

duction path from the die to the PC board. Additionally, the MAX1792's ground pin (GND) performs the dual function of providing an electrical connection to system ground and channeling heat away. Connect the exposed backside pad and GND to the system ground using a large pad or ground plane, or multiple vias to the ground plane layer.

The MAX1792 delivers up to 0.5A(RMS) and operates with input voltages up to 5.5V, but not simultaneously. High output currents can only be sustained when input-output differential voltages are low, as shown in Figure 3.

Applications Information

Capacitor Selection and Regulator Stability

Capacitors are required at the MAX1792's input and output for stable operation over the full temperature range and with load currents up to 500mA. Connect a 1 μ F capacitor between IN and ground and a 3.3 μ F low equivalent series resistance (ESR) capacitor between OUT and ground. For output voltages less than 2V, use a 4.7 μ F low-ESR output capacitor. The input capacitor (C_{IN}) lowers the source impedance of the input supply. Reduce noise and improve load-transient response, stability, and power-supply rejection by using larger output capacitors such as 10 μ F.

The output capacitor's (C_{OUT}) ESR affects stability and output noise. Use output capacitors with an ESR of 0.1Ω or less to ensure stability and optimum transient

response. Surface-mount ceramic capacitors have very low ESR and are commonly available in values up to 10 μ F. Connect C_{IN} and C_{OUT} as close to the MAX1792 as possible to minimize the impact of PC board trace inductance.

Noise, PSRR, and Transient Response The MAX1792 is designed to operate with low dropout voltages and low quiescent currents in battery-powered systems while still maintaining good noise, transient response, and AC rejection. See the *Typical Operating Characteristics* for a plot of Power-Supply Rejection Ratio (PSRR) vs. Frequency. When operating from noisy sources, improved supply-noise rejection and transient response can be achieved by increasing the values of the input and output bypass capacitors and through passive filtering techniques.

The MAX1792 load-transient response graphs (see *Typical Operating Characteristics*) show two components of the output response: a DC shift from the output impedance due to the load current change, and the transient response. A typical transient response for a step change in the load current from 5mA to 500mA is 18mV. Increasing the output capacitor's value and decreasing the ESR attenuates the overshoot.

Input-Output (Dropout) Voltage

A regulator's minimum input-to-output voltage differential (dropout voltage) determines the lowest usable supply voltage. In battery-powered systems, this determines the useful end-of-life battery voltage. Because the MAX1792 uses a P-channel MOSFET pass transistor, its dropout voltage is a function of drain-tosource on-resistance (R_{DS(ON)}) multiplied by the load current (see *Typical Operating Characteristics*):

VDROPOUT = VIN - VOUT = RDS(ON) X IOUT

The MAX1792 ground current remains below 150 $\!\mu\text{A}$ in dropout.

Note: The MAX1792 has an exposed thermal pad on the bottom side of the package.

Chip Information

TRANSISTOR COUNT: 845

MAX1792



Note: MAX1792 has an exposed thermal pad on the bottom side of the package

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Package Information