

## LMT89 2.4V, 10µA, SC70, Temperature Sensor

Check for Samples: [LMT89](#)

### FEATURES

- Cost-Effective Alternative to Thermistors
- Rated for full  $-55^{\circ}\text{C}$  to  $+130^{\circ}\text{C}$  range
- Available in an SC70 Package
- Predictable Curvature Error
- Suitable for Remote Applications

### APPLICATIONS

- Industrial
- HVAC
- Automotive
- Disk Drives
- Portable Medical Instruments
- Computers
- Battery Management
- Printers
- Power Supply Modules
- FAX Machines
- Mobile Phones
- Automotive

### DESCRIPTION

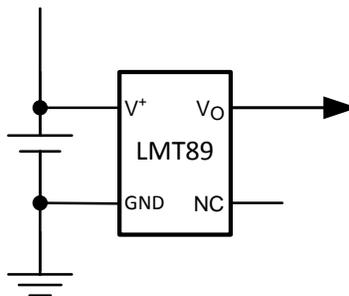
The LMT89 is a precision analog output CMOS integrated-circuit temperature sensor that operates over a  $-55^{\circ}\text{C}$  to  $130^{\circ}\text{C}$  temperature range. The power supply operating range is 2.4 V to 5.5 V. The transfer function of LMT89 is predominately linear, yet has a slight predictable parabolic curvature. The accuracy of the LMT89 when specified to a parabolic transfer function is  $\pm 1.5^{\circ}\text{C}$  at an ambient temperature of  $30^{\circ}\text{C}$ . The temperature error increases linearly and reaches a maximum of  $\pm 2.5^{\circ}\text{C}$  at the temperature range extremes. The temperature range is affected by the power supply voltage. At a power supply voltage of 2.7 V to 5.5 V the temperature range extremes are  $130^{\circ}\text{C}$  and  $-55^{\circ}\text{C}$ . Decreasing the power supply voltage to 2.4 V changes the negative extreme to  $-30^{\circ}\text{C}$ , while the positive remains at  $130^{\circ}\text{C}$ .

The LMT89 quiescent current is less than 10 µA. Therefore, self-heating is less than  $0.02^{\circ}\text{C}$  in still air. Shutdown capability for the LMT89 is intrinsic because its inherent low power consumption allows it to be powered directly from the output of many logic gates or does not necessitate shutdown at all.

The LMT89 is a cost-competitive alternative to thermistors.

### TYPICAL APPLICATION

#### Full-Range Celsius (Centigrade) Temperature Sensor ( $-55^{\circ}\text{C}$ TO $130^{\circ}\text{C}$ ) Operating From a Single LI-Ion Battery Cell



$$V_O = (-3.88 \times 10^{-6} \times T^2) + (-1.15 \times 10^{-2} \times T) + 1.8639$$

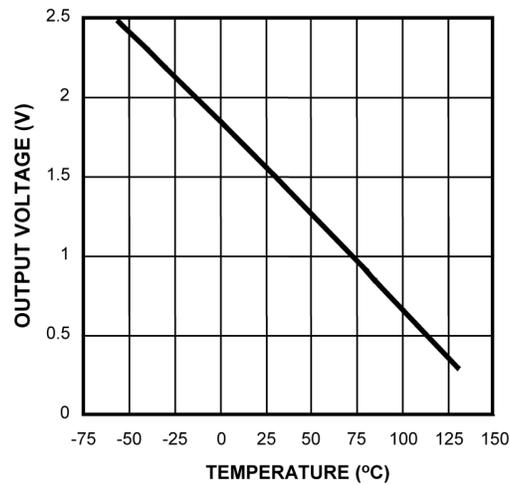
$$T = -1481.96 + \sqrt{2.1962 \times 10^6 + \frac{(1.8639 - V_O)}{3.88 \times 10^{-6}}}$$

where: T is temperature, and  $V_O$  is the measured output voltage of the LMT89.



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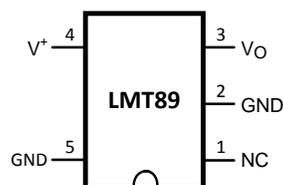


**Figure 1. Output Voltage vs Temperature**

**Table 1. Output Voltage vs Temperature**

TEMPERATURE (T)	TYPICAL $V_O$
130°C	303 mV
100°C	675 mV
80°C	919 mV
30°C	1515 mV
25°C	1574 mV
0°C	1863.9 mV
-30°C	2205 mV
-40°C	2318 mV
-55°C	2485 mV

## CONNECTION DIAGRAMS



GND (pin 2) may be grounded or left floating. For optimum thermal conductivity to the pc board ground plane, pin 2 must be grounded.

NC (pin 1) must be left floating or grounded. Other signal traces must not be connected to this pin.

**Figure 2. SC70-5 Top View**

**ABSOLUTE MAXIMUM RATINGS<sup>(1)</sup>**

		VALUES
Supply Voltage		6.5V to -0.2V
Output Voltage		(V <sup>+</sup> + 0.6 V) to -0.6 V
Output Current		10 mA
Input Current at any pin <sup>(2)</sup>		5 mA
Storage Temperature		-65°C to 150°C
Maximum Junction Temperature (T <sub>JMAX</sub> )		150°C
ESD Susceptibility <sup>(3)</sup>	Human Body Model	2500 V
	Machine Model	250 V
<i>Soldering process must comply with the Reflow Temperature Profile specifications. Refer to <a href="http://www.ti.com/packaging">www.ti.com/packaging</a>.<sup>(4)</sup></i>		

- (1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. For specifications and test conditions, see the [ELECTRICAL CHARACTERISTICS](#). The specified specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.
- (2) When the input voltage (V<sub>I</sub>) at any pin exceeds power supplies (V<sub>I</sub> < GND or V<sub>I</sub> > V<sup>+</sup>), the current at that pin should be limited to 5 mA.
- (3) The human body model is a 100 pF capacitor discharged through a 1.5 kΩ resistor into each pin. The machine model is a 200 pF capacitor discharged directly into each pin.
- (4) Reflow temperature profiles are different for lead-free and non-lead-free packages.

**OPERATION RATINGS**

Specified Temperature Range:	T <sub>MIN</sub> ≤ T <sub>A</sub> ≤ T <sub>MAX</sub>
LMT89 with 2.4 V ≤ V <sup>+</sup> ≤ 2.7 V	-30°C ≤ T <sub>A</sub> ≤ 130°C
LMT89 with 2.7 V ≤ V <sup>+</sup> ≤ 5.5 V	-55°C ≤ T <sub>A</sub> ≤ 130°C
Supply Voltage Range (V <sup>+</sup> )	2.4 V to 5.5 V
Thermal Resistance, θ <sub>JA</sub> <sup>(1)</sup> SC70	415°C/W

- (1) The junction to ambient thermal resistance (θ<sub>JA</sub>) is specified without a heat sink in still air using the printed circuit board layout shown in [PCB Layouts Used For Thermal Measurements](#).

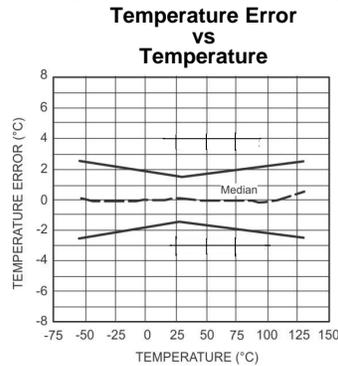
## ELECTRICAL CHARACTERISTICS

Unless otherwise noted, these specifications apply for  $V^+ = +2.7 V_{DC}$ . **Boldface limits apply for  $T_A = T_J = T_{MIN}$  to  $T_{MAX}$** ; all other limits  $T_A = T_J = 25^\circ\text{C}$ ; Unless otherwise noted.

PARAMETER	CONDITIONS	TYPICAL <sup>(1)</sup>	MAX <sup>(2)</sup>	UNIT (Limit)
Temperature to Voltage Error $V_O = (-3.88 \times 10^{-6} \times T^2) + (-1.15 \times 10^{-2} \times T) + 1.8639V$ <sup>(3)</sup>	$T_A = 25^\circ\text{C}$ to $30^\circ\text{C}$	$\pm 1.5$		$^\circ\text{C}$
	$T_A = -55^\circ\text{C}$		<b><math>\pm 2.5</math></b>	$^\circ\text{C}$
Output Voltage at $0^\circ\text{C}$		1.8639		V
Variance from Curve		$\pm 1.0$		$^\circ\text{C}$
Non-Linearity <sup>(4)</sup>	$-20^\circ\text{C} \leq T_A \leq 80^\circ\text{C}$	$\pm 0.4\%$		
Sensor Gain (Temperature Sensitivity or Average Slope) to equation: $V_O = -11.77 \text{ mV}/^\circ\text{C} \times T + 1.860V$	$-30^\circ\text{C} \leq T_A \leq 100^\circ\text{C}$	-11.77	<b>-11.4</b> <b>-12.2</b>	mV/ $^\circ\text{C}$ (min) mV/ $^\circ\text{C}$ (max)
Output Impedance	$0 \mu\text{A} \leq I_L \leq 16 \mu\text{A}$ <sup>(5)</sup> <sup>(6)</sup>		<b>160</b>	$\Omega$
Load Regulation <sup>(7)</sup>	$0 \mu\text{A} \leq I_L \leq 16 \mu\text{A}$ <sup>(5)</sup> <sup>(6)</sup>		<b>-2.5</b>	mV
Line Regulation <sup>(8)</sup>	$2.4 \text{ V} \leq V^+ \leq 5.0 \text{ V}$		<b>3.3</b>	mV/V
	$5.0 \text{ V} \leq V^+ \leq 5.5 \text{ V}$		<b>11</b>	mV
Quiescent Current	$2.4 \text{ V} \leq V^+ \leq 5.0 \text{ V}$	4.5	7	$\mu\text{A}$
	$5.0 \text{ V} \leq V^+ \leq 5.5 \text{ V}$	4.5	9	$\mu\text{A}$
	$2.4 \text{ V} \leq V^+ \leq 5.0 \text{ V}$	4.5	<b>10</b>	$\mu\text{A}$
Change of Quiescent Current	$2.4 \text{ V} \leq V^+ \leq 5.5 \text{ V}$	0.7		$\mu\text{A}$
Temperature Coefficient of Quiescent Current		-11		nA/ $^\circ\text{C}$
Shutdown Current	$V^+ \leq 0.8 \text{ V}$	0.02		$\mu\text{A}$

- (1) Typicals are at  $T_J = T_A = 25^\circ\text{C}$  and represent most likely parametric norm.
- (2) Limits are specified to TI's AOQL (Average Outgoing Quality Level).
- (3) Accuracy is defined as the error between the measured and calculated output voltage at the specified conditions of voltage, current, and temperature (expressed in  $^\circ\text{C}$ ).
- (4) Non-Linearity is defined as the deviation of the calculated output-voltage-versus-temperature curve from the best-fit straight line, over the temperature range specified.
- (5) Negative currents are flowing into the LMT89. Positive currents are flowing out of the LMT89. Using this convention the LMT89 can at most sink  $-1 \mu\text{A}$  and source  $16 \mu\text{A}$ .
- (6) Load regulation or output impedance specifications apply over the supply voltage range of 2.4V to 5.5V.
- (7) Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output due to heating effects can be computed by multiplying the internal dissipation by the thermal resistance.
- (8) Line regulation is calculated by subtracting the output voltage at the highest supply input voltage from the output voltage at the lowest supply input voltage.

## TYPICAL PERFORMANCE CHARACTERISTICS



### PCB Layouts Used For Thermal Measurements

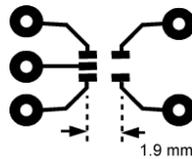


Figure 3. Layout Used For No Heat Sink Measurements

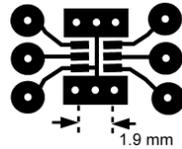


Figure 4. Layout Used For Measurements With Small Heat Sink

### LMT89 TRANSFER FUNCTION

The LMT89 transfer function can be described in different ways with varying levels of precision. A simple linear transfer function, with good accuracy near 25°C, is

$$V_o = -11.69 \text{ mV/}^\circ\text{C} \times T + 1.8663 \text{ V} \quad (1)$$

Over the full operating temperature range of -55°C to 130°C, best accuracy can be obtained by using the parabolic transfer function.

$$V_o = (-3.88 \times 10^{-6} \times T^2) + (-1.15 \times 10^{-2} \times T) + 1.8639 \quad (2)$$

solving for T:

$$T = -1481.96 + \sqrt{2.1962 \times 10^6 + \frac{(1.8639 - V_o)}{3.88 \times 10^{-6}}} \quad (3)$$

A linear transfer function can be used over a limited temperature range by calculating a slope and offset that give best results over that range. A linear transfer function can be calculated from the parabolic transfer function of the LMT89. The slope of the linear transfer function can be calculated using the following equation:

$$m = -7.76 \times 10^{-6} \times T - 0.0115, \quad (4)$$

where T is the middle of the temperature range of interest and m is in V/°C. For example for the temperature range of  $T_{\text{MIN}} = -30$  to  $T_{\text{MAX}} = +100$ °C:

$$T = 35^\circ\text{C} \quad (5)$$

and

$$m = -11.77 \text{ mV/}^\circ\text{C} \quad (6)$$

The offset of the linear transfer function can be calculated using the following equation:

$$b = (V_{OP}(T_{MAX}) + V_{OP}(T) - m \times (T_{MAX} + T))/2 \quad (7)$$

where:

$V_{OP}(T_{MAX})$  is the calculated output voltage at  $T_{MAX}$  using the parabolic transfer function for  $V_O$

$V_{OP}(T)$  is the calculated output voltage at  $T$  using the parabolic transfer function for  $V_O$ .

Using this procedure the best fit linear transfer function for many popular temperature ranges was calculated in [Table 2](#). As shown in [Table 2](#) the error that is introduced by the linear transfer function increases with wider temperature ranges.

**Table 2. First Order Equations Optimized for Different Temperature Ranges**

TEMPERATURE RANGE		LINEAR EQUATION	MAXIMUM DEVIATION OF LINEAR EQUATION FROM PARABOLIC EQUATION (°C)
T <sub>min</sub> (°C)	T <sub>max</sub> (°C)		
-55	130	$V_O = -11.79 \text{ mV/}^\circ\text{C} \times T + 1.8528 \text{ V}$	±1.41
-40	110	$V_O = -11.77 \text{ mV/}^\circ\text{C} \times T + 1.8577 \text{ V}$	±0.93
-30	100	$V_O = -11.77 \text{ mV/}^\circ\text{C} \times T + 1.8605 \text{ V}$	±0.70
-40	85	$V_O = -11.67 \text{ mV/}^\circ\text{C} \times T + 1.8583 \text{ V}$	±0.65
-10	65	$V_O = -11.71 \text{ mV/}^\circ\text{C} \times T + 1.8641 \text{ V}$	±0.23
35	45	$V_O = -11.81 \text{ mV/}^\circ\text{C} \times T + 1.8701 \text{ V}$	±0.004
20	30	$V_O = -11.69 \text{ mV/}^\circ\text{C} \times T + 1.8663 \text{ V}$	±0.004

## MOUNTING

The LMT89 can be applied easily in the same way as other integrated-circuit temperature sensors. It can be glued or cemented to a surface. The temperature that the LMT89 is sensing will be within about +0.02°C of the surface temperature to which the LMT89's leads are attached to.

This presumes that the ambient air temperature is almost the same as the surface temperature; if the air temperature were much higher or lower than the surface temperature, the actual temperature measured would be at an intermediate temperature between the surface temperature and the air temperature.

To ensure good thermal conductivity the backside of the LMT89 die is directly attached to the pin 2 GND pin. The temperatures of the lands and traces to the other leads of the LMT89 will also affect the temperature that is being sensed.

Alternatively, the LMT89 can be mounted inside a sealed-end metal tube, and can then be dipped into a bath or screwed into a threaded hole in a tank. As with any IC, the LMT89 and accompanying wiring and circuits must be kept insulated and dry, to avoid leakage and corrosion. This is especially true if the circuit may operate at cold temperatures where condensation can occur. Printed-circuit coatings and varnishes such as Humiseal and epoxy paints or dips are often used to ensure that moisture cannot corrode the LMT89 or its connections.

The thermal resistance junction to ambient ( $\theta_{JA}$ ) is the parameter used to calculate the rise of a device junction temperature due to its power dissipation. For the LMT89 the equation used to calculate the rise in the die temperature is as follows:

$$T_J = T_A + \theta_{JA} [(V^+ I_Q) + (V^+ - V_O) I_L]$$

where  $I_Q$  is the quiescent current and  $I_L$  is the load current on the output. Since the LMT89's junction temperature is the actual temperature being measured care should be taken to minimize the load current that the LMT89 is required to drive.

The tables shown in [Table 3](#) summarize the rise in die temperature of the LMT89 without any loading, and the thermal resistance for different conditions.

Table 3. Temperature Rise of LMT89 Due to Self-Heating and Thermal Resistance ( $\theta_{JA}$ )<sup>(1)</sup>

	SC70-5		SC70-5	
	NO HEAT SINK		SMALL HEAT SINK	
	$\theta_{JA}$ (°C/W)	$T_J - T_A$ (°C)	$\theta_{JA}$ (°C/W)	$T_J - T_A$ (°C)
Still air	412	0.2	350	0.19
Moving air	312	0.17	266	0.15

(1) See [PCB Layouts Used For Thermal Measurements](#) for PCB layout samples.

### CAPACITIVE LOADS

The LMT89 handles capacitive loading well. Without any precautions, the LMT89 can drive any capacitive load less than 300 pF as shown in . Over the specified temperature range the LMT89 has a maximum output impedance of 160 Ω. In an extremely noisy environment it may be necessary to add some filtering to minimize noise pickup. It is recommended that 0.1 μF be added from V<sup>+</sup> to GND to bypass the power supply voltage, as shown in [Figure 5](#). In a noisy environment it may even be necessary to add a capacitor from the output to ground with a series resistor as shown in [Figure 5](#). A 1 μF output capacitor with the 160 Ω maximum output impedance and a 200 Ω series resistor will form a 442 Hz lowpass filter. Since the thermal time constant of the LMT89 is much slower, the overall response time of the LMT89 will not be significantly affected.

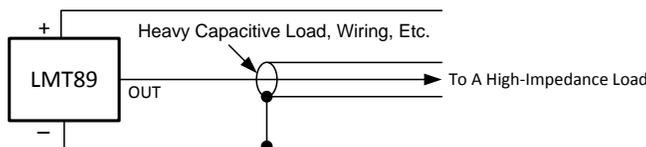


Figure 5. LMT89 No Decoupling Required for Capacitive Loads Less Than 300 pF

R (Ω)	C (μF)
200	1
470	0.1
680	0.01
1 k	0.001

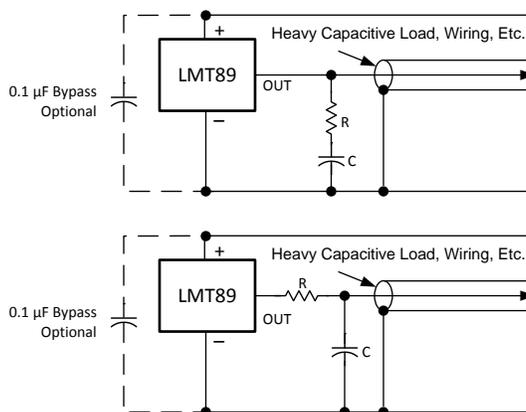


Figure 6. LMT89 with Filter for Noisy Environment and Capacitive Loading Greater Than 300 pF

**NOTE**

Either placement of resistor as shown above is just as effective.

APPLICATION CIRCUITS

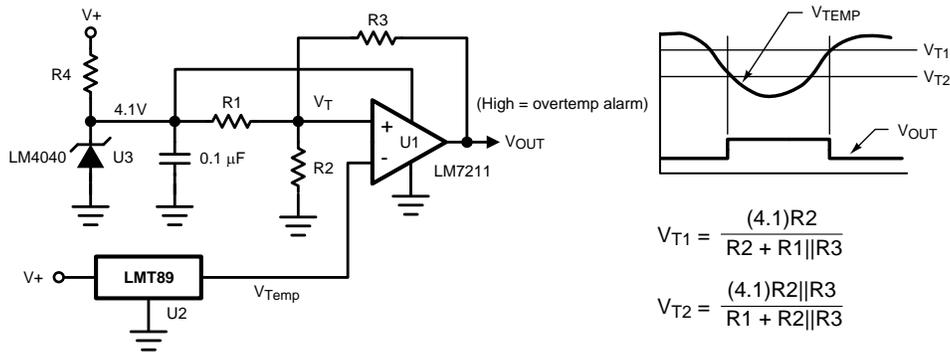


Figure 7. Centigrade Thermostat

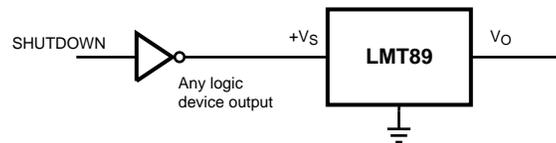


Figure 8. Conserving Power Dissipation with Shutdown

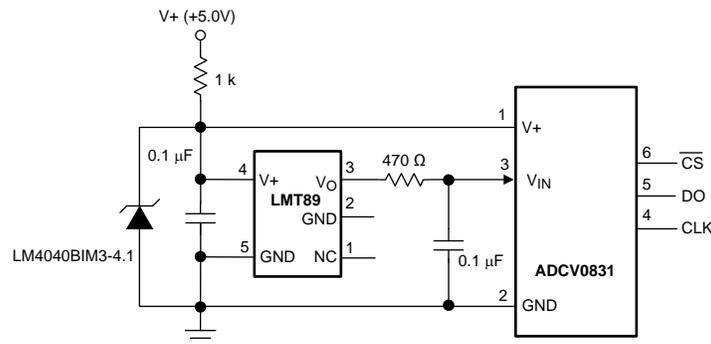


Figure 9. Suggested Connection to a Sampling Analog to Digital Converter Input Stage

Most CMOS ADCs found in ASICs have a sampled data comparator input structure that is notorious for causing grief to analog output devices such as the LMT89 and many op amps. The cause of this grief is the requirement of instantaneous charge of the input sampling capacitor in the ADC. This requirement is easily accommodated by the addition of a capacitor. Since not all ADCs have identical input stages, the charge requirements will vary necessitating a different value of compensating capacitor. This ADC is shown as an example only. If a digital output temperature is required please refer to devices such as the LM74.

**PACKAGING INFORMATION**

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)	Op Temp (°C)	Top-Side Markings (4)	Samples
LMT89DCKR	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-55 to 130	T3B	<a href="#">Samples</a>
LMT89DCKT	ACTIVE	SC70	DCK	5	250	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-55 to 130	T3B	<a href="#">Samples</a>

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

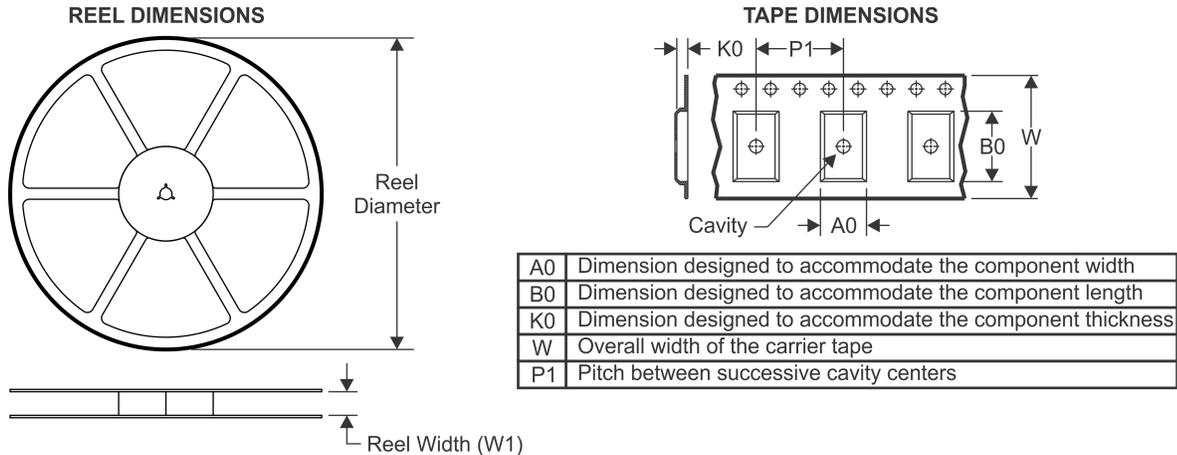
(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) Multiple Top-Side Markings will be inside parentheses. Only one Top-Side Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Top-Side Marking for that device.

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## TAPE AND REEL INFORMATION



### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LMT89DCKR	SC70	DCK	5	3000	178.0	8.4	2.25	2.45	1.2	4.0	8.0	Q3
LMT89DCKT	SC70	DCK	5	250	178.0	8.4	2.25	2.45	1.2	4.0	8.0	Q3

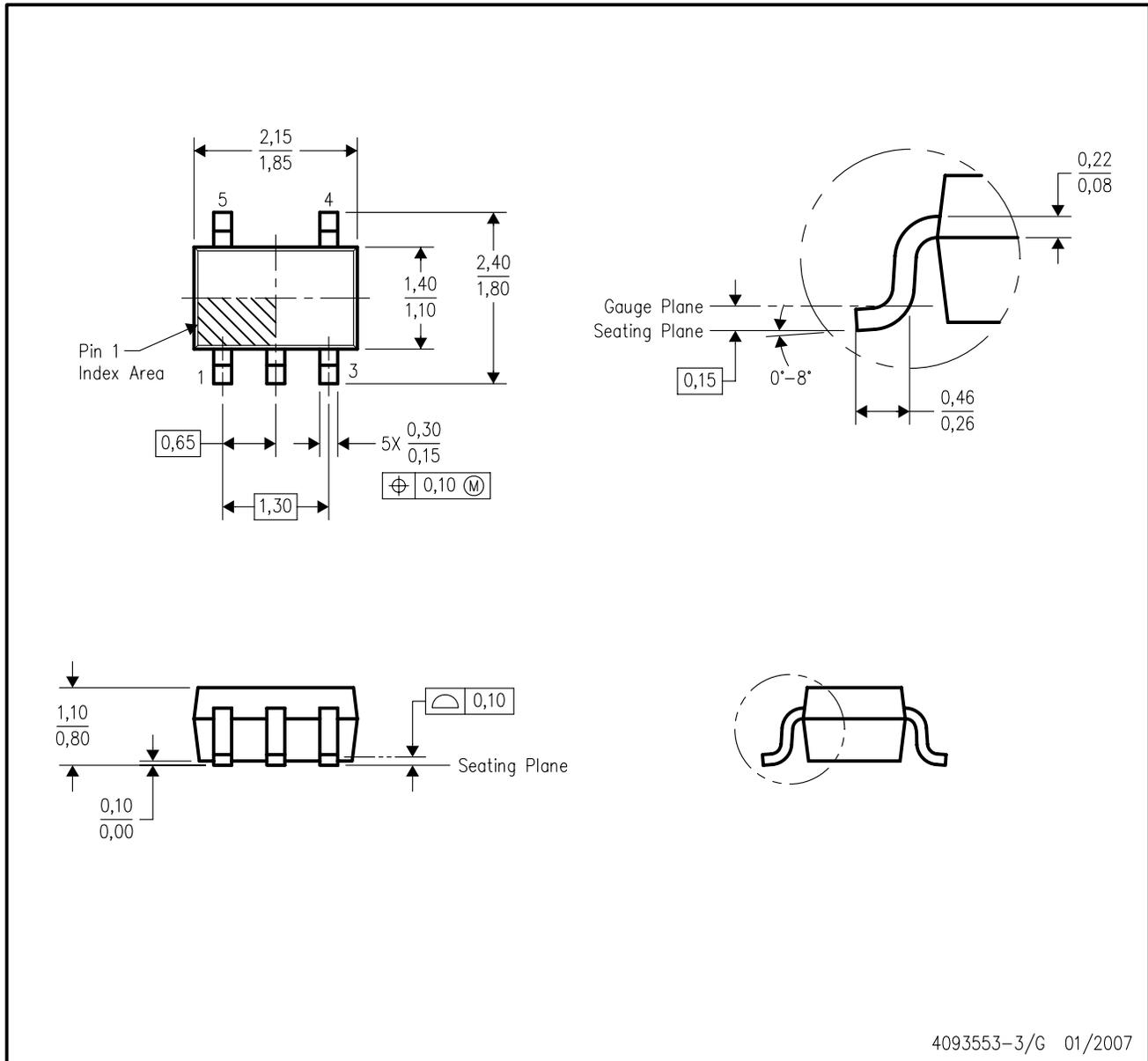
**TAPE AND REEL BOX DIMENSIONS**


\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LMT89DCKR	SC70	DCK	5	3000	210.0	185.0	35.0
LMT89DCKT	SC70	DCK	5	250	210.0	185.0	35.0

DCK (R-PDSO-G5)

PLASTIC SMALL-OUTLINE PACKAGE



- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
  - D. Falls within JEDEC MO-203 variation AA.

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