March 2000

M129/LM329 Precision Reference



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General Description

The LM129 and LM329 family are precision multi-current temperature-compensated 6.9V zener references with dynamic impedances a factor of 10 to 100 less than discrete diodes. Constructed in a single silicon chip, the LM129 uses active circuitry to buffer the internal zener allowing the device to operate over a 0.5 mA to 15 mA range with virtually no change in performance. The LM129 and LM329 are available with selected temperature coefficients of 0.001, 0.002, 0.005 and 0.01%/°C. These references also have excellent long term stability and low noise.

A new subsurface breakdown zener used in the LM129 gives lower noise and better long-term stability than conventional IC zeners. Further the zener and temperature compensating transistor are made by a planar process so they are immune to problems that plague ordinary zeners. For example, there is virtually no voltage shift in zener voltage due to temperature cycling and the device is insensitive to stress on the leads.

The LM129 can be used in place of conventional zeners with improved performance. The low dynamic impedance simplifies biasing and the wide operating current allows the replacement of many zener types.

Connection Diagrams





Pin 2 is electrically connected to case

Bottom View Order Number LM129AH, LM129AH/883, LM129BH, LM129BH/883, LM129CH, LM329AH, LM329BH, LM329CH or LM329DH See NS Package H02A



Features

- 0.6 mA to 15 mA operating current
- 0.6Ω dynamic impedance at any current
- Available with temperature coefficients of 0.001%/°C
- 7µV wideband noise
- 5% initial tolerance
- 0.002% long term stability
- Low cost
- Subsurface zener

Plastic Package (TO-92)



Bottom View Order Number LM329BZ, LM329CZ or LM329DZ See NS Package Z03A

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Typical Applications



Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications. (Note 3)

Reverse Breakdown Current	30 mA
Forward Current	2 mA

Operating Temperature Range	
LM129	–55°C to +125°C
LM329	0°C to +70°C
Storage Temperature Range	–55°C to +150°C
Soldering Information	
TO-92 package: 10 sec.	260°C
TO-46 package: 10 sec.	300°C

Electrical Characteristics (Note 2)

Parameter	Conditions	LM129A, B, C			LM329A, B, C, D			Units
		Min	Тур	Max	Min	Тур	Max	
Reverse Breakdown Voltage	$T_A = 25^{\circ}C,$							
	$0.6 \text{ mA} \le I_R \le 15 \text{ mA}$	6.7	6.9	7.2	6.6	6.9	7.25	V
Reverse Breakdown Change	$T_A = 25^{\circ}C,$							
with Current (Note 4)	$0.6 \text{ mA} \le I_R \le 15 \text{ mA}$		9	14		9	20	mV
Reverse Dynamic Impedance	$T_{A} = 25^{\circ}C, I_{R} = 1 \text{ mA}$		0.6	1		0.8	2	Ω
(Note 4)								
RMS Noise	$T_A = 25^{\circ}C,$							
	$10 \text{ Hz} \le F \le 10 \text{ kHz}$		7	20		7	100	μV
Long Term Stability	$T_{A} = 45^{\circ}C \pm 0.1^{\circ}C,$							
(1000 hours)	$I_{R} = 1 \text{ mA} \pm 0.3\%$		20			20		ppm
Temperature Coefficient	I _R = 1 mA							
LM129A, LM329A			6	10		6	10	ppm/°C
LM129B, LM329B			15	20		15	20	ppm/°C
LM129C, LM329C			30	50		30	50	ppm/°C
LM329D						50	100	ppm/°C
Change In Reverse Breakdown	$1 \text{ mA} \le I_R \le 15 \text{ mA}$		1			1		ppm/°C
Temperature Coefficient								
Reverse Breakdown Change	$1 \text{ mA} \le I_R \le 15 \text{ mA}$		12			12		mV
with Current								
Reverse Dynamic Impedance	$1 \text{ mA} \le I_R \le 15 \text{ mA}$		0.8			1		Ω

Note 1: "Absolute Maximum Ratings" indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits.

Note 2: These specifications apply for $-55^{\circ}C \le T_A \le +125^{\circ}C$ for the LM129 and $0^{\circ}C \le T_A \le +70^{\circ}C$ for the LM329 unless otherwise specified. The maximum junction temperature for an LM129 is 150°C and LM329 is 100°C. For operating at elevated temperature, devices in TO-46 package must be derated based on a thermal resistance of 440°C/W junction to ambient or 80°C/W junction to case. For the TO-92 package, the derating is based on 180°C/W junction to ambient with 0.4" leads from a PC board and 160°C/W junction to ambient with 0.125" lead length to a PC board.

Note 3: Refer to RETS129H for LM129 family military specifications.

Note 4: These changes are tested on a pulsed basis with a low duty-cycle. For changes versus temperature, compute in terms of tempco.

Typical Applications

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External Reference for Temperature Transducer



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Typical Applications (Continued)



Buffered Reference with Single Supply



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Typical Performance Characteristics (Continued)

Low Frequency Noise Voltage









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