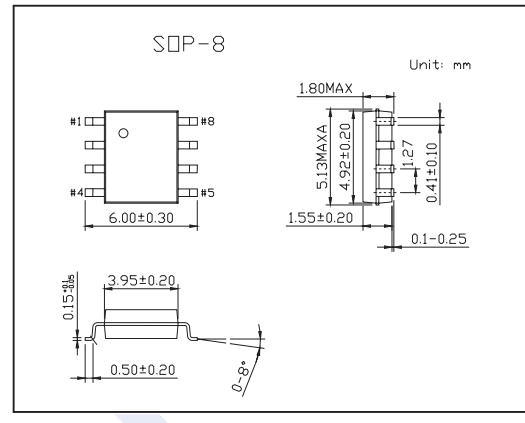
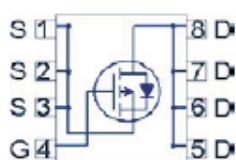


MOS Small Signal Transistor

KSO200P03S(BSO200P03S)

■ Features

- P-Channel
- Enhancement mode
- Logic level
- Avalanche rated
- dv /dt rated
- Ideal for fast switching buck converter



■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	≤10 secs	steady state	Unit
Continuous drain current TA=25°C	ID	-9.1	-7.4	A
TA=70°C		-7.3	-5.9	
Pulsed drain current TA=25 °C	IDP	-37		A
Avalanche energy, single pulse *1	EAS	98		mJ
Reverse diode dv /dt *2	dv /dt	-6		kV/ μ s
Gate source voltage	VGS	±25		V
Power dissipation	PD	2.36	1.56	W
Thermal resistance,junction - soldering point	RthJS	35		K/W
Thermal resistance,junction - ambient	RthJA	110		K/W
Operating and storage temperature	Tj, T stg	-55 to 150		°C

*1 ID=-9.1A, RGS=25 Ω

*2 ID=-9.1A, VDS=20 V,di /dt =200 A/ μ s,Tj,max=150°C

KSO200P03S(BSO200P03S)■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Drain-source breakdown voltage	V_{DSS}	$V_{GS}=0\text{ V}, I_D=-250\text{ }\mu\text{A}$	-30			V
Zero gate voltage drain current	I_{DSS}	$V_{DS}=-30\text{V}, V_{GS}=0\text{V}, T_j=25^\circ\text{C}$		-0.1	-1	μA
		$V_{DS}=-30\text{ V}, V_{GS}=0\text{ V}, T_j=125^\circ\text{C}$		-10	-100	
Gate-source leakage current	I_{GSS}	$V_{GS}=\pm 25\text{ V}, V_{DS}=0\text{ V}$			± 100	nA
Gate threshold voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=-100\text{ }\mu\text{A}$	-1	-1.5		V
Drain-source on-state resistance	$R_{DS(\text{on})}$	$V_{GS}=-10\text{ V}, I_D=-9.1\text{ A}$		16.7	20.0	$\text{m}\Omega$
Forward Transconductance	g_f	$ V_{DS} >2 I_D R_{DS(\text{on})\text{max}}, I_D=-7.3\text{ A}$	11	21		S
Input capacitance	C_{iss}	$V_{GS}=0\text{V}, V_{DS}=-25\text{V}, f=1\text{ MHz}$		1750	2330	pF
Output capacitance	C_{oss}			470	625	
Reverse transfer capacitance	C_{rss}			390	580	
Turn-on delay time	$t_{d(on)}$	$V_{DD}=-15\text{ V}, V_{GS}=-10\text{ V}, I_D=-1\text{ A}, R_G=6\text{ }\Omega$		10	53	ns
Rise time	t_r			11	17	
Turn-off delay time	$t_{d(off)}$			42	63	
Fall time	t_f			33	50	
Gate to source charge	Q_{gs}	$V_{DD}=-24\text{V}, I_D=9.1\text{A}, V_{GS}=0\text{ to }-10\text{ V}$		4.8	6.4	nC
Gate charge at threshold	$Q_{g(\text{th})}$			2.6	3.5	
Gate to drain charge	Q_{gd}			14		
Switching charge	Q_{sw}			16	24	
Gate charge total	Q_g			40	54	
Output charge	Q_{oss}	$V_{DD}=-15\text{V}, V_{GS}=0\text{V}$		14	19	nC
Reverse recovery time	t_{rr}	$V_R=15\text{V}, I_F=-9.1\text{A}, dI_F/dt=100\text{A}/\mu\text{s}$		19	24	ns
Reverse recovery charge	Q_{rr}			9	11	nC
Diode continous forward current	I_S	$T_A=25^\circ\text{C}$			-2.1	A
Diode pulse current	I_{SM}				-36.5	A
Diode forward voltage	V_{SD}	$V_{GS}=0\text{ V}, I_F=-9.1\text{ A}, T_j=25^\circ\text{C}$		-0.88	-1.2	V