

## isc N-Channel MOSFET Transistor

## IPP50R500CE

### • FEATURES

- With low gate drive requirements
- Very high commutation ruggedness
- Extremely high frequency operation
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### • APPLICATIONS

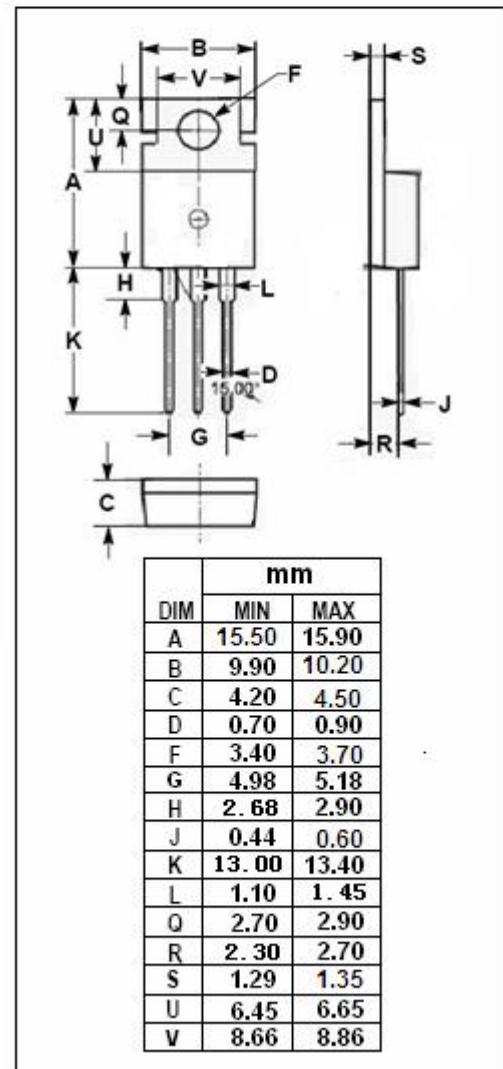
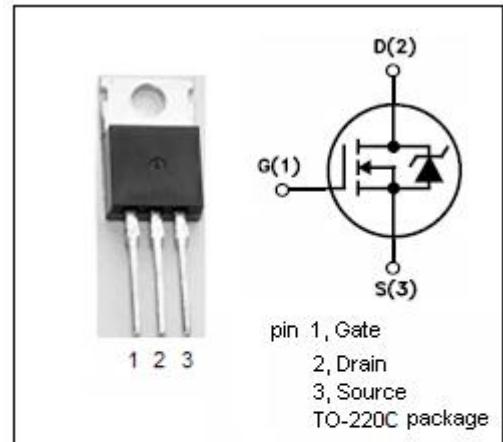
- Switching applications
- LCD&PDP TV
- PC silverbox
- UPS and solar

### • ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	500	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-Continuous@ $T_c=25^\circ\text{C}$ $T_c=100^\circ\text{C}$	11.1 7	A
$I_{DM}$	Drain Current-Single Pulsed	24	A
$P_D$	Total Dissipation	81	W
$T_J$	Operating Junction Temperature	-55~150	°C
$T_{stg}$	Storage Temperature	-55~150	°C

### • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	1.55	°C/W
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	62	°C/W



**isc N-Channel MOSFET Transistor****IPP50R500CE****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}; \text{I}_D= 0.25\text{mA}$	500			V
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\pm 20\text{V}; \text{I}_D=1.4\text{mA}$	2.5		3.5	V
$\text{R}_{\text{DS}(\text{on})}$	Drain-Source On-Resistance	$\text{V}_{\text{GS}}= 10\text{V}; \text{I}_D=2.3\text{A}$		450	500	$\text{m}\Omega$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$\text{V}_{\text{GS}}= \pm 20\text{V}; \text{V}_{\text{DS}}= 0\text{V}$			$\pm 0.1$	$\mu\text{A}$
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$\text{V}_{\text{DS}}= 500\text{V}; \text{V}_{\text{GS}}= 0\text{V}$			1	$\mu\text{A}$
$\text{V}_{\text{SDF}}$	Diode forward voltage	$\text{I}_{\text{SD}}=2.9\text{A}, \text{V}_{\text{GS}} = 0 \text{ V}$			0.85	V