

isc N-Channel MOSFET Transistor

IPP041N04N,IIPP041N04N

• FEATURES

- Static drain-source on-resistance: $R_{DS(on)} \leq 4.1\text{m}\Omega$
- Enhancement mode
- Fast Switching Speed
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• DESCRIPTION

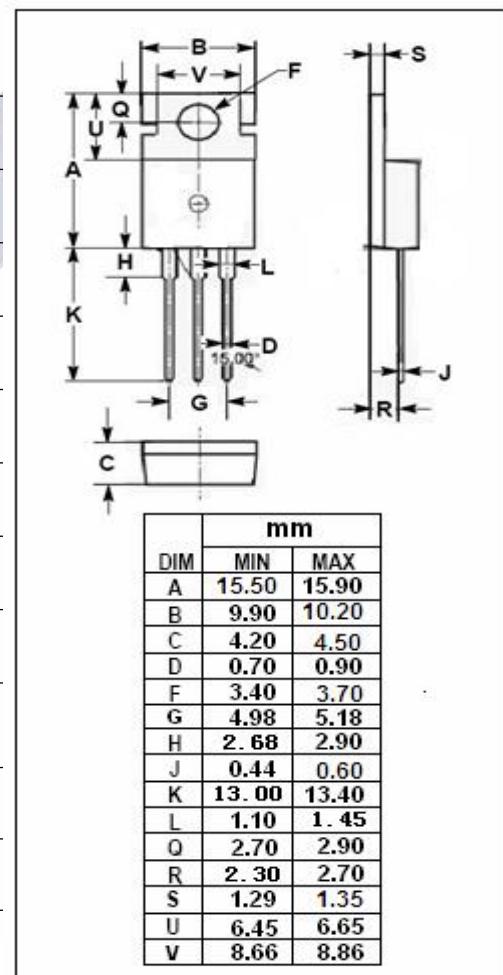
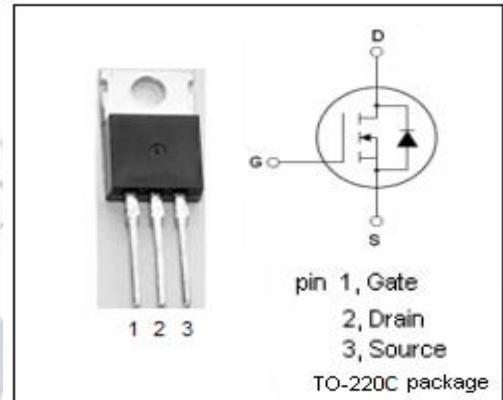
- Fast switching for SMPS
- Optimized technology for DC/DC converters

• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous	80	A
I_{DM}	Drain Current-Single Pulsed	400	A
P_D	Total Dissipation @ $T_c=25^\circ\text{C}$	94	W
T_j	Max. Operating Junction Temperature	175	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55~175	$^\circ\text{C}$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	1.6	$^\circ\text{C}/\text{W}$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	62	$^\circ\text{C}/\text{W}$



isc N-Channel MOSFET Transistor**IPP041N04N,IIPP041N04N****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}; \text{I}_D = 1\text{mA}$	40			V
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}; \text{I}_D=45 \mu\text{A}$	2		4	V
$\text{R}_{\text{DS(on)}}$	Drain-Source On-Resistance	$\text{V}_{\text{GS}}=10\text{V}; \text{I}_D=80\text{A}$			4.1	$\text{m}\Omega$
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=20\text{V}; \text{V}_{\text{DS}}=0\text{V}$			100	nA
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=40\text{V}; \text{V}_{\text{GS}}=0\text{V}$			1	μA
V_{SD}	Diode forward voltage	$\text{I}_F = 80\text{A}; \text{V}_{\text{GS}} = 0 \text{ V}$			1.2	V