

## isc N-Channel MOSFET Transistor

**IPD90R1K2C3,IIPD90R1K2C3**

### • FEATURES

- Static drain-source on-resistance:  $R_{DS(on)} \leq 1.2\Omega$
- Enhancement mode:
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### • DESCRIPTION

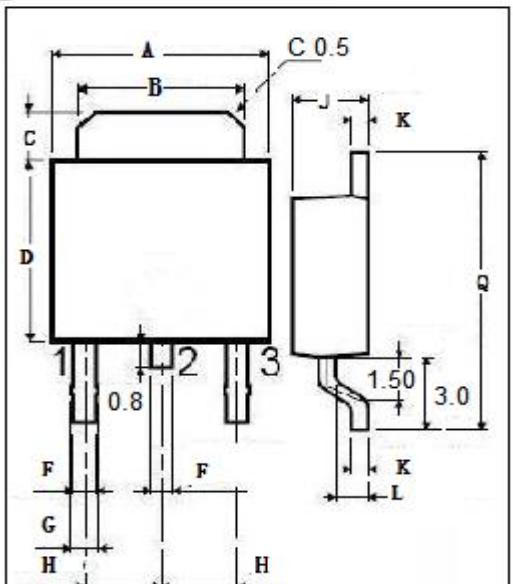
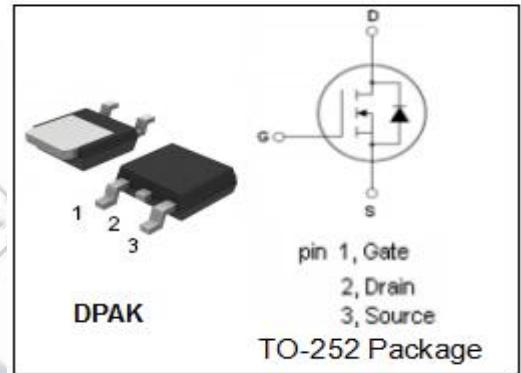
- High peak current capability

### • ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ C$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	900	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-Continuous	5.1	A
$I_{DM}$	Drain Current-Single Pulsed	10	A
$P_D$	Total Dissipation @ $T_c=25^\circ C$	83	W
$T_j$	Max. Operating Junction Temperature	150	°C
$T_{stg}$	Storage Temperature	-55~150	°C

### • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(j-c)}$	Channel-to-case thermal resistance	1.5	°C/W
$R_{th(j-a)}$	Channel-to-ambient thermal resistance	62	°C/W



DIM	mm	
	MIN	MAX
A	6.40	6.60
B	5.20	5.40
C	1.15	1.35
D	5.70	6.10
E	0.65	
G	0.75	
H	2.10	2.50
J	2.10	2.40
K	0.40	0.60
L	0.90	1.10
Q	9.90	10.1

**isc N-Channel MOSFET Transistor****IPD90R1K2C3,IIPD90R1K2C3****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}; \text{I}_D=0.25\text{mA}$	900			V
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}; \text{I}_D=0.31\text{mA}$	2.5		3.5	V
$\text{R}_{\text{DS(on)}}$	Drain-Source On-Resistance	$\text{V}_{\text{GS}}=10\text{V}; \text{I}_D=2.8\text{A}$			1.2	$\Omega$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=20\text{V}; \text{V}_{\text{DS}}=0\text{V}$			0.1	$\mu\text{A}$
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=900\text{V}; \text{V}_{\text{GS}}=0\text{V}$			1	$\mu\text{A}$
$\text{V}_{\text{SD}}$	Diode forward voltage	$\text{I}_F=2.8\text{A}, \text{V}_{\text{GS}}=0\text{V}$			1.2	V