

**isc N-Channel MOSFET Transistor**
**IPD65R660CFD, IIPD65R660CFD**
**• FEATURES**

- Static drain-source on-resistance:  $R_{DS(on)} \leq 0.66\Omega$
- Enhancement mode:
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**• DESCRIPTION**

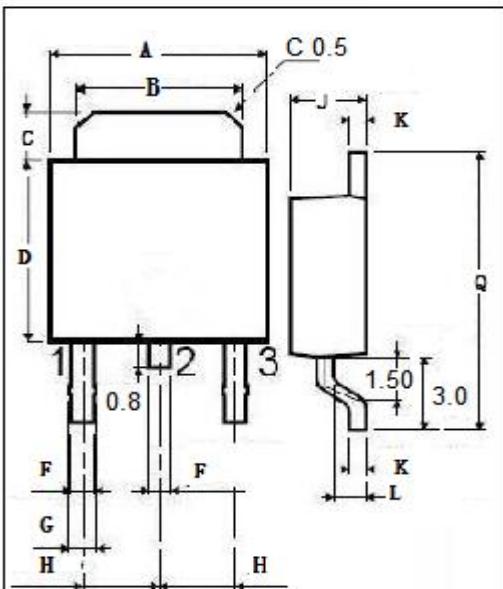
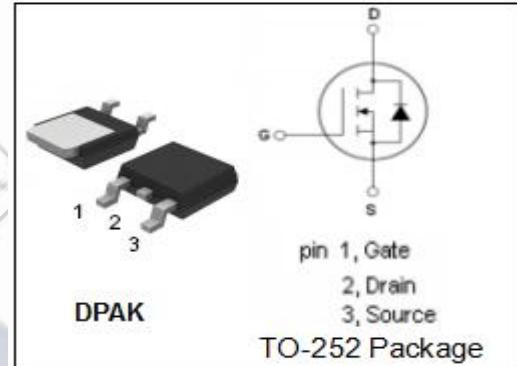
- High commutation ruggedness

**• ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ C$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	650	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-Continuous	6	A
$I_{DM}$	Drain Current-Single Pulsed	17	A
$P_D$	Total Dissipation @ $T_c=25^\circ C$	63	W
$T_j$	Max. Operating Junction Temperature	150	°C
$T_{stg}$	Storage Temperature	-55~150	°C

**• THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(j-c)}$	Channel-to-case thermal resistance	2	°C/W
$R_{th(j-a)}$	Channel-to-ambient thermal resistance	62	°C/W



**isc N-Channel MOSFET Transistor****IPD65R660CFD, IIPD65R660CFD****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}; \text{I}_D=0.25\text{mA}$	650			V
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}; \text{I}_D=210 \mu\text{A}$	3.5		4.5	V
$\text{R}_{\text{DS(on)}}$	Drain-Source On-Resistance	$\text{V}_{\text{GS}}=10\text{V}; \text{I}_D=2.1\text{A}$			0.66	$\Omega$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=20\text{V}; \text{V}_{\text{DS}}=0\text{V}$			0.1	$\mu\text{A}$
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=650\text{V}; \text{V}_{\text{GS}}=0\text{V}$			5	$\mu\text{A}$
$\text{V}_{\text{SD}}$	Diode forward voltage	$\text{I}_F=3.2\text{A}, \text{V}_{\text{GS}}=0\text{V}$	0.9			V