

Isc N-Channel MOSFET Transistor

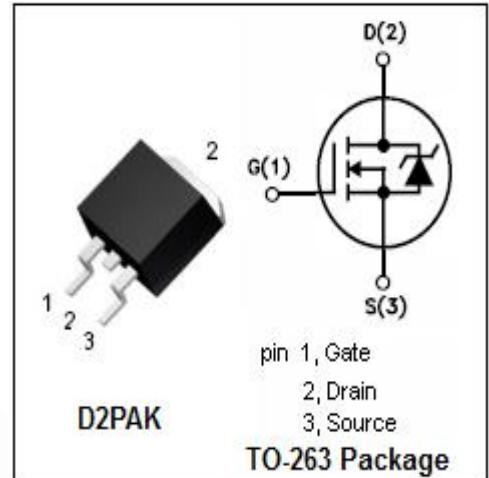
IPB123N10N3G

• FEATURES

- With To-263(D2PAK) package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• APPLICATIONS

- Switching applications

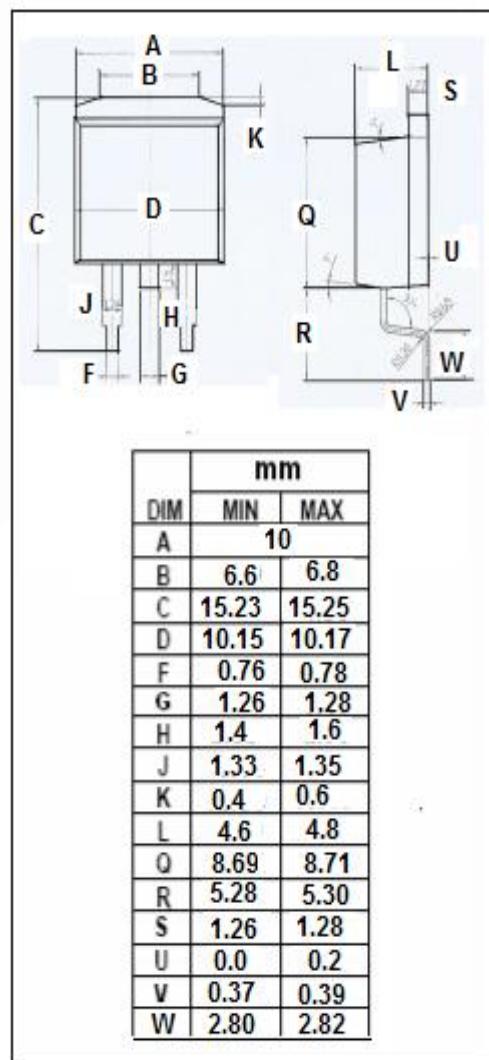


• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	100	V
V_{GSS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous $T_c=25^\circ\text{C}$ $T_c=100^\circ\text{C}$	58 42	A
I_{DM}	Drain Current-Single Pulsed	232	A
P_D	Total Dissipation @ $T_c=25^\circ\text{C}$	94	W
T_{ch}	Max. Operating Junction Temperature	175	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55~175	$^\circ\text{C}$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	1.6	$^\circ\text{C}/\text{W}$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	40	$^\circ\text{C}/\text{W}$



Isc N-Channel MOSFET Transistor**IPB123N10N3G****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}; \text{I}_D= 1\text{mA}$	100			V
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}; \text{I}_D=0.046\text{mA}$	2.0		3.5	V
$\text{R}_{\text{DS(on)}}$	Drain-Source On-Resistance	$\text{V}_{\text{GS}}= 10\text{V}; \text{I}_D=46\text{A}$		11	12.6	$\text{m}\Omega$
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}= \pm 20\text{V}; \text{V}_{\text{DS}}= 0\text{V}$			± 0.1	μA
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=100\text{V}; \text{V}_{\text{GS}}= 0\text{V}; \text{T}_j=25^\circ\text{C}$ $\text{V}_{\text{DS}}=100\text{V}; \text{V}_{\text{GS}}= 0\text{V}; \text{T}_j=125^\circ\text{C}$			1 100	μA
V_{SDF}	Diode forward voltage	$\text{I}_{\text{SD}}=46\text{A}, \text{V}_{\text{GS}} = 0 \text{Vs}$		0.9	1.2	V