

## Isc N-Channel MOSFET Transistor

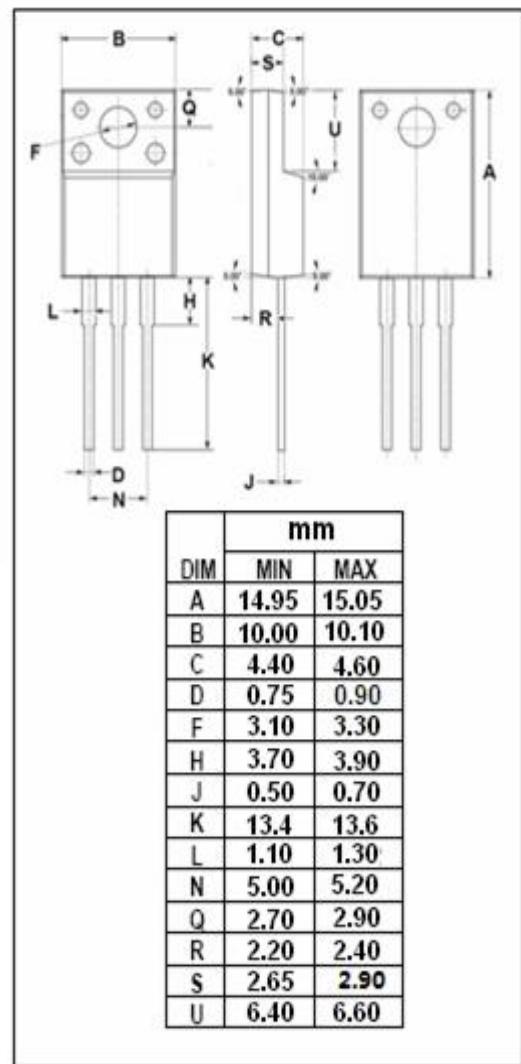
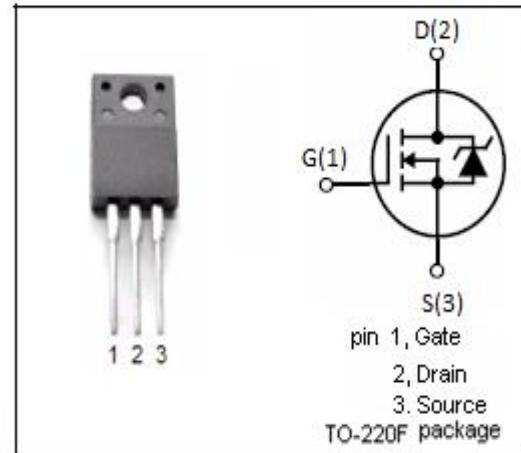
**IPA083N10N5**

### • FEATURES

- With To-220F package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### • APPLICATIONS

- Switching applications



### • ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	100	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-Continuous@ $T_c=25^\circ\text{C}$ $T_c=100^\circ\text{C}$	44 32	A
$I_{DM}$	Drain Current-Single Pulsed	176	A
$P_D$	Total Dissipation @ $T_c=25^\circ\text{C}$	36	W
$T_j$	Max. Operating Junction Temperature	175	$^\circ\text{C}$
$T_{stg}$	Storage Temperature	-55~175	$^\circ\text{C}$

### • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	4.1	$^\circ\text{C}/\text{W}$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	62.5	$^\circ\text{C}/\text{W}$

**Isc N-Channel MOSFET Transistor****IPA083N10N5****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}; I_D=1\text{mA}$	100			V
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=\pm 20\text{V}; I_D=0.05\text{mA}$	2.2		3.8	V
$R_{DS(\text{on})}$	Drain-Source On-Resistance	$V_{GS}= 10\text{V}; I_D=44\text{A}$		7.2	8.3	$\text{m}\Omega$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}= \pm 20\text{V}; V_{DS}= 0\text{V}$			$\pm 0.1$	$\mu\text{A}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}= 100\text{V}; V_{GS}= 0\text{V}; T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$			1 100	$\mu\text{A}$
$V_{SDF}$	Diode forward voltage	$I_{SD}=44\text{A}, V_{GS} = 0 \text{ V}$			1.2	V