



# INA134 INA2134

# AUDIO DIFFERENTIAL LINE RECEIVERS 0dB (G = 1)

### **FEATURES**

- SINGLE AND DUAL VERSIONS
- LOW DISTORTION: 0.0005% at f = 1kHz
- HIGH SLEW RATE: 14V/µs
- FAST SETTLING TIME: 3µs to 0.01%
- WIDE SUPPLY RANGE: ±4V to ±18V
- LOW QUIESCENT CURRENT: 2.9mA max
- HIGH CMRR: 90dB
- FIXED GAIN = 0dB (1V/V)
- PACKAGES—SINGLE: 8-PIN DIP, SO-8 DUAL: 14-PIN DIP, SO-14

## DESCRIPTION

The INA134 and INA2134 are differential line receivers consisting of high performance op amps with onchip precision resistors. They are fully specified for high performance audio applications and have excellent ac specifications, including low distortion (0.0005% at 1kHz) and high slew rate ( $14V/\mu$ s), assuring good dynamic response. In addition, wide output voltage swing and high output drive capability allow use in a wide variety of demanding applications. The dual version features completely independent circuitry for lowest crosstalk and freedom from interaction, even when overdriven or overloaded.

The INA134 and INA2134 on-chip resistors are laser trimmed for accurate gain and optimum common-mode rejection. Furthermore, excellent TCR tracking of the resistors maintains gain accuracy and common-mode rejection over temperature. Operation is guaranteed from  $\pm 4V$  to  $\pm 18V$  (8V to 36V total supply).

The INA134 is available in 8-pin DIP and SO-8 surface-mount packages. The INA2134 comes in 14-pin DIP and SO-14 surface-mount packages. Both are specified for operation over the extended industrial temperature range,  $-40^{\circ}$ C to  $+85^{\circ}$ C.

## APPLICATIONS

- AUDIO DIFFERENTIAL LINE RECEIVER
- SUMMING AMPLIFIER
- UNITY-GAIN INVERTING AMPLIFIER
- PSUEDOGROUND GENERATOR
- INSTRUMENTATION BUILDING BLOCK
- CURRENT SHUNT MONITOR
- VOLTAGE-CONTROLLED CURRENT SOURCE



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## SPECIFICATIONS: $V_s = \pm 18V$

At  $T_A = +25^{\circ}$ C,  $V_S = \pm 18$ V,  $R_L = 2k\Omega$ , and Ref Pin connected to Ground, unless otherwise noted.

PARAMETER	CONDITIONS	INA134PA, UA INA2134PA, UA			
		MIN	ТҮР	MAX	UNITS
AUDIO PERFORMANCE Total Harmonic Distortion + Noise, f = 1kHz Noise Floor <sup>(1)</sup> Headroom <sup>(1)</sup>	V <sub>IN</sub> = 10Vrms 20kHz BW THD+N < 1%		0.0005 -100 +23		% dBu dBu
FREQUENCY RESPONSE Small-Signal Bandwidth Slew Rate Settling Time: 0.1% 0.01% Overload Recovery Time Channel Separation (dual), f = 1kHz	10V Step, C <sub>L</sub> = 100pF 10V Step, C <sub>L</sub> = 100pF 50% Overdrive		3.1 14 2 3 3 117		MHz V/μs μs μs dB
OUTPUT NOISE VOLTAGE <sup>(2)</sup> f = 20Hz to 20kHz f = 1kHz			7 52		μVr <u>ms</u> nV/√HZ
OFFSET VOLTAGE <sup>(3)</sup> Input Offset Voltage vs Temperature vs Power Supply	$V_{CM} = 0V$ Specified Temperature Range $V_{S} = \pm 4V$ to $\pm 18V$		±100 ±2 ±5	±1000 ±60	μV μV/°C μV/V
INPUT Common-Mode Voltage Range: Positive Negative Differential Voltage Range Common-Mode Rejection Impedance <sup>(4)</sup>	$V_{O} = 0V$ $V_{O} = 0V$ $V_{CM} = \pm 31V, R_{S} = 0\Omega$	2(V+)–5 2(V–)+5 74	2(V+)–4 2(V–)+2 See Typical Curve 90		V V dB
Differential Common-Mode			50 50		kΩ kΩ
GAIN Initial Error vs Temperature Nonlinearity	$V_0 = -16V$ to 16V $V_0 = -16V$ to 16V		1 ±0.02 ±1 0.0001	±0.1 ±10	V/V % ppm/°C %
OUTPUT Voltage Output, Positive Negative Current Limit, Continuous to Common Capacitive Load (Stable Operation)		(V+)–2 (V–)+2	(V+)-1.8 (V-)+1.6 ±60 500		V V mA pF
POWER SUPPLY Rated Voltage Voltage Range Quiescent Current (per Amplifier)	I <sub>O</sub> = 0	±4	±18 ±2.4	±18 ±2.9	V V mA
<b>TEMPERATURE RANGE</b> Specification Range   Operation Range   Storage Range   Thermal Resistance, $\theta_{JA}$		-40 -55 -55		85 125 125	ວ° ວ° ວ
8-Pin DIP SO-8 Surface-Mount 14-Pin DIP SO-14 Surface-Mount			100 150 80 100		°C/W °C/W °C/W °C/W

NOTES: (1) dBu = 20log (Vrms/0.7746). (2) Includes effects of amplifier's input current noise and thermal noise contribution of resistor network. (3) Includes effects of amplifier's input bias and offset currents. (4)  $25k\Omega$  resistors are ratio matched but have  $\pm 25\%$  absolute value.

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#### **PIN CONFIGURATIONS**



### ABSOLUTE MAXIMUM RATINGS<sup>(1)</sup>

Supply Voltage, V+ to V	40V
Input Voltage Range	
Output Short-Circuit (to ground) <sup>(2)</sup>	Continuous
Operating Temperature	55°C to +125°C
Storage Temperature	55°C to +125°C
Junction Temperature	+150°C
Lead Temperature (soldering, 10s)	+300°C

NOTE: (1) Stresses above these ratings may cause permanent damage. (2) One channel per package.

### PACKAGE/ORDERING INFORMATION

PRODUCT	PACKAGE	PACKAGE DRAWING NUMBER <sup>(1)</sup>	SPECIFICATION TEMPERATURE RANGE
Single INA134PA INA134UA	8-Pin DIP SO-8 Surface-Mount	006 182	–40°C to +85°C –40°C to +85°C
<b>Dual</b> INA2134PA INA2134UA	14-Pin DIP SO-14 Surface-Mount	010 235	-40°C to +85°C -40°C to +85°C

NOTE: (1) For detailed drawing and dimension table, please see end of data sheet, or Appendix C of Burr-Brown IC Data Book.



### ELECTROSTATIC DISCHARGE SENSITIVITY

This integrated circuit can be damaged by ESD. Burr-Brown recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.



## **TYPICAL PERFORMANCE CURVES**

At  $T_{A}$  = +25°C,  $V_{S}$  =  $\pm 18V,$  unless otherwise noted.





HEADROOM - TOTAL HARMONIC DISTORTION+NOISE vs OUTPUT AMPLITUDE







OUTPUT NOISE VOLTAGE vs NOISE BANDWIDTH





## **TYPICAL PERFORMANCE CURVES (CONT)**

At  $T_A = +25^{\circ}C$ ,  $V_S = \pm 18V$ , unless otherwise noted.











INPUT COMMON-MODE VOLTAGE RANGE vs OUTPUT VOLTAGE





## **TYPICAL PERFORMANCE CURVES (CONT)**

At  $T_{A}$  = +25°C,  $V_{S}$  =  $\pm 18V,$  unless otherwise noted.





SHORT-CIRCUIT CURRENT vs TEMPERATURE 80 60 Short-Circuit Current (mA) 40 +I<sub>SC</sub> 20 0 -20 -I<sub>sc</sub> -40 -60 -80 -75 -50 -25 0 25 50 75 100 125 Temperature (°C)









## **TYPICAL PERFORMANCE CURVES (CONT)**

At T<sub>A</sub> = +25°C, V<sub>S</sub> =  $\pm$ 18V, unless otherwise noted.





SMALL-SIGNAL STEP RESPONSE



1µs/div



LARGE-SIGNAL STEP RESPONSE

1µs/div

## **APPLICATIONS INFORMATION**

Figure 1 shows the basic connections required for operation of the INA134. Decoupling capacitors are strongly recommended in applications with noisy or high impedance power supplies. The capacitors should be placed close to the device pins as shown in Figure 1. All circuitry is completely independent in the dual version assuring lowest crosstalk and normal behavior when one amplifier is overdriven or short-circuited.

As shown in Figure 1, the differential input signal is connected to pins 2 and 3. The source impedances connected to the inputs must be nearly equal to assure good common-mode rejection. A  $10\Omega$  mismatch in source impedance will degrade the common-mode rejection of a typical device to approximately 74dB. If the source has a known impedance mismatch, an additional resistor in series with the opposite input can be used to preserve good common-mode rejection.

Do not interchange pins 1 and 3 or pins 2 and 5, even though nominal resistor values are equal. These resistors are laser trimmed for precise resistor ratios to achieve accurate gain and highest CMR. Interchanging these pins would not provide specified performance.

### AUDIO PERFORMANCE

The INA134 and INA2134 were designed for enhanced ac performance. Very low distortion, low noise, and wide bandwidth provide superior performance in high quality audio applications. Laser-trimmed matched resistors provide optimum common-mode rejection (typically 90dB), especially when compared to circuits implemented with an op amp and discrete precision resistors. In addition, high slew rate  $(14V/\mu s)$  and fast settling time (3µs to 0.01%) ensure good dynamic performance.

The INA134 and INA2134 have excellent distortion characteristics. THD+Noise is below 0.002% throughout the audio frequency range. Up to approximately 10kHz distortion is below the measurement limit of commonly used test equipment. Furthermore, distortion remains relatively flat over its wide output voltage swing range (approximately 1.7V from either supply).

### OFFSET VOLTAGE TRIM

The INA134 and INA2134 are laser trimmed for low offset voltage and drift. Most applications require no external offset adjustment. Figure 2 shows an optional circuit for trimming the output offset voltage. The output is referred to the output reference terminal (pin 1), which is normally grounded. A voltage applied to the Ref terminal will be summed with the output signal. This can be used to null offset voltage as shown in Figure 2. The source impedance of a signal applied to the Ref terminal should be less than  $10\Omega$  to maintain good common-mode rejection.

INA134/2134



FIGURE 1. Precision Difference Amplifier (Basic Power Supply and Signal Connections).



FIGURE 2. Offset Adjustment.



FIGURE 3. Precision Summing Amplifier.





FIGURE 5. High Input Impedance Instrumentation Amplifier.

FIGURE 4. Boosting Output Current.

The difference amplifier is a highly versatile building block that is useful in a wide variety of applications. See the INA105 data sheet for additional applications ideas, including:

- Current Receiver with Compliance to Rails
- Precision Unity-Gain Inverting Amplifier
- ±10V Precision Voltage Reference
- ±5V Precision Voltage Reference
- Precision Unity-Gain Buffer
- Precision Average Value Amplifier
- Precision G = 2 Amplifier
- Precision Summing Amplifier
- Precision G = 1/2 Amplifier
- Precision Bipolar Offsetting
- Precision Summing Amplifier with Gain
- Instrumentation Amplifier Guard Drive Generator

- Precision Summing Instrumentation Amplifier
- Precision Absolute Value Buffer
- Precision Voltage-to-Current Converter with Differential Inputs
- Differential Input Voltage-to-Current Converter for Low  $I_{\rm OUT}$
- Isolating Current Source
- Differential Output Difference Amplifier
- Isolating Current Source with Buffering Amplifier for Greater Accuracy
- Window Comparator with Window Span and Window Center Inputs
- Precision Voltage-Controlled Current Source with Buffered Differential Inputs and Gain
- Digitally Controlled Gain of ±1 Amplifier

