

GaAs HEMT MMIC LOW NOISE AMPLIFIER, 24 - 40 GHz

Typical Applications

This HMC-ALH140 is ideal for:

- Point-to-Point Radios
- Point-to-Multi-Point Radios
- VSAT
- SATCOM

Features

Noise Figure: 4 dB

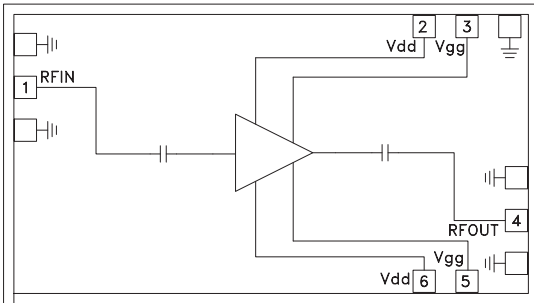
Gain: 11.5 dB

P1dB Output Power: +15 dBm

Supply Voltage: +4V @ 60 mA

Die Size: 2.5 x 1.4 x 0.1 mm

Functional Diagram



General Description

The HMC-ALH140 is a two Stage GaAs MMIC HEMT Low Noise Amplifier die which operates between 24 and 40 GHz. The amplifier provides 11.5 dB of gain, from a bias supply of +4V @ 60 mA with a noise figure of 4 dB. The HMC-ALH140 amplifier die is ideal for integration into Multi-Chip-Modules (MCMs) due to its small size (3.5 mm²).

Electrical Specifications, $T_A = +25^\circ \text{C}$, $V_{dd} = 4\text{V}$ [1], $I_{dd} = 60\text{mA}$ [2]

Parameter	Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	Units
Frequency Range	24 - 30			24 - 40			35 - 40			GHz
Gain	10	12		10	11.5		10	11.5		dB
Noise Figure		4			4			4		
Input Return Loss		13			13			20		dB
Output Return Loss		15			15			20		dB
Output Power for 1 dB Compression		15			15			15		dBm
Supply Current (I _{dd})		60	100		60	100		60	100	mA

[1] Unless otherwise indicated, all measurements are from probed die

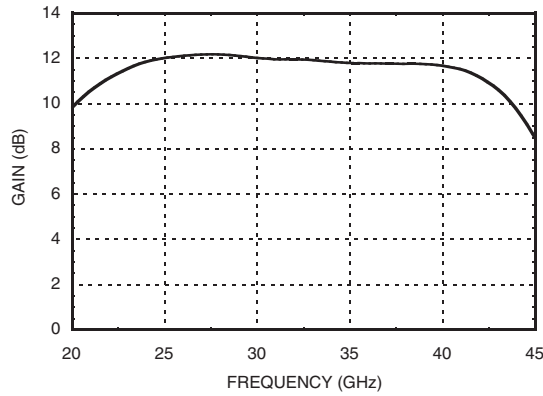
[2] Adjust V_{gg} between -1V to +0.3V (Typ. -0.2V) to achieve I_{dd,total} = 60 mA

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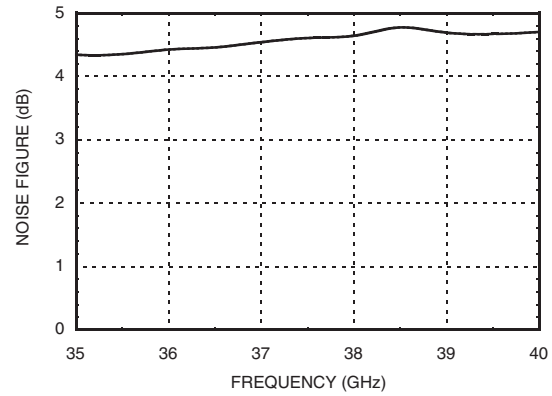
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LOW NOISE AMPLIFIERS - CHIP

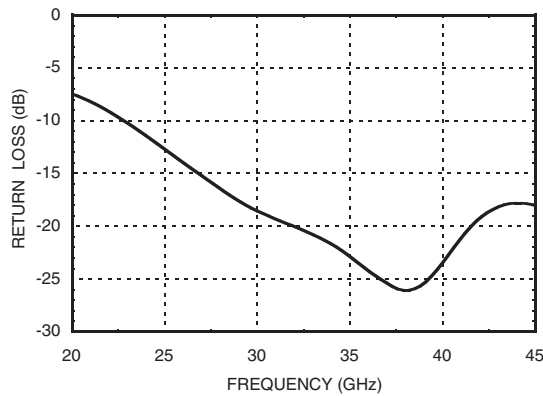
Linear Gain vs. Frequency



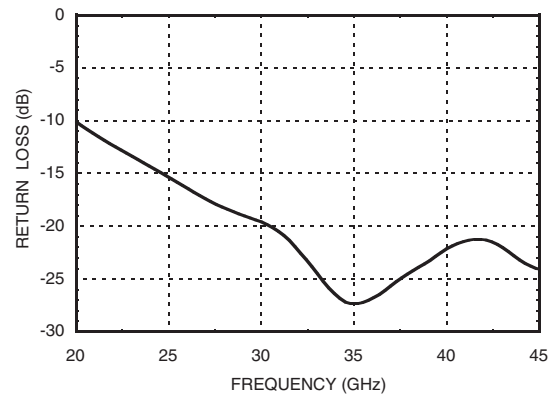
Noise Figure vs. Frequency



Input Return Loss vs. Frequency



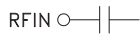
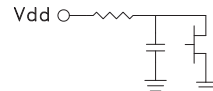
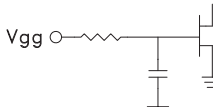
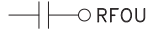

Output Return Loss vs. Frequency



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LOW NOISE AMPLIFIERS - CHIP
Pad Descriptions

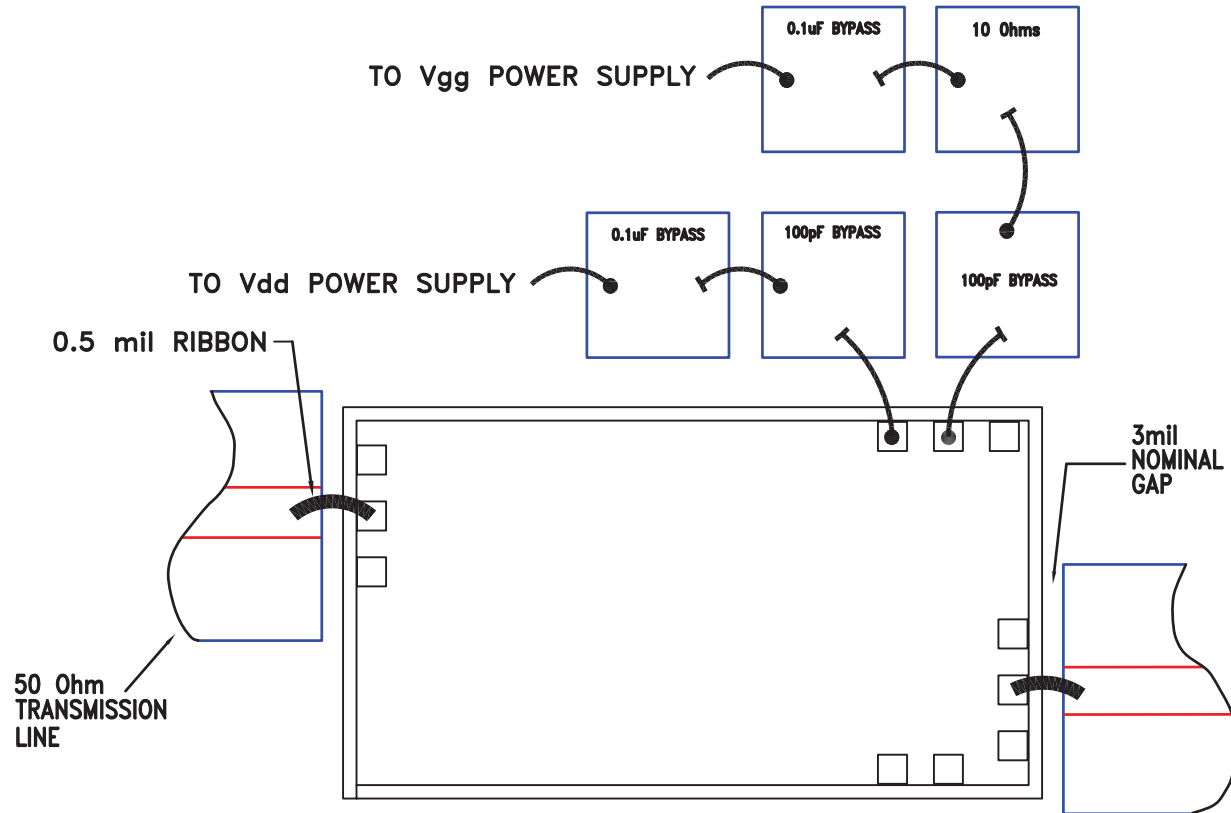
Pad Number	Function	Description	Interface Schematic
1	RFIN	This pad is AC coupled and matched to 50 Ohms.	
2, 6	Vdd	Power Supply Voltage for the amplifier. See assembly for required external components.	
3, 5	Vgg	Gate control for amplifier. Please follow "MMIC Amplifier Biasing Procedure" application note. See assembly for required external components.	
4	RFOUT	This pad is AC coupled and matched to 50 Ohms.	
Die bottom	GND	Die bottom must be connected to RF/DC ground.	

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LOW NOISE AMPLIFIERS - CHIP

Assembly Diagram



Note 1: Bypass caps should be 100 pF (approximately) ceramic (single-layer) placed no farther than 30 mils from the amplifier

Note 2: Best performance obtained from use of <10 mil (long) by 3 by 0.5 mil ribbons on input and output. 10 Ohms

Note 3: Gate bond pads (VG) exist on the upper & lower sides of the MMIC for assembly convenience. For best performance the unused pad should be attached to a 100pF cap to ground, but is not required.

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LOW NOISE AMPLIFIERS - CHIP

Mounting & Bonding Techniques for Millimeterwave GaAs MMICs

The die should be attached directly to the ground plane eutectically or with conductive epoxy (see HMC general Handling, Mounting, Bonding Note).

50 Ohm Microstrip transmission lines on 0.127mm (5 mil) thick alumina thin film substrates are recommended for bringing RF to and from the chip (Figure 1). If 0.254mm (10 mil) thick alumina thin film substrates must be used, the die should be raised 0.150mm (6 mils) so that the surface of the die is coplanar with the surface of the substrate. One way to accomplish this is to attach the 0.102mm (4 mil) thick die to a 0.150mm (6 mil) thick molybdenum heat spreader (moly-tab) which is then attached to the ground plane (Figure 2).

Microstrip substrates should be placed as close to the die as possible in order to minimize bond wire length. Typical die-to-substrate spacing is 0.076mm to 0.152 mm (3 to 6 mils).

Handling Precautions

Follow these precautions to avoid permanent damage.

Storage: All bare die are placed in either Waffle or Gel based ESD protective containers, and then sealed in an ESD protective bag for shipment. Once the sealed ESD protective bag has been opened, all die should be stored in a dry nitrogen environment.

Cleanliness: Handle the chips in a clean environment. DO NOT attempt to clean the chip using liquid cleaning systems.

Static Sensitivity: Follow ESD precautions to protect against ESD strikes.

Transients: Suppress instrument and bias supply transients while bias is applied. Use shielded signal and bias cables to minimize inductive pick-up.

General Handling: Handle the chip along the edges with a vacuum collet or with a sharp pair of bent tweezers. The surface of the chip has fragile air bridges and should not be touched with vacuum collet, tweezers, or fingers.

Mounting

The chip is back-metallized and can be die mounted with AuSn eutectic preforms or with electrically conductive epoxy. The mounting surface should be clean and flat.

Eutectic Die Attach: A 80/20 gold tin preform is recommended with a work surface temperature of 255 °C and a tool temperature of 265 °C. When hot 90/10 nitrogen/hydrogen gas is applied, tool tip temperature should be 290 °C. DO NOT expose the chip to a temperature greater than 320 °C for more than 20 seconds. No more than 3 seconds of scrubbing should be required for attachment.

Epoxy Die Attach: Apply a minimum amount of epoxy to the mounting surface so that a thin epoxy fillet is observed around the perimeter of the chip once it is placed into position. Cure epoxy per the manufacturer's schedule.

Wire Bonding

RF bonds made with 0.003" x 0.0005" ribbon are recommended. These bonds should be thermosonically bonded with a force of 40-60 grams. DC bonds of 0.001" (0.025 mm) diameter, thermosonically bonded, are recommended. Ball bonds should be made with a force of 40-50 grams and wedge bonds at 18-22 grams. All bonds should be made with a nominal stage temperature of 150 °C. A minimum amount of ultrasonic energy should be applied to achieve reliable bonds. All bonds should be as short as possible, less than 12 mils (0.31 mm).

